

Silicon Epitaxial Planar Schottky Barrier Diode

Features

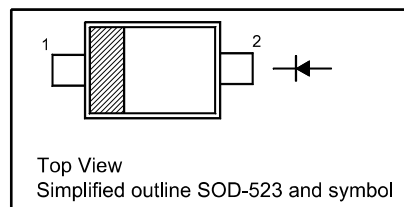
- AEC-Q101 Qualified
- Extremely small surface mounting type
- Halogen and Antimony Free(HAF), RoHS compliant
- Device Marking: .C

Applications

- for low current rectification
- high speed switching applications

PINNING

PIN	DESCRIPTION
1	Cathode
2	Anode



Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Value	Unit
Reverse Voltage	V_R	30	V
Average Rectified Forward Current	$I_{F(AV)}$	200	mA
Peak Forward Surge Current (60 Hz for Cyc.)	I_{FSM}	1	A
Junction Temperature	T_j	125	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	- 40 to + 125	$^\circ\text{C}$

Characteristics at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Min.	Max.	Unit
Reverse Breakdown Voltage at $I_R = 150 \mu\text{A}$	$V_{(BR)R}$	30	-	V
Forward Voltage at $I_F = 200 \text{ mA}$	V_F	-	0.5	V
Reverse Current at $V_R = 10 \text{ V}$	I_R	-	30	μA

Electrical Characteristics Curves

Fig 1. Reverse Characteristic Curve

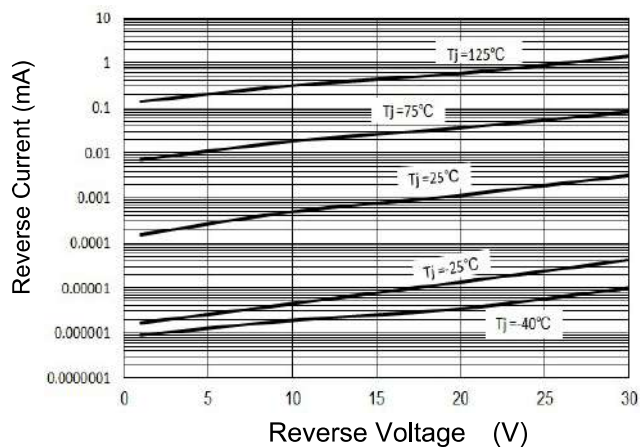


Fig 2. Forward Characteristic Curve

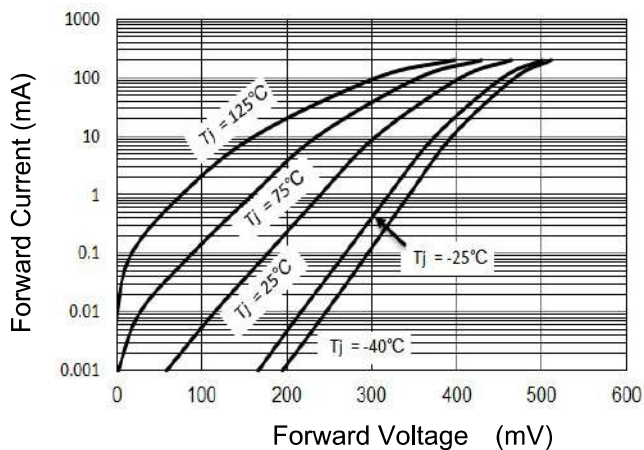


Fig 3. Junction Capacitance

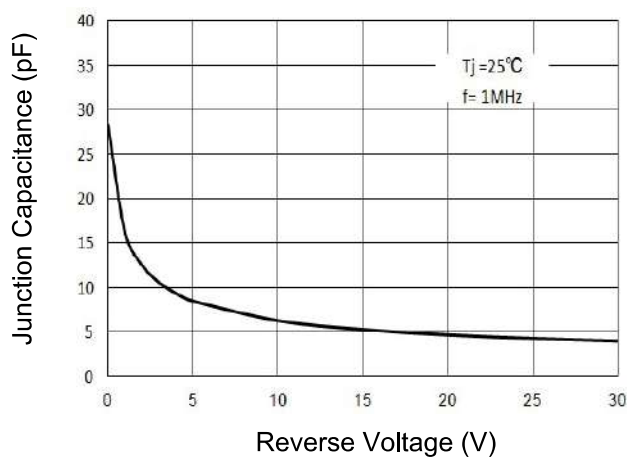
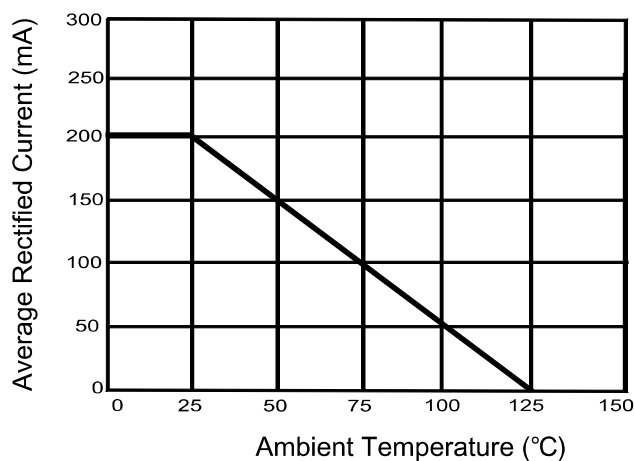


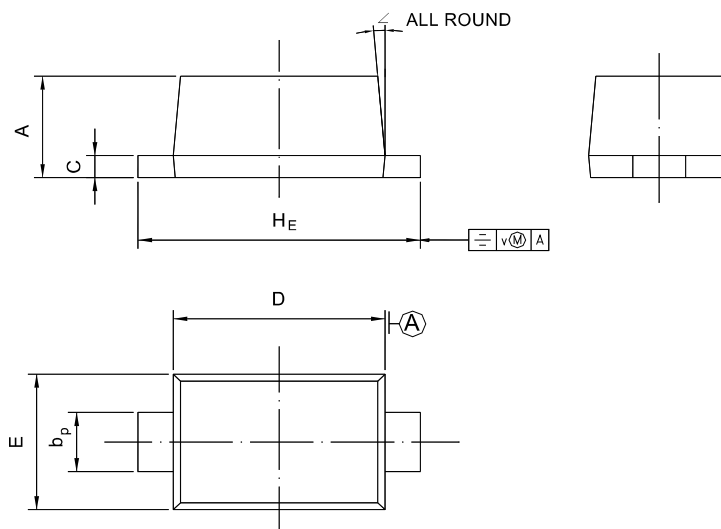
Fig 4. Average Rectified Current Derating Curve



PACKAGE OUTLINE

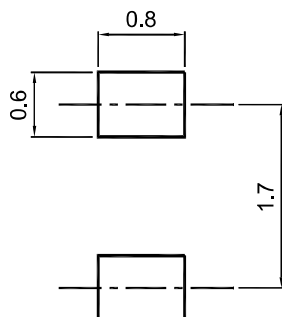
Plastic surface mounted package; 2 leads

SOD-523



UNIT	A	b _p	C	D	E	H _E	V	∠
mm	0.70 0.60	0.4 0.3	0.135 0.100	1.25 1.15	0.85 0.75	1.7 1.5	0.1	5°

Recommended Soldering Footprint



Packing information

Package	Tape Width (mm)	Pitch		Reel Size		Per Reel Packing Quantity
		mm	inch	mm	inch	
SOD-523	8	4 ± 0.1	0.157 ± 0.004	178	7	4,000