

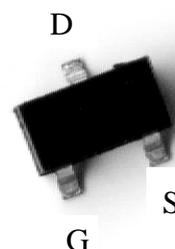
## 60V N-CHANNEL Enhancement Mode MOSFET

### Features:

- Simple drive requirement
- Small package outline
- Pb-free lead plating and halogen-free package

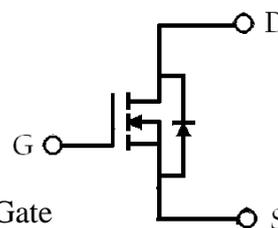
### Outline

SOT-23



### Symbol

KWN2310



G : Gate  
 S : Source  
 D : Drain

$BV_{DSS}$	60V
$I_D$	4A
$R_{DS(on)}@V_{GS}=10V, I_D=4A$	41m $\Omega$ (typ)
$R_{DS(on)}@V_{GS}=5V, I_D=3A$	46m $\Omega$ (typ)

### Ordering Information

Device	Package	Shipping
KWN2310	SOT-23 (Pb-free lead plating and halogen-free package)	3000 pcs / Tape & Reel

### Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V <sub>DS</sub>	60	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Continuous Drain Current @ T <sub>A</sub> =25°C (Note 3)	I <sub>D</sub>	4	A
Continuous Drain Current @ T <sub>A</sub> =70°C (Note 3)		3.2	A
Pulsed Drain Current (Notes 1, 2)	I <sub>DM</sub>	16	A
Maximum Power Dissipation @ T <sub>A</sub> =25°C (Note 3)	P <sub>D</sub>	1.38	W
Linear Derating Factor		0.01	W/°C
Operating Junction and Storage Temperature Range	T <sub>j</sub> ; T <sub>stg</sub>	-55~+150	°C

Note : 1. Pulse width limited by maximum junction temperature.

2. Pulse width ≤ 300μs, duty cycle ≤ 2%.

3. Surface mounted on 1 in<sup>2</sup> copper pad of FR-4 board; 270°C/W when mounted on minimum copper pad

### Thermal Performance

Parameter	Symbol	Limit	Unit
Thermal Resistance, Junction-to-Ambient, max	R <sub>θJA</sub>	90	°C/W
Thermal Resistance, Junction-to-Case, max	R <sub>θJC</sub>	70	°C/W

Note : Surface mounted on 1 in<sup>2</sup> copper pad of FR-4 board; 270°C/W when mounted on minimum copper pad

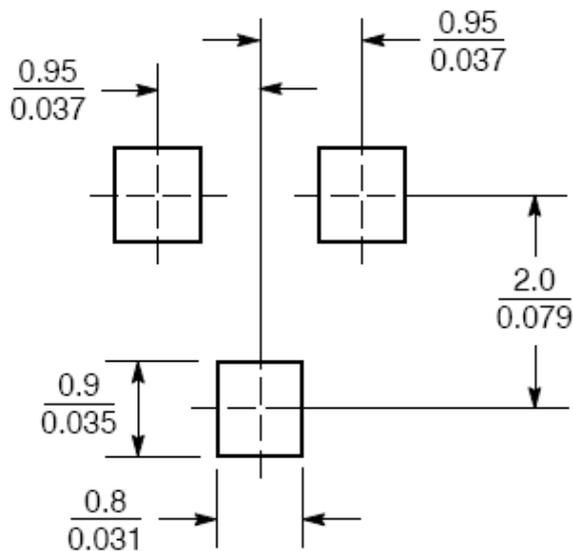
### Electrical Characteristics (T<sub>j</sub>=25°C, unless otherwise noted)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
BV <sub>DSS</sub>	60	-	-	V	V <sub>GS</sub> =0, I <sub>D</sub> =250μA
V <sub>GS(th)</sub>	1.0	1.5	2.0	V	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA
I <sub>GSS</sub>	-	-	±100	nA	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0
I <sub>DSS</sub>	-	-	1	μA	V <sub>DS</sub> =48V, V <sub>GS</sub> =0
	-	-	25		V <sub>DS</sub> =40V, V <sub>GS</sub> =0 (T <sub>j</sub> =70°C)
*R <sub>DS(ON)</sub>	-	41	55	mΩ	I <sub>D</sub> =4A, V <sub>GS</sub> =10V
	-	46	65		I <sub>D</sub> =3A, V <sub>GS</sub> =5V
*G <sub>FS</sub>	-	9	-	S	V <sub>DS</sub> =5V, I <sub>D</sub> =4A
<b>Dynamic</b>					
C <sub>iss</sub>	-	1128	-	pF	V <sub>DS</sub> =30V, V <sub>GS</sub> =0, f=1MHz
C <sub>oss</sub>	-	42	-		
C <sub>rss</sub>	-	32	-		
t <sub>d(ON)</sub>	-	12	-	ns	V <sub>DS</sub> =30V, I <sub>D</sub> =1A, V <sub>GS</sub> =10V, R <sub>G</sub> =6Ω
t <sub>r</sub>	-	18	-		
t <sub>d(OFF)</sub>	-	32	-		
t <sub>f</sub>	-	6	-		

Qg	-	12	-	nC	V <sub>DS</sub> =30V, I <sub>D</sub> =4A, V <sub>GS</sub> =10V
Qgs	-	3.7	-		
Qgd	-	3.9	-		
<b>Source-Drain Diode</b>					
*I <sub>S</sub>	-	-	4	A	
*I <sub>SM</sub>	-	-	16		
*V <sub>SD</sub>	-	-	1.3	V	V <sub>GS</sub> =0V, I <sub>F</sub> =I <sub>S</sub>
T <sub>rr</sub>	-	15	-	ns	V <sub>GS</sub> =0V, I <sub>F</sub> =4A, dI/dt=100A/μs
Q <sub>rr</sub>	-	8	-	nC	

\*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

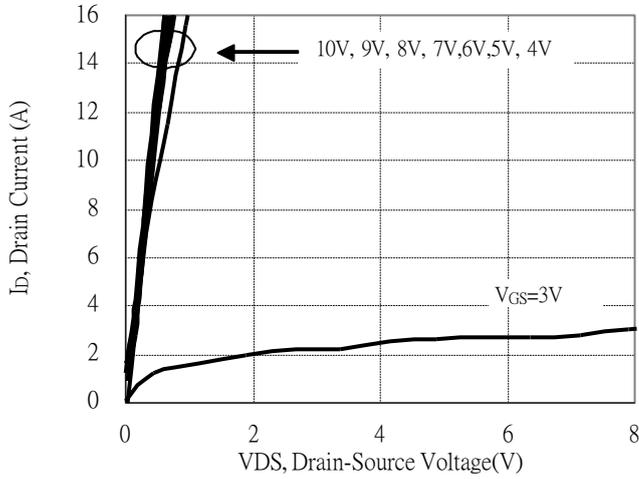
### Recommended Soldering Footprint



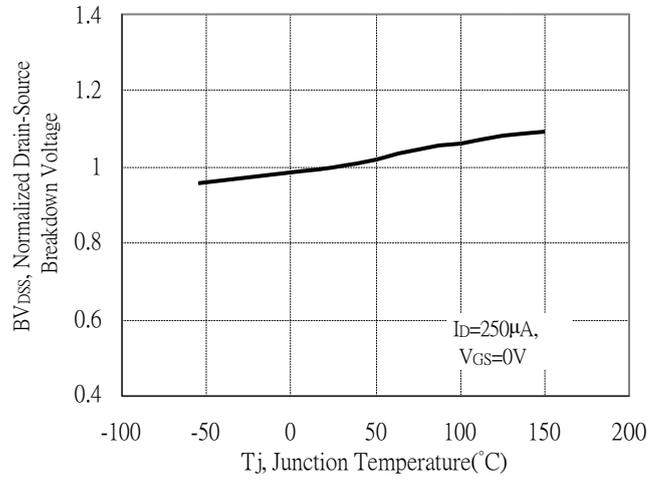
Unit :  $\frac{\text{mm}}{\text{inches}}$

## Typical Characteristics

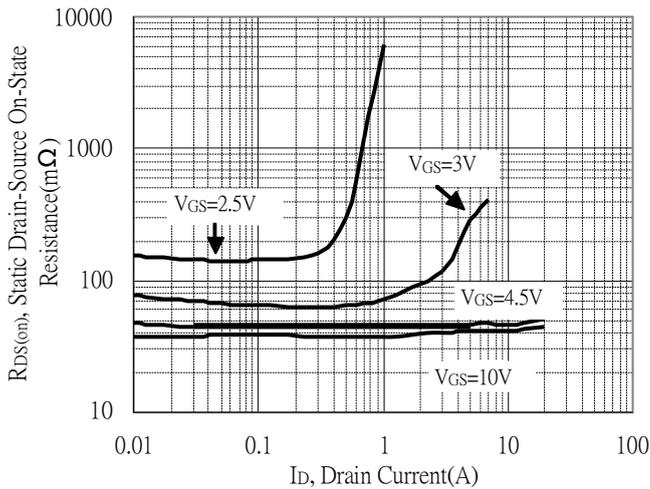
Typical Output Characteristics



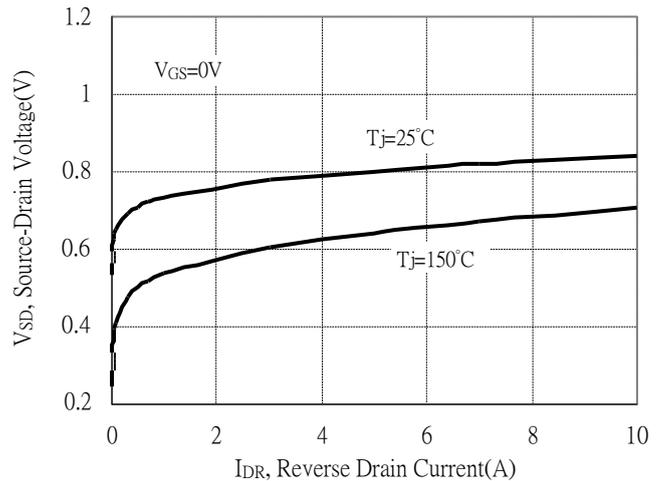
Breakdown Voltage vs Ambient Temperature



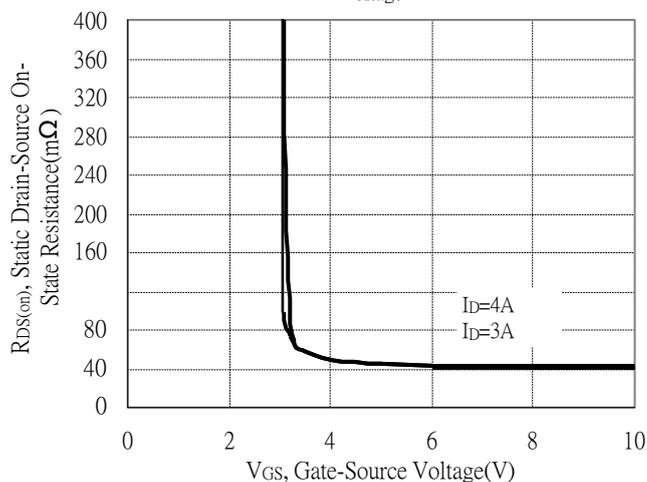
Static Drain-Source On-State resistance vs Drain Current



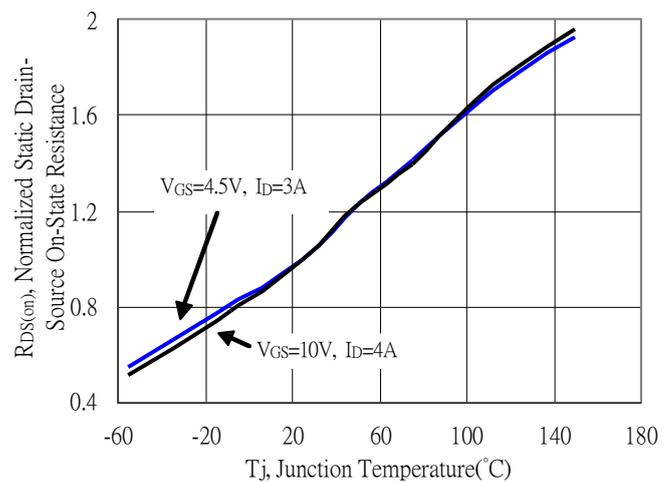
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

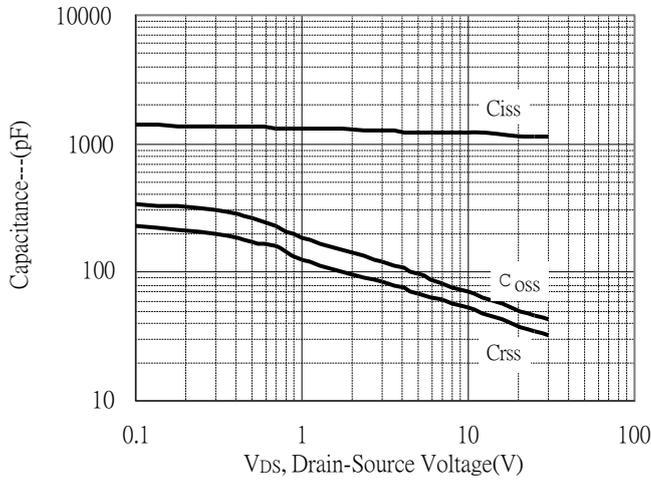


Drain-Source On-State Resistance vs Junction Temperature

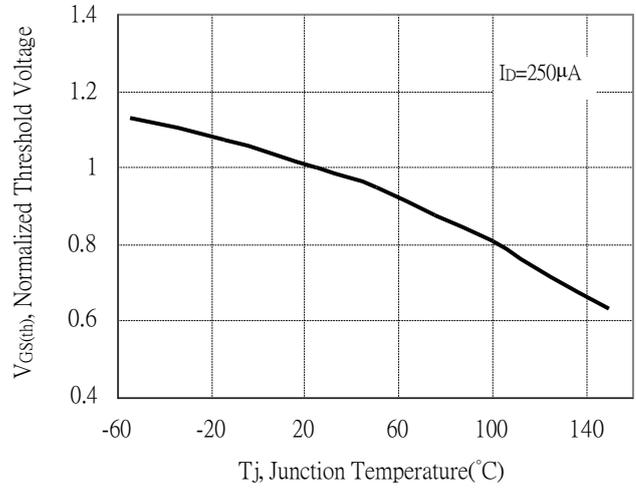


**Typical Characteristics(Cont.)**

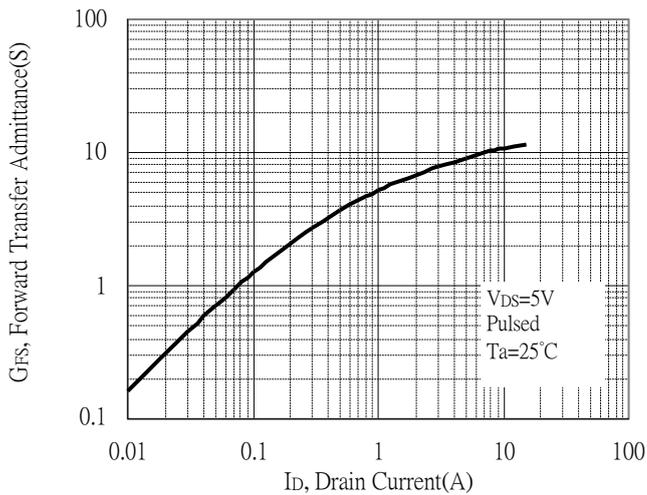
Capacitance vs Drain-to-Source Voltage



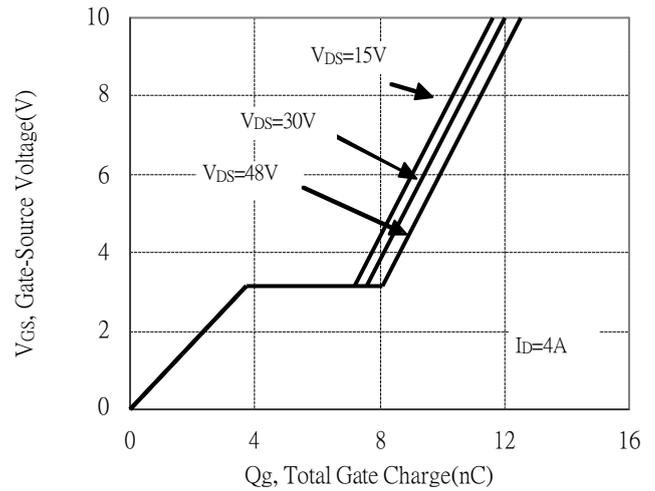
Threshold Voltage vs Junction Temperature



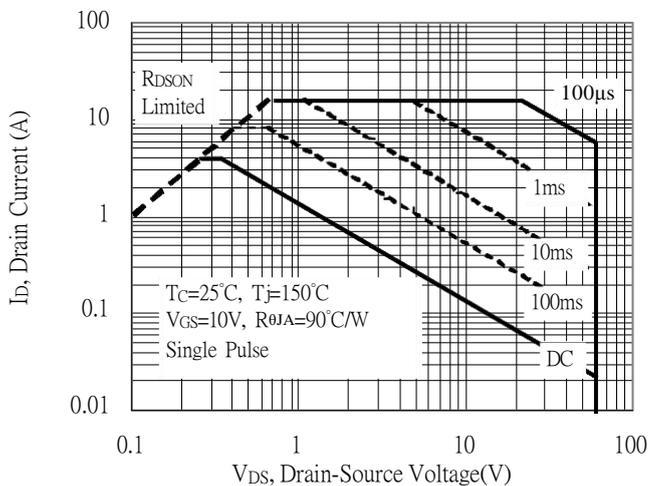
Forward Transfer Admittance vs Drain Current



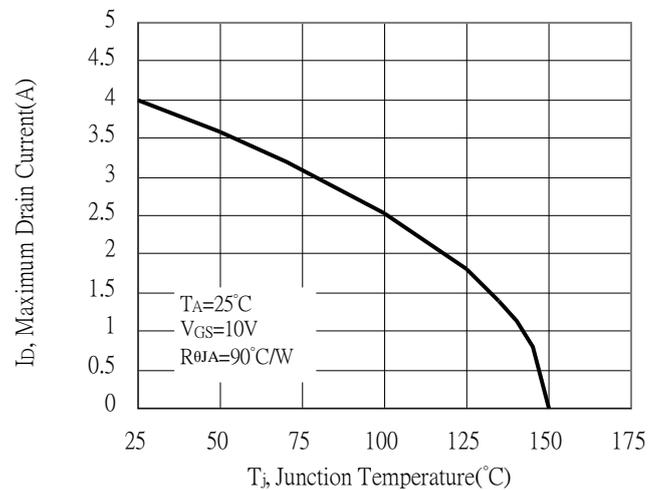
Gate Charge Characteristics



Maximum Safe Operating Area

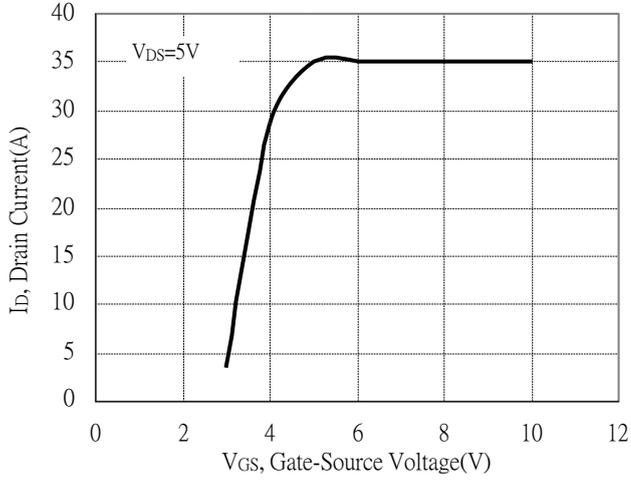


Maximum Drain Current vs Junction Temperature

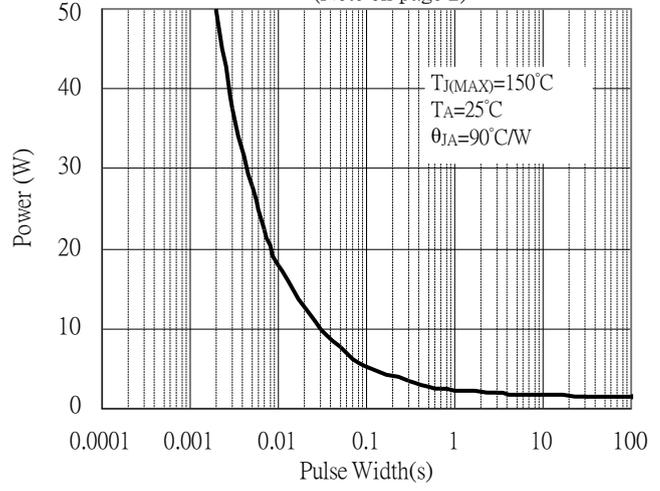


**Typical Characteristics(Cont.)**

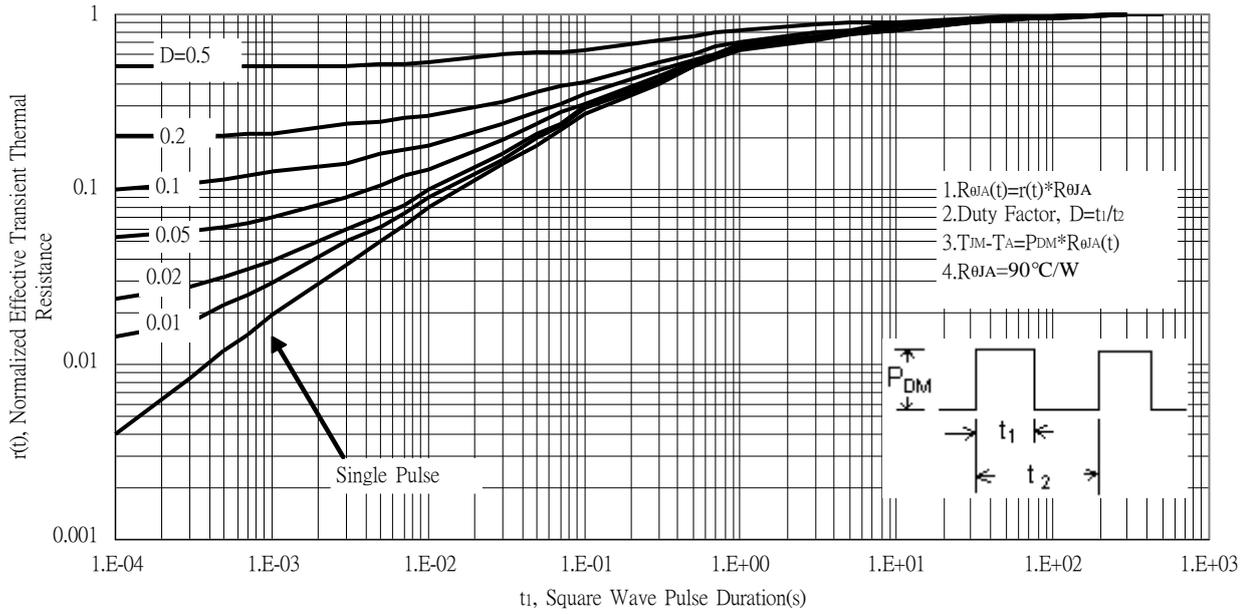
Typical Transfer Characteristics



Single Pulse Power Rating, Junction to Ambient  
 (Note on page 2)

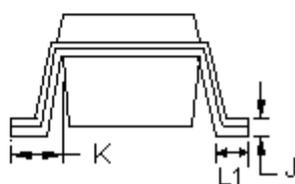
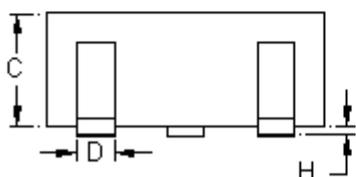
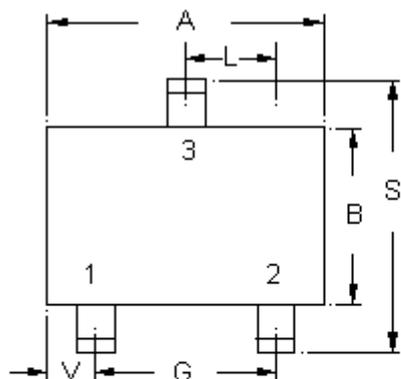


Transient Thermal Response Curves

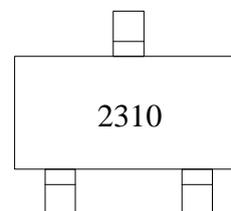




**SOT-23 Dimension**



Marking:



3-Lead SOT-23 Plastic  
 Surface Mounted Package  
 Package Code: N3

Style: Pin 1.Gate 2.Source 3.Drain

\*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.1102	0.1204	2.80	3.04	J	0.0032	0.0079	0.08	0.20
B	0.0472	0.0669	1.20	1.70	K	0.0118	0.0266	0.30	0.67
C	0.0335	0.0512	0.89	1.30	L	0.0335	0.0453	0.85	1.15
D	0.0118	0.0197	0.30	0.50	S	0.0830	0.1161	2.10	2.95
G	0.0669	0.0910	1.70	2.30	V	0.0098	0.0256	0.25	0.65
H	0.0000	0.0040	0.00	0.10	L1	0.0118	0.0197	0.30	0.50