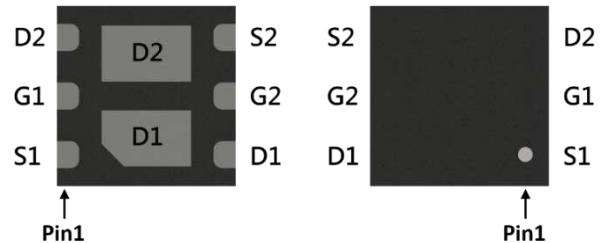


N- AND P-Channel Enhancement Mode Power MOSFET

Features:

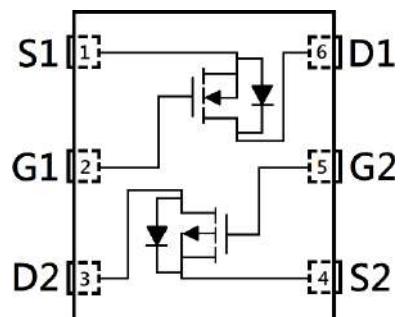
- Simple drive requirement
- Low On Resistance
- Low Gate Charge
- Fast switching speed

DFN2x2-6L



	N-CH	P-CH
BV _{DSS}	14V	-14V
I _D @V _{GS} =(-)4.5V, T _C =25°C	11A	-7.6A
I _D @V _{GS} =(-)4.5V, T _A =25°C	6.5A	-4.5A
R _{DS(ON)} typ. @ V _{GS} =(-)4.5V	17mΩ	38mΩ
R _{DS(ON)} typ. @ V _{GS} =(-)2.5V	20mΩ	50mΩ
R _{DS(ON)} typ. @ V _{GS} =(-)1.8V	28mΩ	65mΩ
R _{DS(ON)} typ. @ V _{GS} =(-)1.5V	42mΩ	100mΩ

Equivalent Circuit



G : Gate S : Source D : Drain

Ordering Information

Device	Package	Shipping
KWC3588C	DFN2x2-6L (Pb-free lead plating and halogen-free package)	3000 pcs / Tape & Reel



Absolute Maximum Ratings ($T_A=25^\circ\text{C}$)

Parameter	Symbol	Limits		Unit
		N-CH	P-CH	
Drain-Source Voltage	V_{DS}	14	-14	V
Gate-Source Voltage	V_{GS}	± 8	± 8	
Continuous Drain Current @ $V_{GS}=(-)4.5\text{V}$, $T_C=25^\circ\text{C}$	I_D	11	-7.6	A
Continuous Drain Current @ $V_{GS}=(-)4.5\text{V}$, $T_C=100^\circ\text{C}$		7	-4.8	
Continuous Drain Current @ $V_{GS}=(-)4.5\text{V}$, $T_A=25^\circ\text{C}$		6.5	-4.5	
Continuous Drain Current @ $V_{GS}=(-)4.5\text{V}$, $T_A=70^\circ\text{C}$		5.2	-3.6	
Pulsed Drain Current	I_{DM}	40	-30	mJ
Continuous Body Diode Forward Current @ $T_C=25^\circ\text{C}$	I_S	3.3	-3.3	
Avalanche Current @ $L=0.1\text{mH}$	I_{AS}	8	-8	
Avalanche Energy @ $L=0.5\text{mH}$	E_{AS}	6	6	
Total Power Dissipation	P_D	4 1.6 1.4 0.9	W	
$T_C=25^\circ\text{C}$				
$T_C=100^\circ\text{C}$				
$T_A=25^\circ\text{C}$				
$T_A=70^\circ\text{C}$				
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55~+150		$^\circ\text{C}$

Thermal Data

Parameter	Symbol	Steady State	Unit
Thermal Resistance, Junction-to-case	$R_{\theta JC}$	31	$^\circ\text{C/W}$
Thermal Resistance, Junction-to-ambient	$R_{\theta JA}$	90	

Note:

- *a. The power dissipation P_D is based on $T_{J(MAX)}=150^\circ\text{C}$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
- *b. The value of $R_{\theta JA}$ is measured with the device mounted on 1 in² FR-4 board with 2 oz. copper, in a still air environment with $T_A=25^\circ\text{C}$. The power dissipation P_D is based on $R_{\theta JA}$ and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
- *c. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}=150^\circ\text{C}$. Ratings are based on low frequency and low duty cycles to keep initial $T_J=25^\circ\text{C}$.

N-Channel Electrical Characteristics ($T_A=25^\circ C$, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions	
Static						
BV _{DSS}	14	-	-	V	V _{GS} =0V, I _D =250μA	
V _{GS(th)}	0.3	-	1		V _{DS} =V _{GS} , I _D =250μA	
G _{FS}	-	7.5	-	S	V _{DS} =5V, I _D =3A	
I _{GSS}	-	-	±100	nA	V _{GS} =±8V, V _{DS} =0V	
I _{DSS}	-	-	1	μA	V _{DS} =10V, V _{GS} =0V	
R _{DSS(ON)}	-	17	21	mΩ	V _{GS} =4.5V, I _D =5A	
	-	20	27		V _{GS} =2.5V, I _D =4.6A	
	-	28	42		V _{GS} =1.8V, I _D =1A	
	-	42	84		V _{GS} =1.5V, I _D =1A	
Dynamic						
C _{iss}	-	470	-	pF	V _{DS} =10V, V _{GS} =0V, f=1MHz	
C _{oss}	-	100	-			
C _{rss}	-	90	-			
R _g	-	2.2	-	Ω	f=1MHz	
Q _g *1, 2	-	6.7	-	nC	V _{DS} =10V, I _D =2A, V _{GS} =4.5V	
Q _{gs} *1, 2	-	0.8	-			
Q _{gd} *1, 2	-	1.9	-			
t _{d(ON)} *1, 2	-	4	-			
t _r *1, 2	-	17	-	ns	V _{DS} =10V, I _D =1A, V _{GS} =4.5V, R _{GS} =6Ω	
t _{d(OFF)} *1, 2	-	28	-			
t _f *1, 2	-	6	-			
Source-Drain Diode						
V _{SD} *1	-	0.84	1.2	V	I _s =3A, V _{GS} =0V	
trr	-	6	-	ns	I _F =2A, dI _F /dt=100A/μs	
Qrr	-	1.5	-	nC		

Note:

*1. Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

*2. Independent of operating temperature

P-Channel Electrical Characteristics ($T_A=25^\circ C$, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions	
Static						
BV _{DSS}	-14	-	-	V	V _{GS} =0V, I _D =-250μA	
V _{GS(th)}	-0.3	-	-1		V _{DS} =V _{GS} , I _D =-250μA	
G _{FS}	-	8.1	-	S	V _{DS} =-5V, I _D =-3A	
I _{GSS}	-	-	±100	nA	V _{GS} =±8V, V _{DS} =0V	
I _{DSS}	-	-	-1	μA	V _{DS} =-10V, V _{GS} =0V	
R _{DSS(ON)}	-	38	49	mΩ	V _{GS} =-4.5V, I _D =-3.6A	
	-	50	65		V _{GS} =-2.5V, I _D =-3.2A	
	-	65	98		V _{GS} =-1.8V, I _D =-0.5A	
	-	100	200		V _{GS} =-1.5V, I _D =-0.5A	
Dynamic						
C _{iss}	-	600	-	pF	V _{DS} =-10V, V _{GS} =0V, f=1MHz	
C _{oss}	-	120	-			
C _{rss}	-	110	-	Ω	f=1MHz	
R _g	-	6.2	-			
Q _g *1, 2	-	8	-			
Q _{gs} *1, 2	-	1	-			
Q _{gd} *1, 2	-	2.3	-			
t _{d(ON)} *1, 2	-	10	-			
t _r *1, 2	-	20	-			
t _{d(OFF)} *1, 2	-	35	-			
t _f *1, 2	-	17	-			
Source-Drain Diode						
V _{SD} *1	-	-0.85	-1.2	V	I _s =-2A, V _{GS} =0V	
trr	-	10	-	ns	I _F =-2A, dI _F /dt=100A/μs	
Qrr	-	2	-	nC		

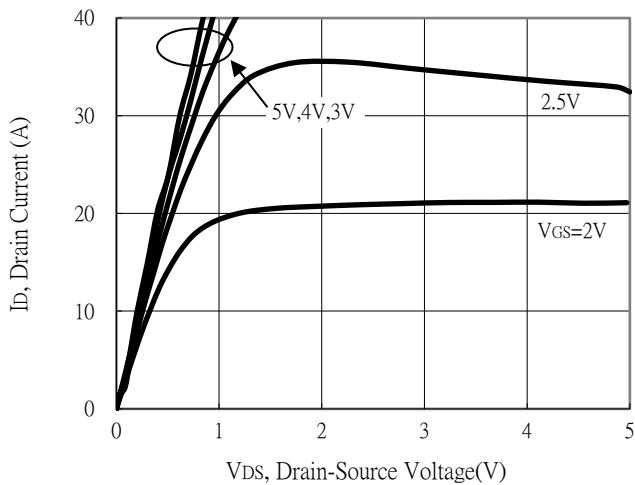
Note:

*1. Pulse Test : Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$

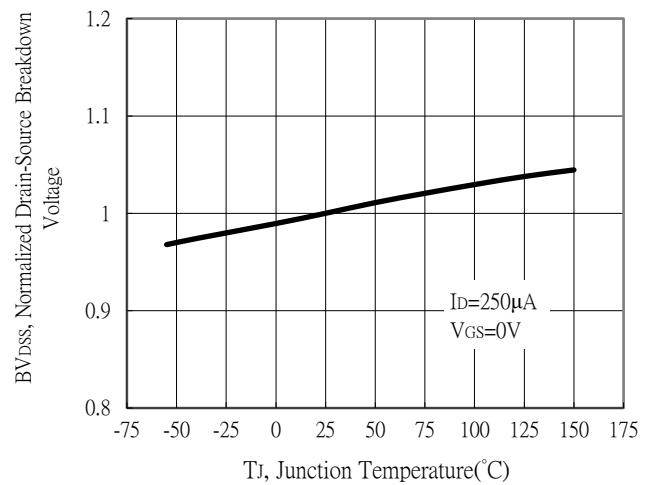
*2. Independent of operating temperature

Typical Characteristics : Q1(N-channel)

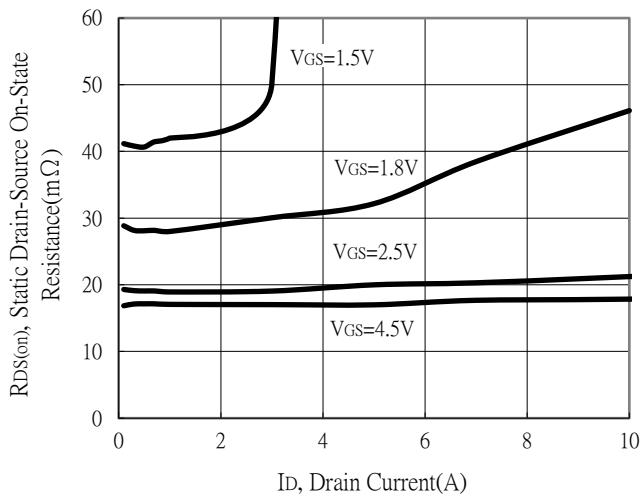
Typical Output Characteristics



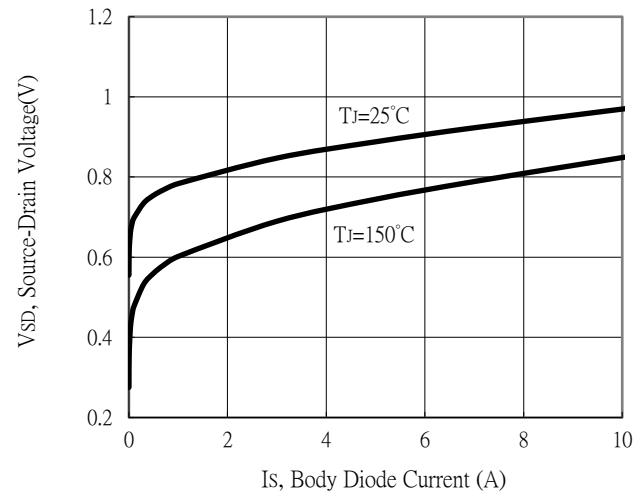
Breakdown Voltage vs Ambient Temperature



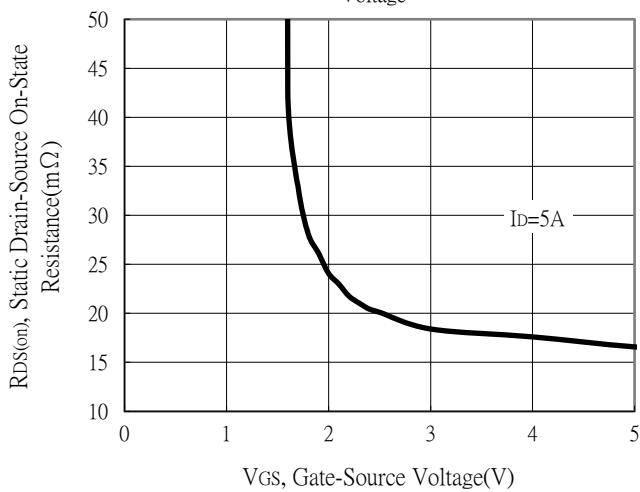
Static Drain-Source On-State resistance vs Drain Current



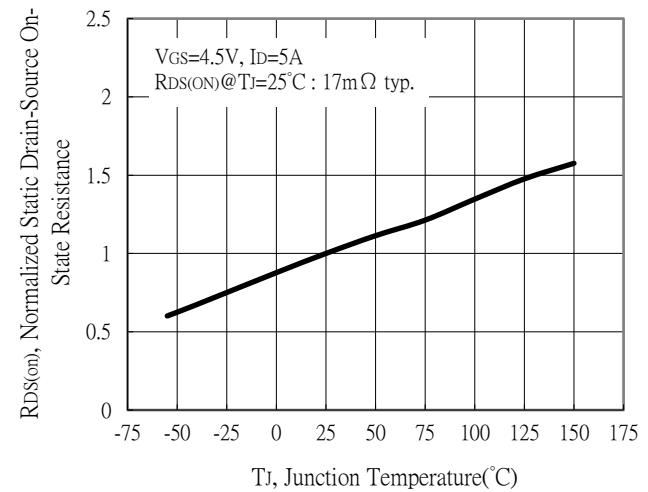
Body Diode Current vs Source-Drain Voltage



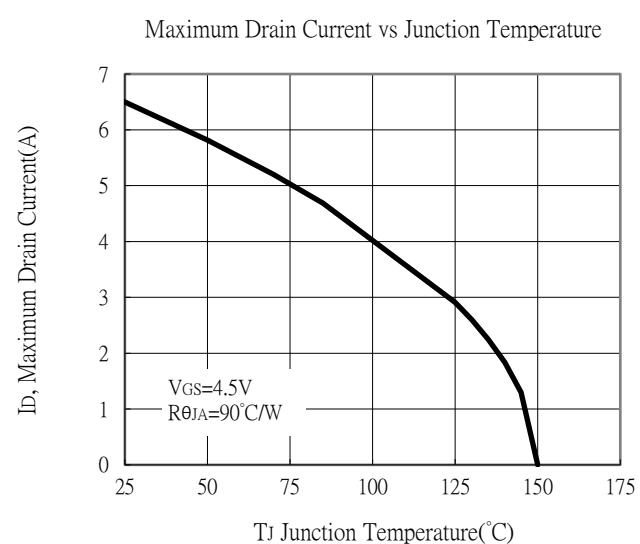
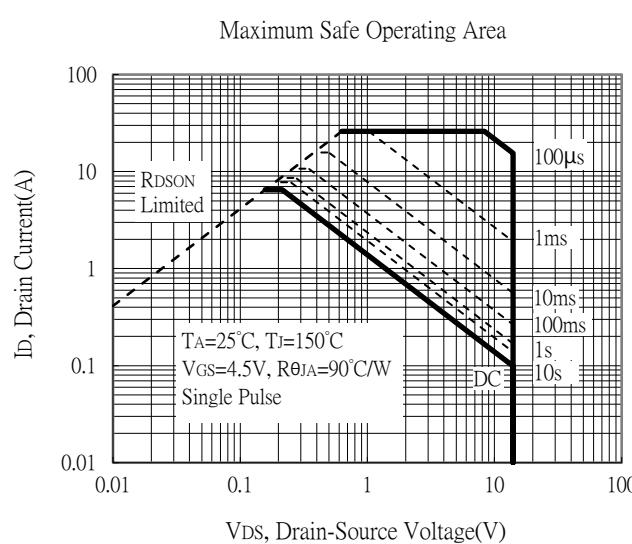
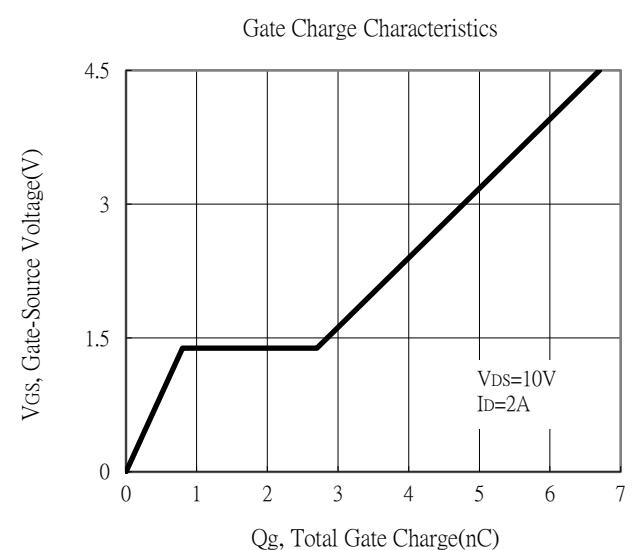
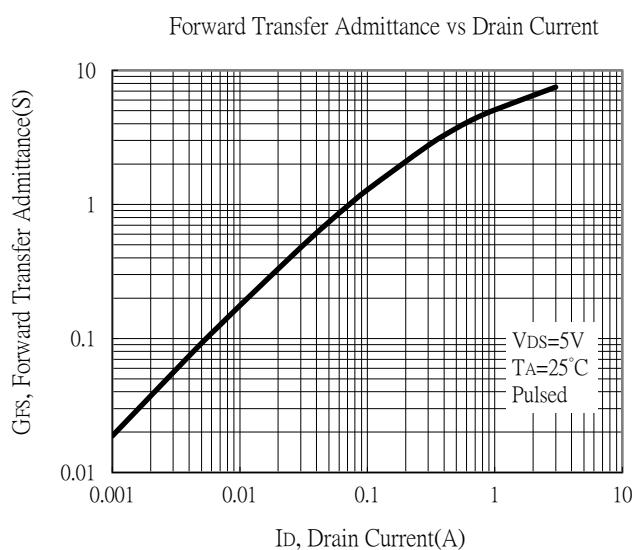
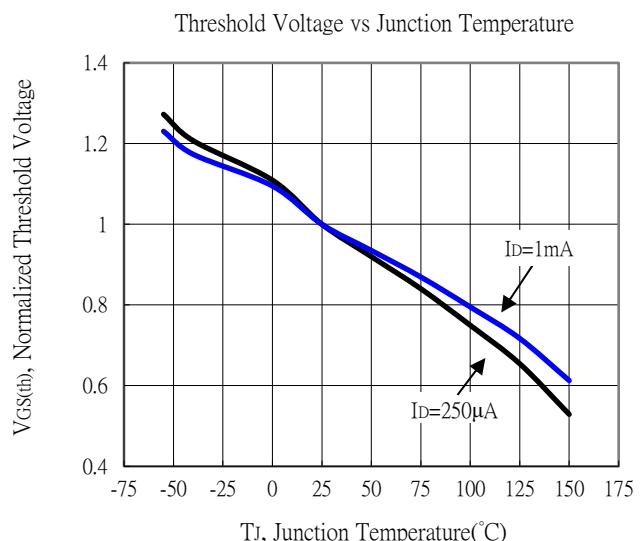
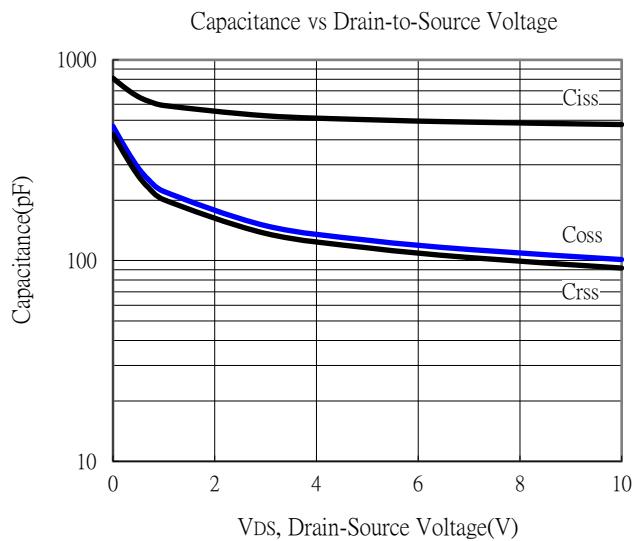
Static Drain-Source On-State Resistance vs Gate-Source Voltage



Drain-Source On-State Resistance vs Junction Temperature

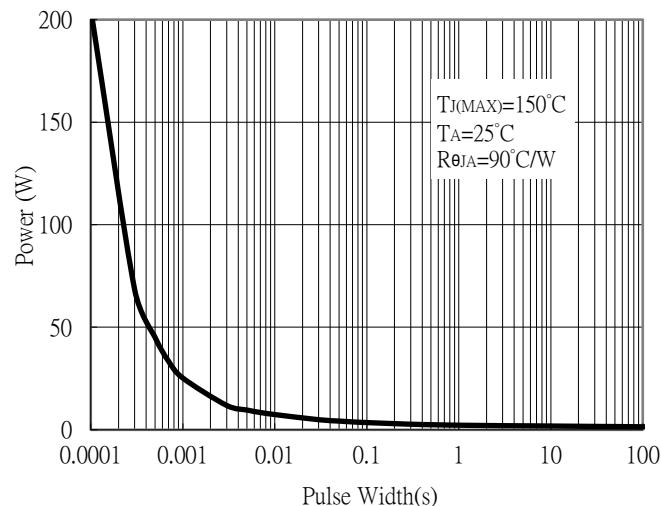


Typical Characteristics (Cont.) : Q1(N-channel)

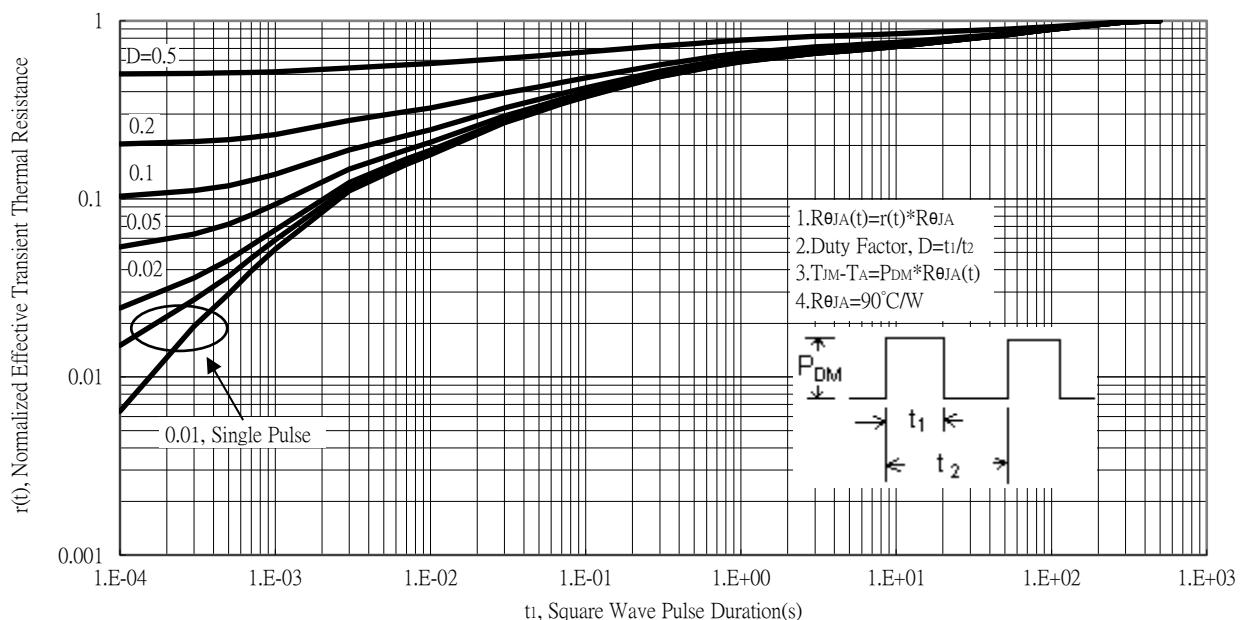


Typical Characteristics (Cont.) : Q1(N-channel)

Single Pulse Power Rating, Junction to Ambient

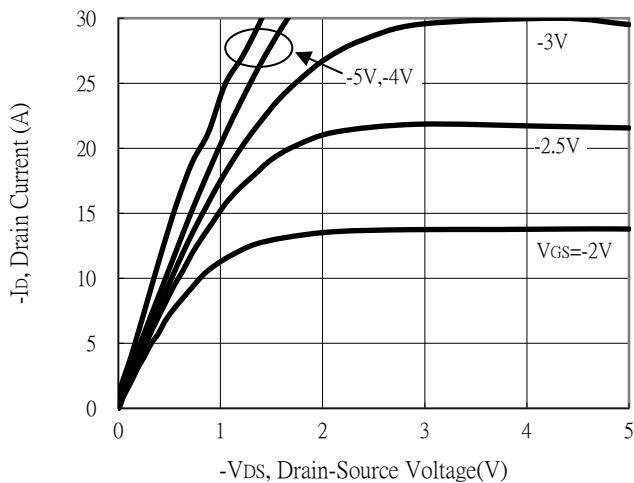


Transient Thermal Response Curves

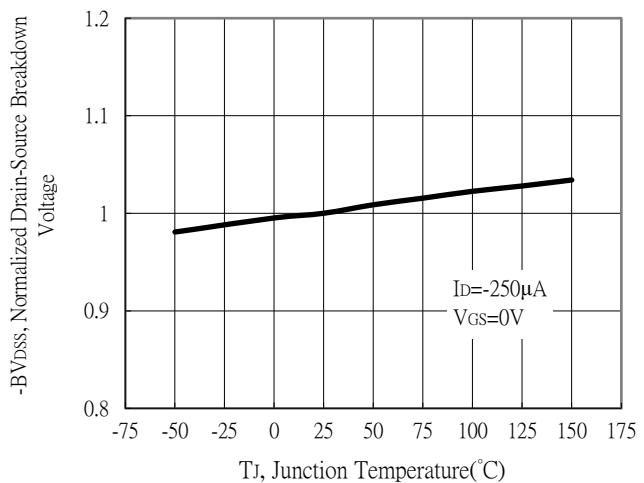


Typical Characteristics : Q2(P-channel)

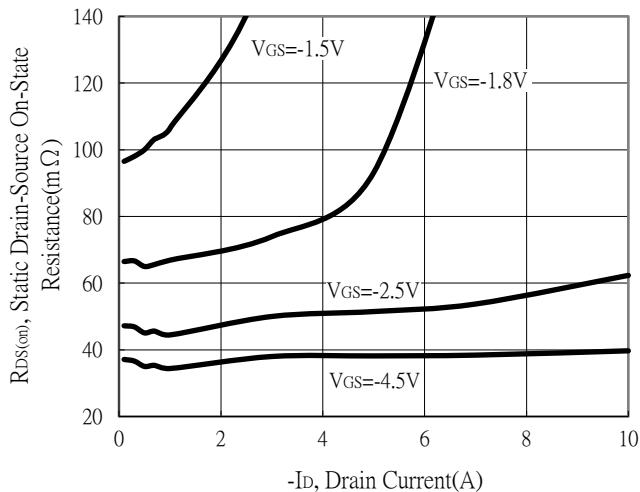
Typical Output Characteristics



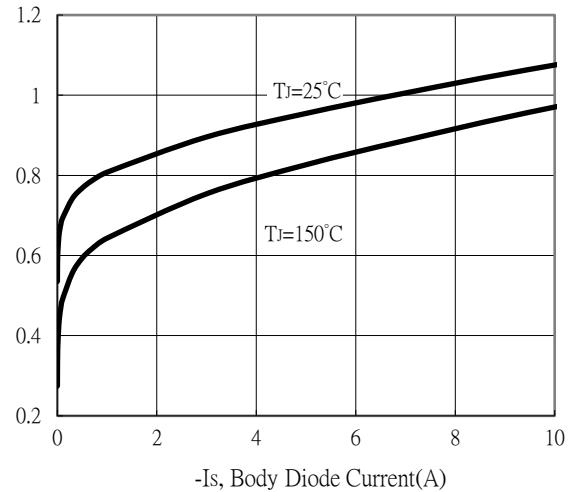
Breakdown Voltage vs Ambient Temperature



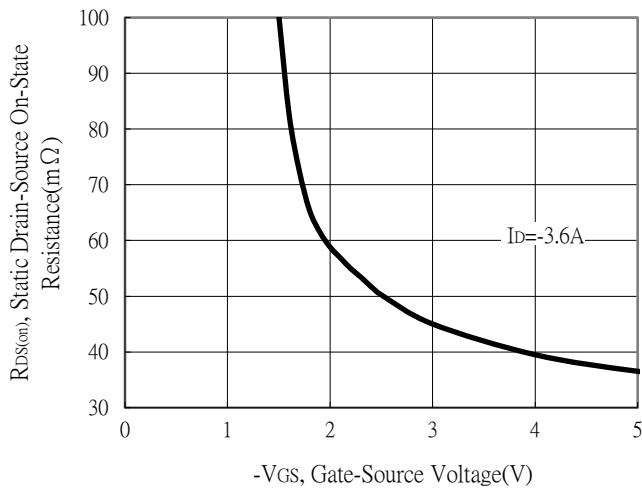
Static Drain-Source On-State resistance vs Drain Current



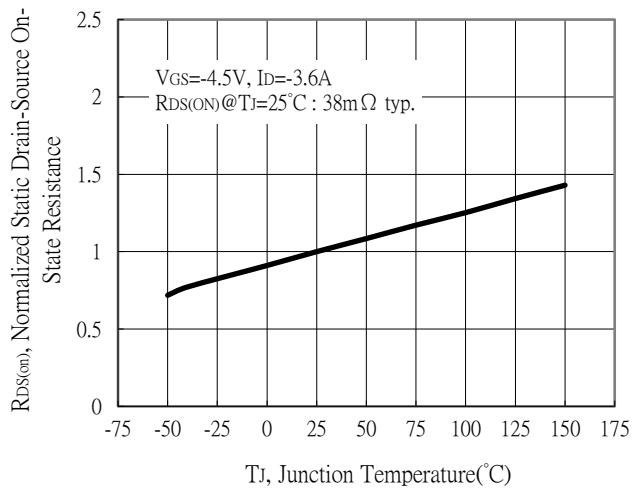
Body Diode Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

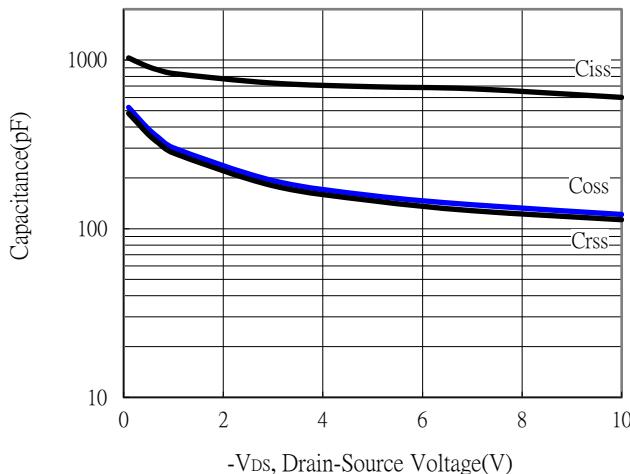


Drain-Source On-State Resistance vs Junction Temperature

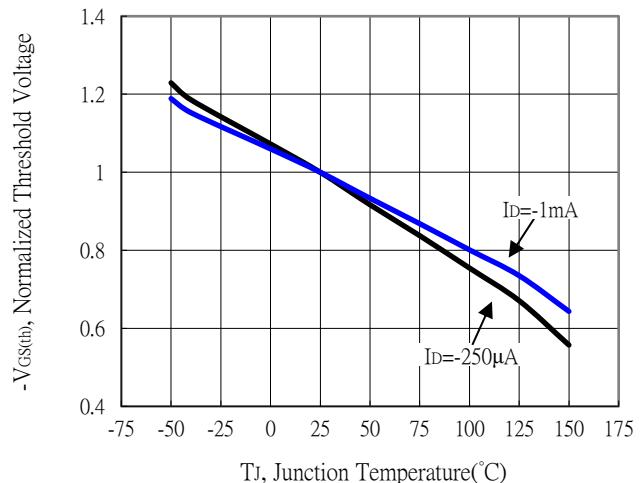


Typical Characteristics (Cont.) : Q2(P-channel)

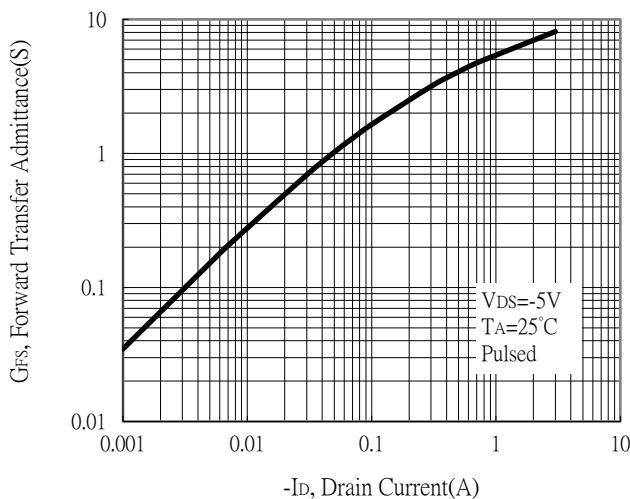
Capacitance vs Drain-to-Source Voltage



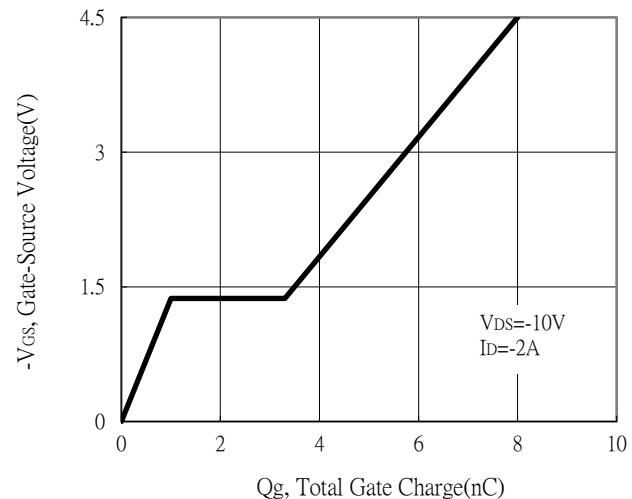
Threshold Voltage vs Junction Temperature



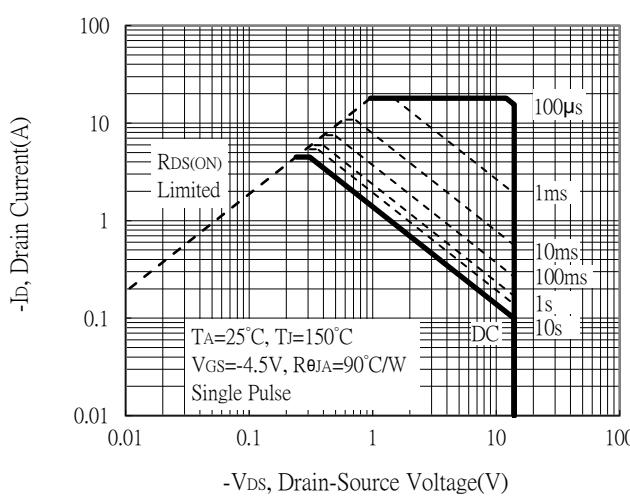
Forward Transfer Admittance vs Drain Current



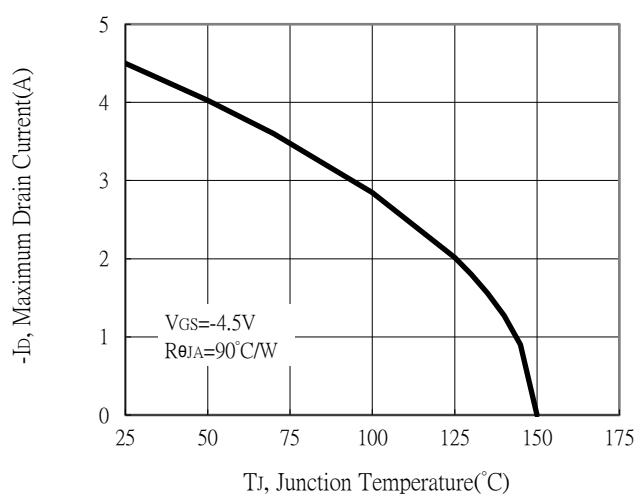
Gate Charge Characteristics



Maximum Safe Operating Area

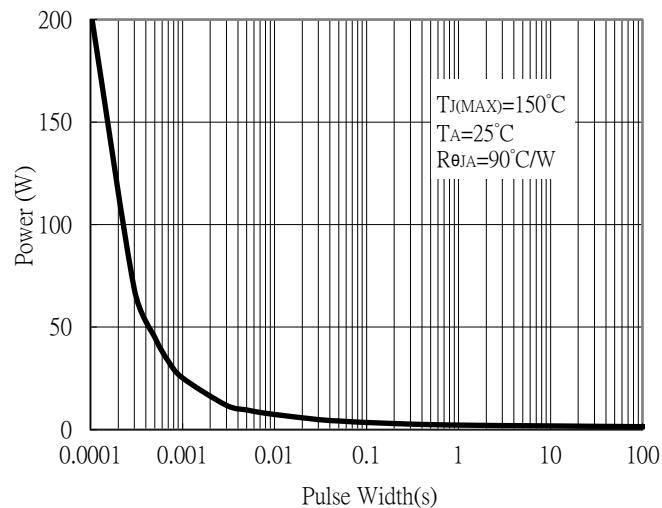


Maximum Drain Current vs Junction Temperature

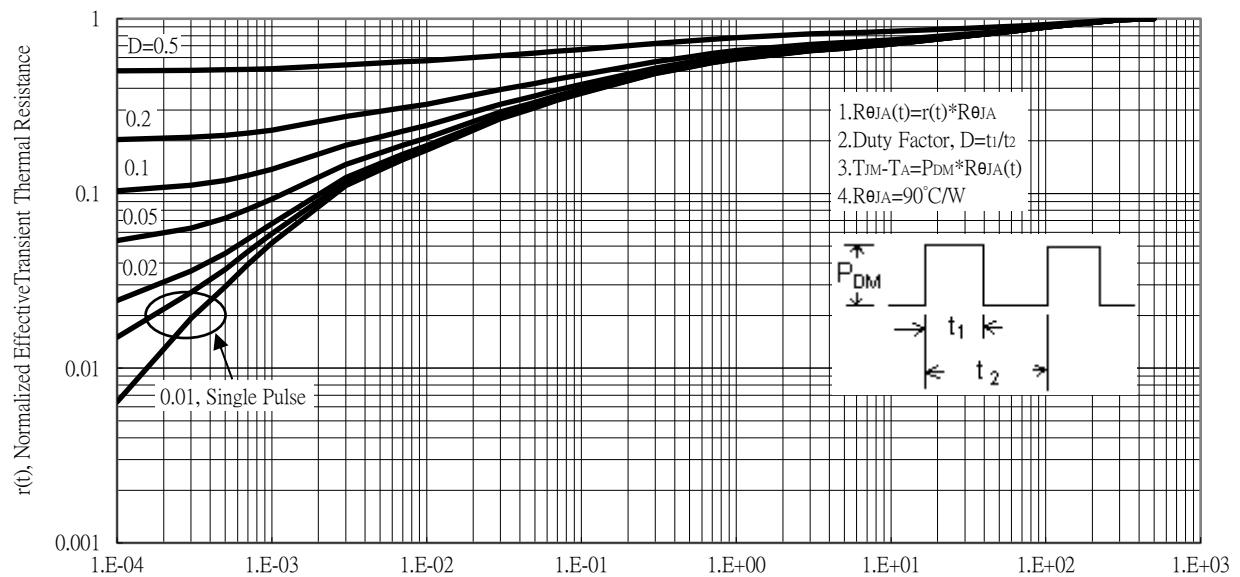


Typical Characteristics (Cont.) : Q2(P-channel)

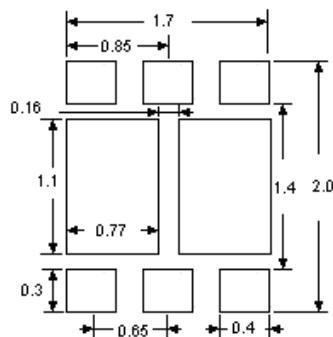
Single Pulse Power Rating, Junction to Ambient



Transient Thermal Response Curves

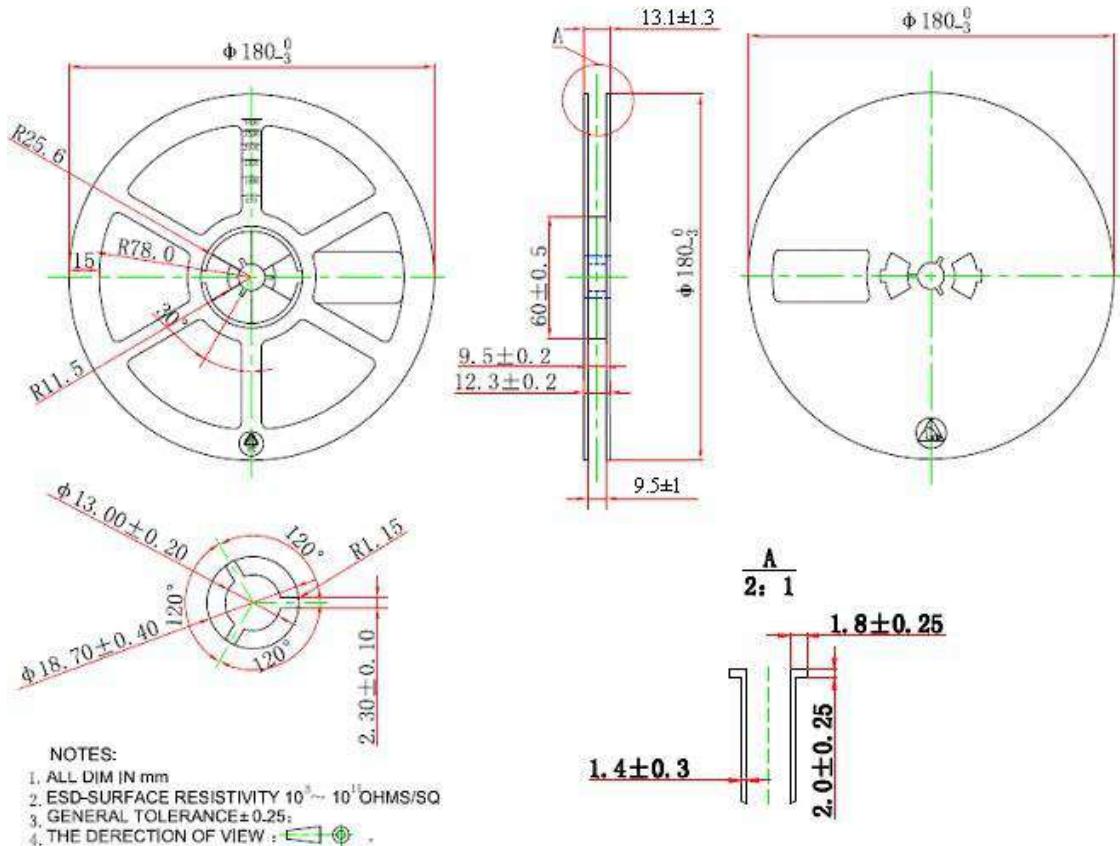


Recommended Soldering Footprint

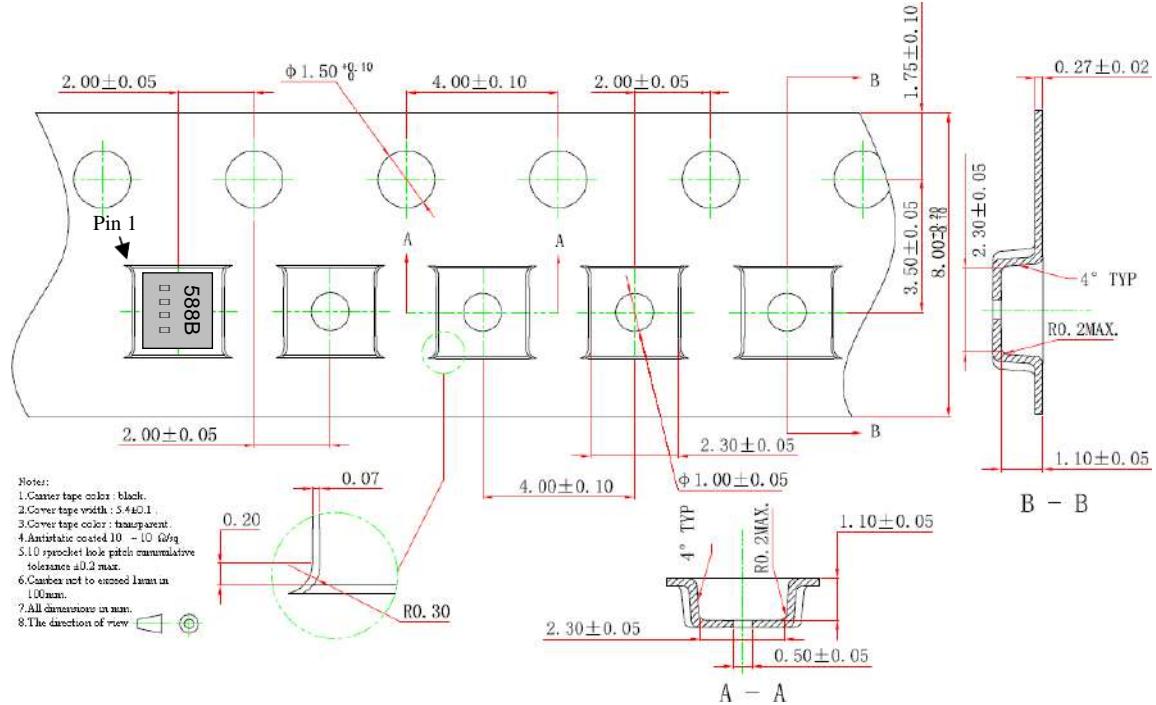


Unit : mm

Reel Dimension



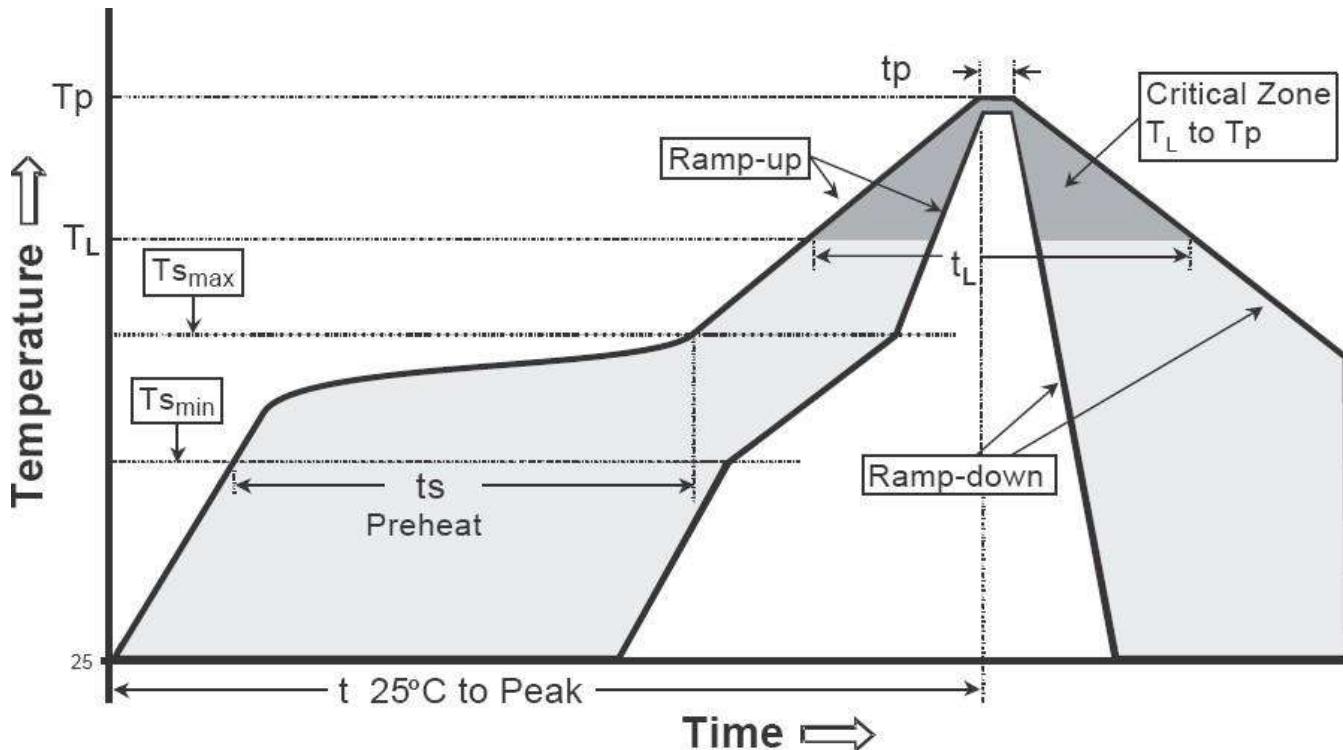
Carrier Tape Dimension



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

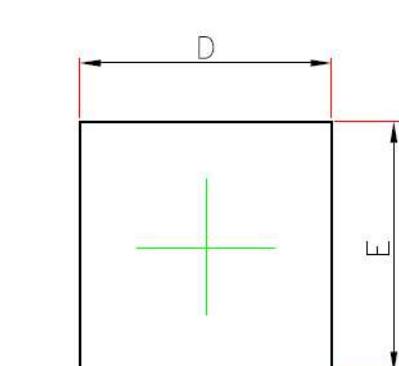
Recommended temperature profile for IR reflow



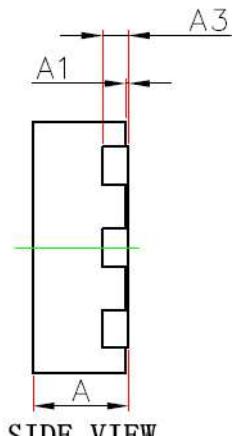
Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate ($T_{s\max}$ to T_p)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min($T_{s\min}$)	100°C	150°C
-Temperature Max($T_{s\max}$)	150°C	200°C
-Time($t_{s\min}$ to $t_{s\max}$)	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T_L)	183°C	217°C
-Time (t_L)	60-150 seconds	60-150 seconds
Peak Temperature(T_p)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(t_p)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

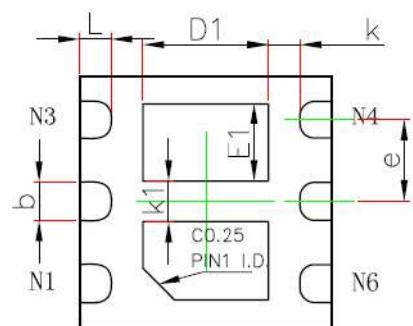
DFN2x2-6L Dimension



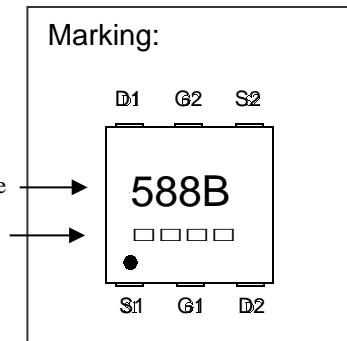
TOP VIEW



SIDE VIEW



BOTTOM VIEW



Style:
 Pin 1. Source1 (S1)
 Pin 2. Gate 1 (G1)
 Pin 3. Drain2 (D2)
 Pin 4. Source2 (S2)
 Pin 5. Gate2 (G2)
 Pin 6. Drain1 (D1)

Date Code(counting from left to right) :
 1st code: year code, the last digit of Christian year
 2nd code : month code, Jan→A, Feb→B, Mar→C,
 Apr→D, May→E, Jun→F, Jul→G, Aug→H,
 Sep→J, Oct→K, Nov→L, Dec→M
 3rd and 4th codes : production serial number, 01~99

6-Lead DFN2x2-6L Plastic
 Surface Mounted Package C

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.700	0.800	0.028	0.031	E1	0.520	0.720	0.020	0.028
A1	0.000	0.050	0.000	0.002	b	0.250	0.350	0.010	0.014
A3	0.203 REF		0.008 REF		e	0.650 TYP		0.026 TYP	
D	1.900	2.100	0.075	0.083	k	0.250 REF		0.010 REF	
E	1.900	2.100	0.075	0.083	k1	0.320 REF		0.013 REF	
D1	0.900	1.100	0.035	0.043	L	0.200	0.300	0.008	0.012