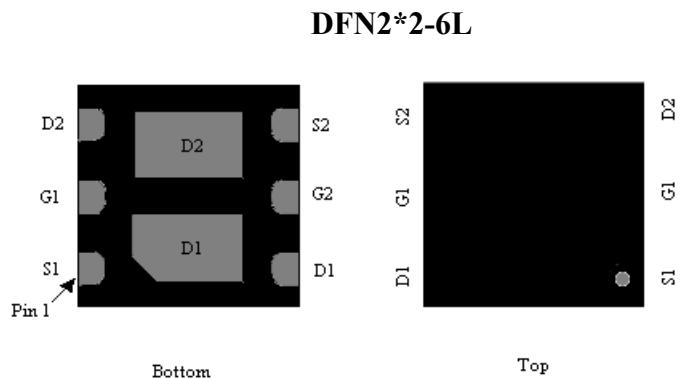


## N- AND P-Channel Enhancement Mode MOSFET

### Features:

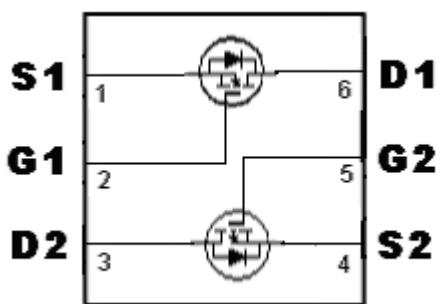
- Simple drive requirement
- Low gate charge
- Low on-resistance
- Fast switching speed
- Pb-free lead plating and halogen-free package



### Description:

The KWC3586BDFA6 consists of a N-channel and a P-channel enhancement-mode MOSFET in a single DFN2\*2-6L package, providing the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness. The I-industrial surface mount applications.

**KWC3586BDFA6**



G : Gate S : Source D : Drain

### Ordering Information

Device	Package	Shipping
KWC3586BDFA6	DFN2x2-6L (Pb-free lead plating and halogen-free package)	3000 pcs / Tape & Reel

### Absolute Maximum Ratings ( $T_a=25^\circ C$ )

Parameter	Symbol	Limits		Unit
		N-channel	P-channel	
Drain-Source Breakdown Voltage	$BV_{DSS}$	20	-20	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	$\pm 12$	
Continuous Drain Current @ $T_A=25^\circ C$ (Note 1)	$I_D$	5	-3.3	A
Continuous Drain Current @ $T_A=70^\circ C$ (Note 1)	$I_D$	4	-2.6	
Pulsed Drain Current (Note 2)	$I_{DM}$	20	-20	
Total Power Dissipation (Note 1)	$P_d$	1.38		W
Linear Derating Factor		0.01		W / °C
Operating Junction and Storage Temperature	$T_j, T_{stg}$	-55~+150		°C

Note : 1.Surface mounted on 1 in<sup>2</sup> copper pad of FR-4 board,  $t \leq 5$  sec

2.Pulse width limited by maximum junction temperature

### N-Channel Electrical Characteristics ( $T_j=25^\circ C$ , unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
$BV_{DSS}$	20	-	-	V	$V_{GS}=0, I_D=250\mu A$
$\Delta BV_{DSS}/\Delta T_j$	-	0.02	-	V/°C	Reference to 25°C, $I_D=1mA$
$V_{GS(th)}$	0.5	0.7	1.2	V	$V_{DS}=V_{GS}, I_D=250\mu A$
$I_{GSS}$	-	-	$\pm 100$	nA	$V_{GS}=\pm 12V, V_{DS}=0$
$I_{DSS}$	-	-	1	$\mu A$	$V_{DS}=20V, V_{GS}=0$
	-	-	10		$V_{DS}=16V, V_{GS}=0, T_j=70^\circ C$
$*R_{DS(ON)}$	-	27	40	m $\sim$	$I_D=3.5A, V_{GS}=4.5V$
	-	37	50		$I_D=1.2A, V_{GS}=2.5V$
	-	82	105		$I_D=0.5A, V_{GS}=1.5V$
$*G_{FS}$	-	7	-	S	$V_{DS}=5V, I_D=3A$
<b>Dynamic</b>					
$C_{iss}$	-	423	-	pF	$V_{DS}=20V, V_{GS}=0, f=1MHz$
$C_{oss}$	-	50	-		
$C_{rss}$	-	48	-		
$*t_{d(ON)}$	-	6	-	ns	$V_{DS}=15V, I_D=1A, V_{GS}=5V, R_G=3.3\Omega, R_D=15\Omega$
$*t_r$	-	8	-		
$*t_{d(OFF)}$	-	11	-		
$*t_f$	-	10	-		
$*Q_g$	-	6	-	nC	$V_{DS}=16V, I_D=3A, V_{GS}=4.5V$
$*Q_{gs}$	-	0.8	-		
$*Q_{gd}$	-	2.5	-		
<b>Source-Drain Diode</b>					
$*V_{SD}$	-	0.77	1.2	V	$V_{GS}=0V, I_S=1.2A$
$*tr$	-	16	-	ns	$I_S=3A, V_{GS}=0V, dI/dt=100A/\mu s$
$*Q_{rr}$	-	8	-		

\*Pulse Test : Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$



**P-Channel Electrical Characteristics (T<sub>j</sub>=25°C, unless otherwise specified)**

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
BV <sub>DSS</sub>	-20	-	-	V	V <sub>GS</sub> =0, I <sub>D</sub> =-250μA
ΔBV <sub>DSS</sub> /ΔT <sub>j</sub>	-	-0.01	-	V/°C	Reference to 25°C, I <sub>D</sub> =-1mA
V <sub>GS(th)</sub>	-	-0.8	-1.2	V	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA
I <sub>GSS</sub>	-	-	±100	nA	V <sub>GS</sub> =±12V, V <sub>DS</sub> =0
I <sub>DSS</sub>	-	-	-1	μA	V <sub>DS</sub> =-20V, V <sub>GS</sub> =0
	-	-	-25		V <sub>DS</sub> =-16V, V <sub>GS</sub> =0, T <sub>j</sub> =70°C
*R <sub>DSS(ON)</sub>	-	78	105	m └─┐	I <sub>D</sub> =-2.5A, V <sub>GS</sub> =-4.5V
	-	115	150		I <sub>D</sub> =-2A, V <sub>GS</sub> =-2.5V
	-	280	350		I <sub>D</sub> =-0.5A, V <sub>GS</sub> =-1.5V
*G <sub>FS</sub>	-	5	-	S	V <sub>DS</sub> =-5V, I <sub>D</sub> =-2A
<b>Dynamic</b>					
C <sub>iss</sub>	-	429	-	pF	V <sub>DS</sub> =-20V, V <sub>GS</sub> =0, f=1MHz
C <sub>oss</sub>	-	45	-		
C <sub>rss</sub>	-	41	-		
*t <sub>d(ON)</sub>	-	6	-	ns	V <sub>DS</sub> =-10V, I <sub>D</sub> =-1A, V <sub>GS</sub> =-10V, R <sub>G</sub> =3.3Ω, R <sub>D</sub> =10Ω
*t <sub>r</sub>	-	17	-		
*t <sub>d(OFF)</sub>	-	16	-		
*t <sub>f</sub>	-	5	-		
*Q <sub>g</sub>	-	6	-	nC	V <sub>DS</sub> =-16V, I <sub>D</sub> =-2A, V <sub>GS</sub> =-4.5V
*Q <sub>gs</sub>	-	0.8	-		
*Q <sub>gd</sub>	-	2.4	-		
<b>Source-Drain Diode</b>					
*V <sub>SD</sub>	-	-0.82	-1.2	V	V <sub>GS</sub> =0V, I <sub>S</sub> =-1.2A
*trr	-	20	-	ns	I <sub>S</sub> =-2A, V <sub>GS</sub> =0V, dI/dt=100A/μs
*Qrr	-	15	-	nC	

\*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

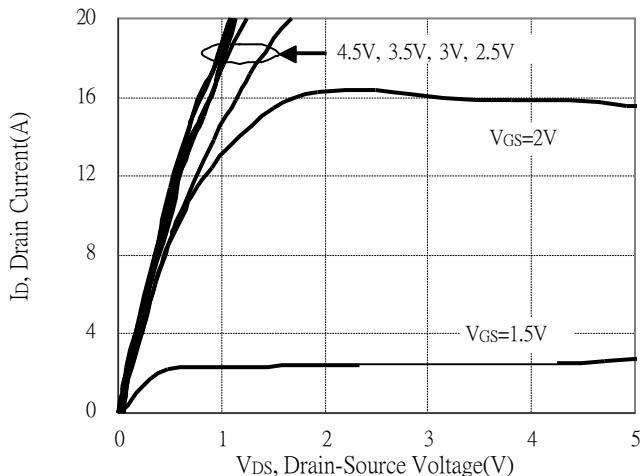
**Thermal Data**

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	R <sub>θJC</sub>	80	°C/W
Thermal Resistance, Junction-to-ambient, max	R <sub>θJA</sub>	90 (Note)	°C/W

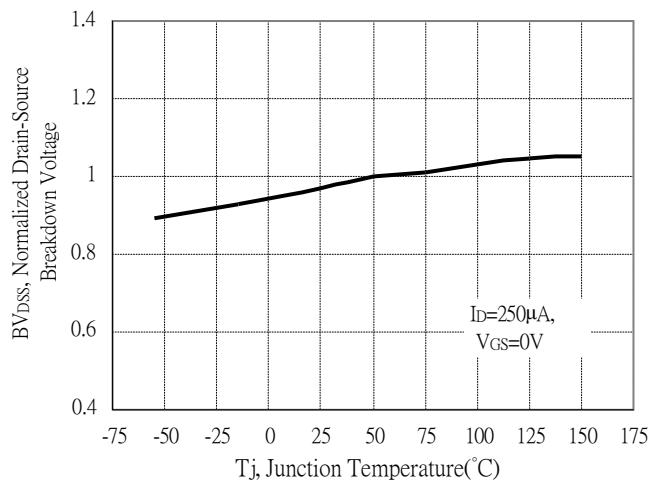
Note : Surface mounted on 1 in<sup>2</sup>copper pad of FR-4 board, t≤5 sec; 195°C/W when mounted on minimum copper pad

## N-channel Typical Characteristics

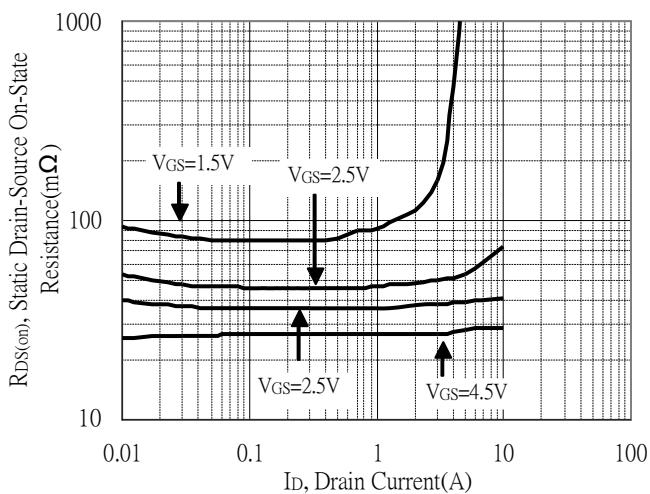
Typical Output Characteristics



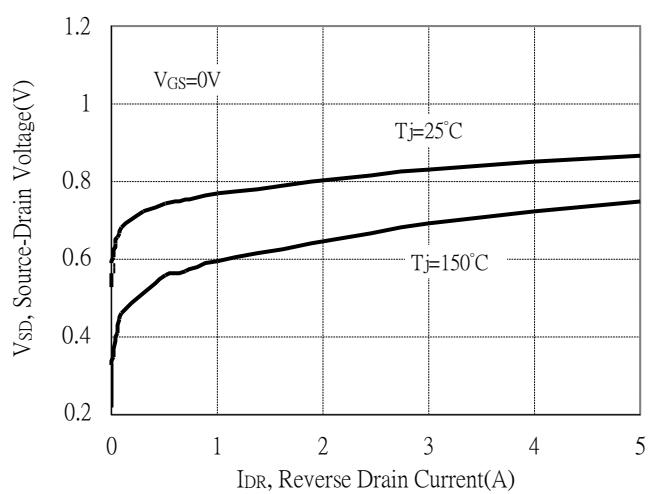
Breakdown Voltage vs Ambient Temperature



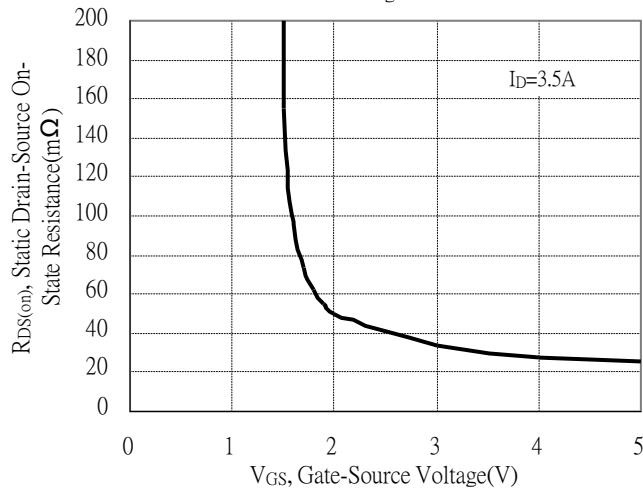
Static Drain-Source On-State resistance vs Drain Current



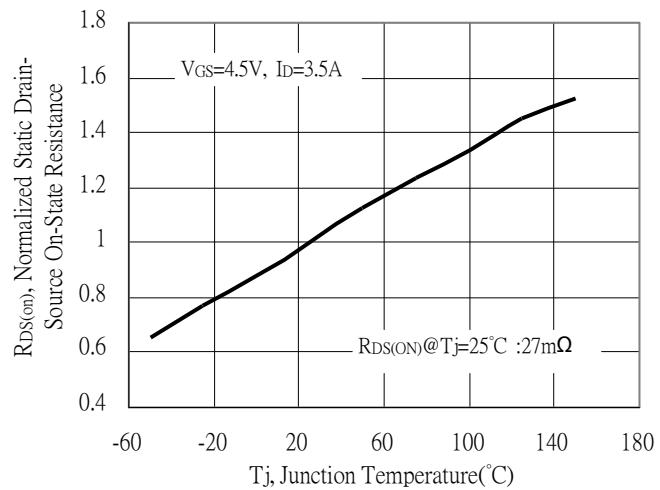
Reverse Drain Current vs Source-Drain Voltage



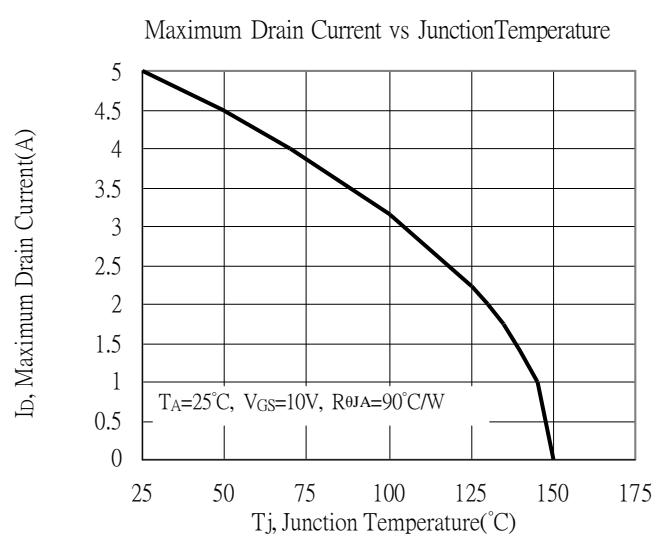
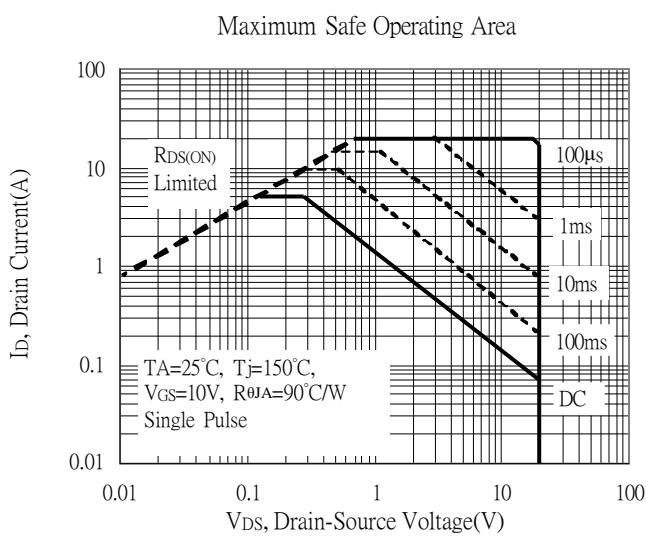
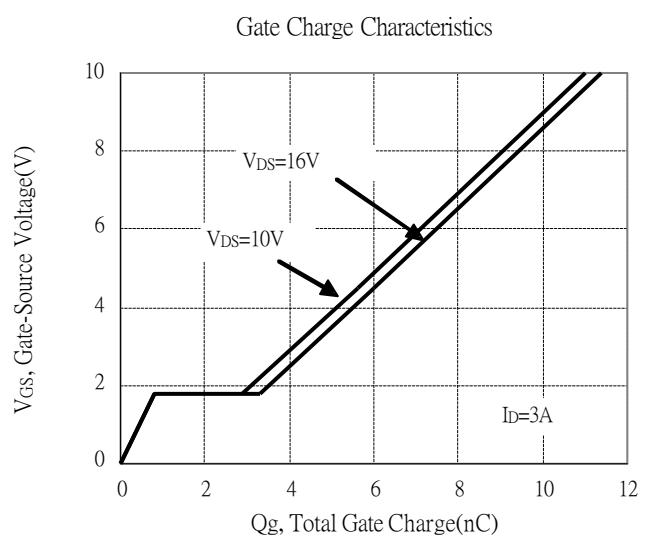
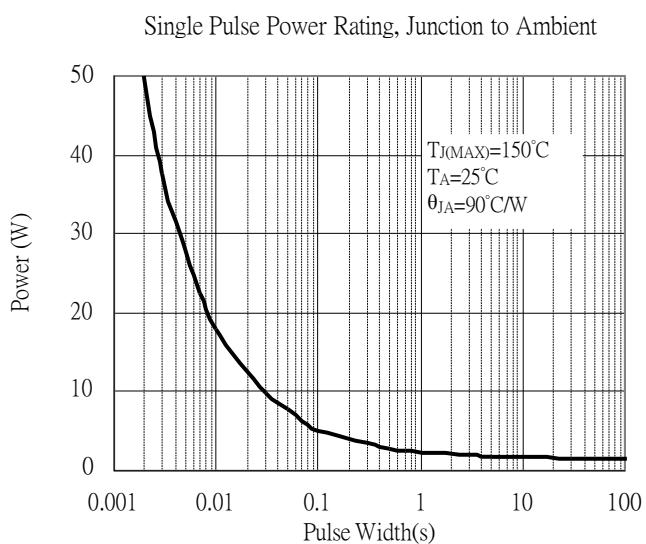
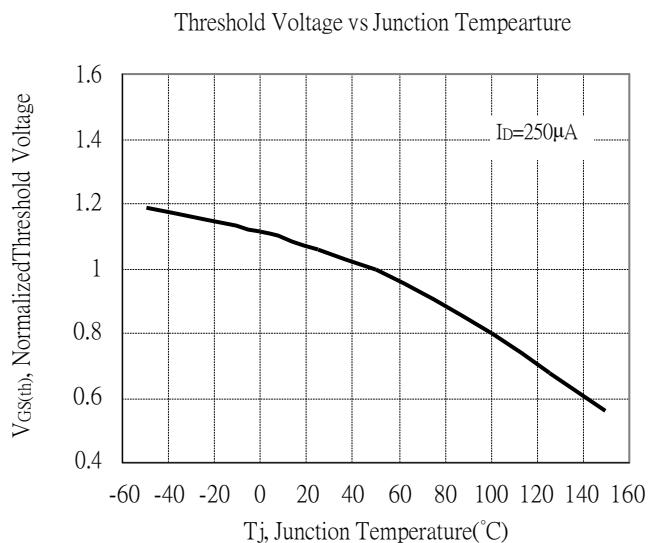
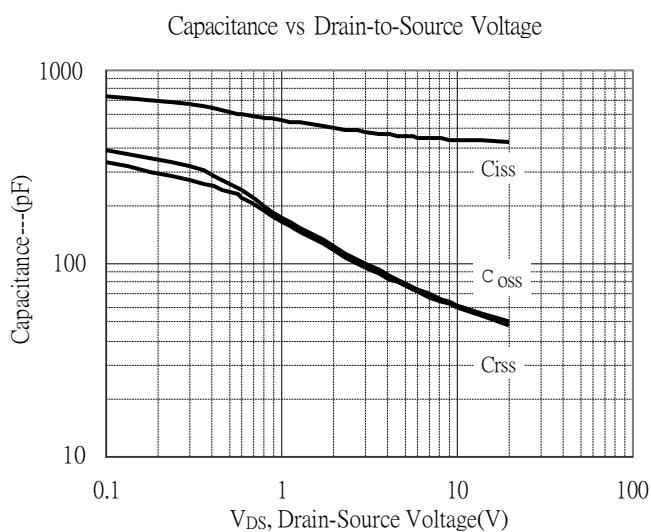
Static Drain-Source On-State Resistance vs Gate-Source Voltage



Drain-Source On-State Resistance vs Junction Temperature

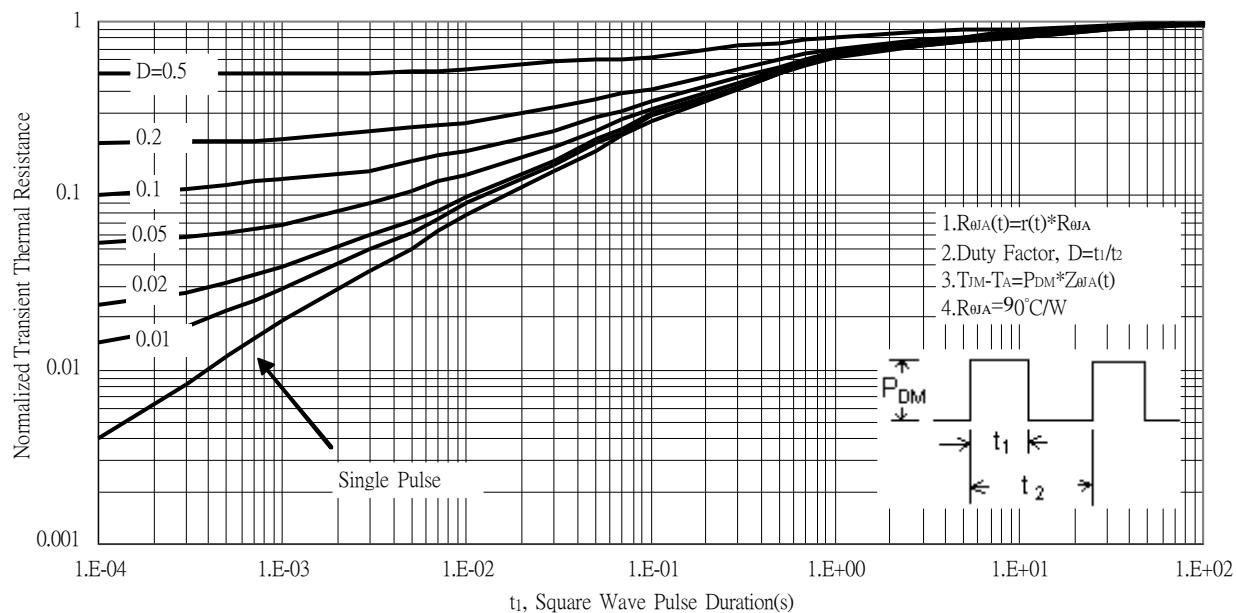


## N-channel Typical Characteristics(Cont.)



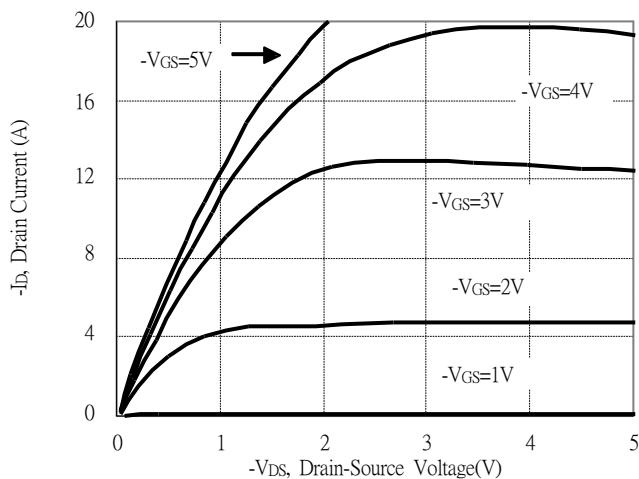
## N-channel Typical Characteristics(Cont.)

Transient Thermal Response Curves

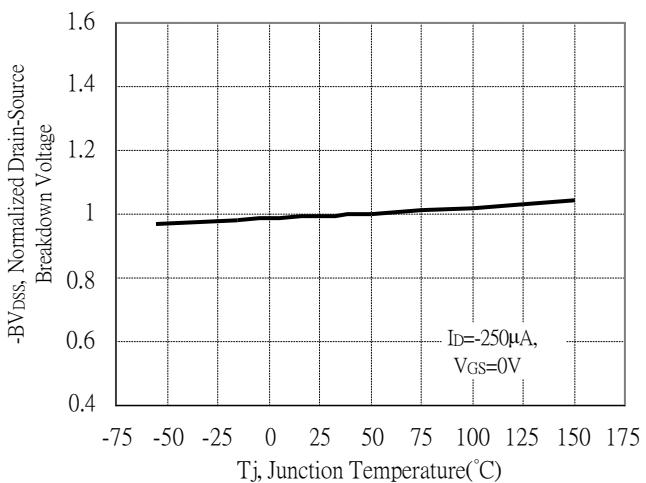


## P-channel Typical Characteristics

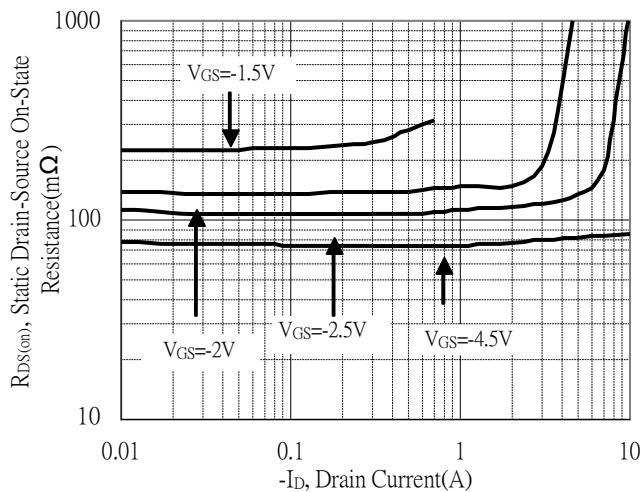
Typical Output Characteristics



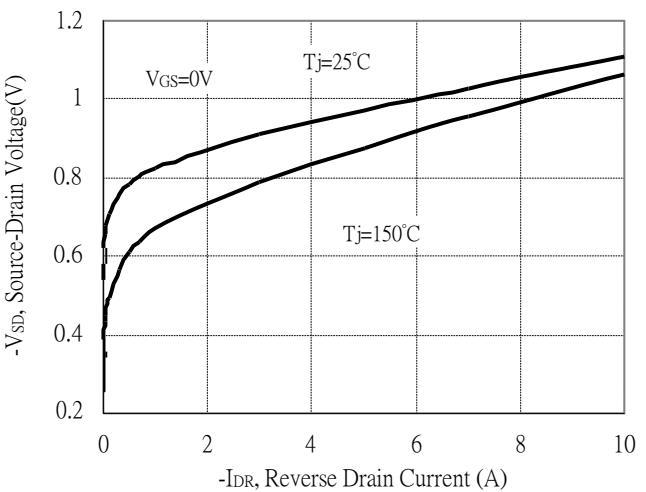
Breakdown Voltage vs Ambient Temperature



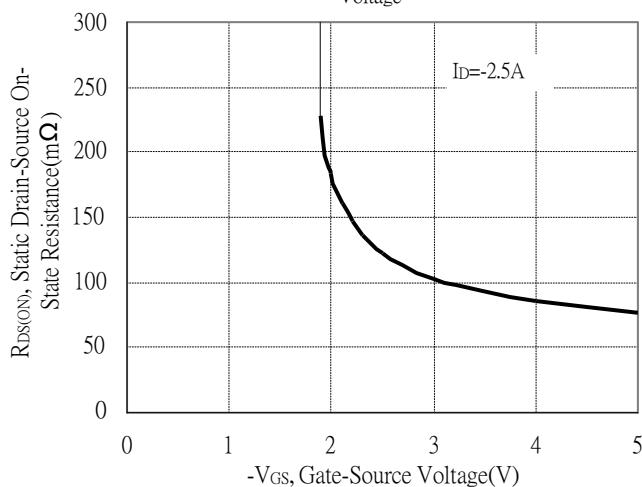
Static Drain-Source On-State resistance vs Drain Current



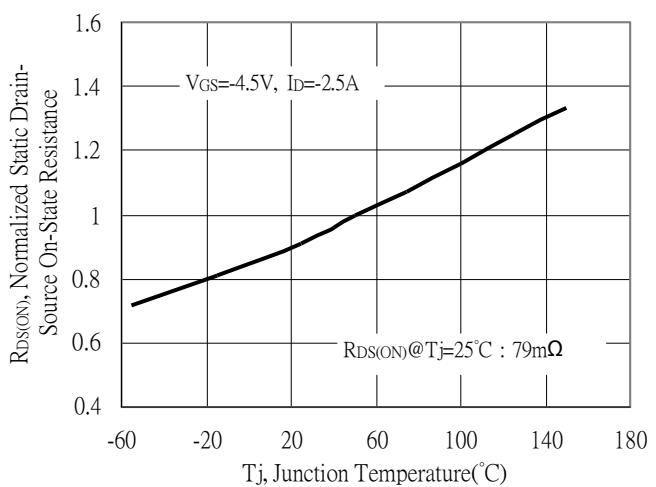
Reverse Drain Current vs Source-Drain Voltage



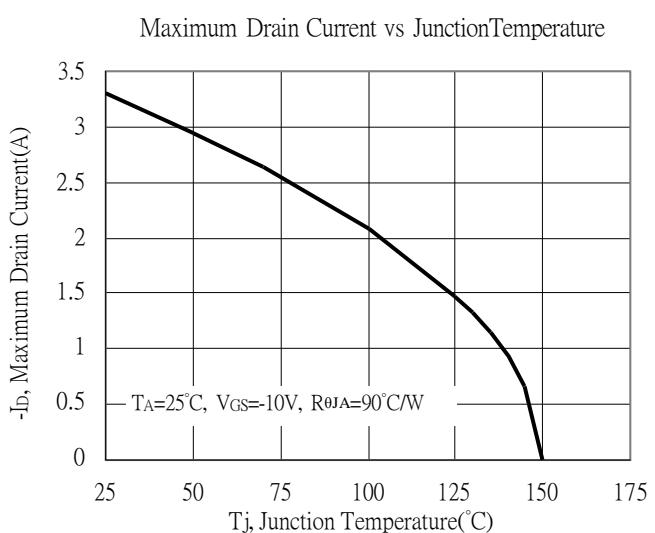
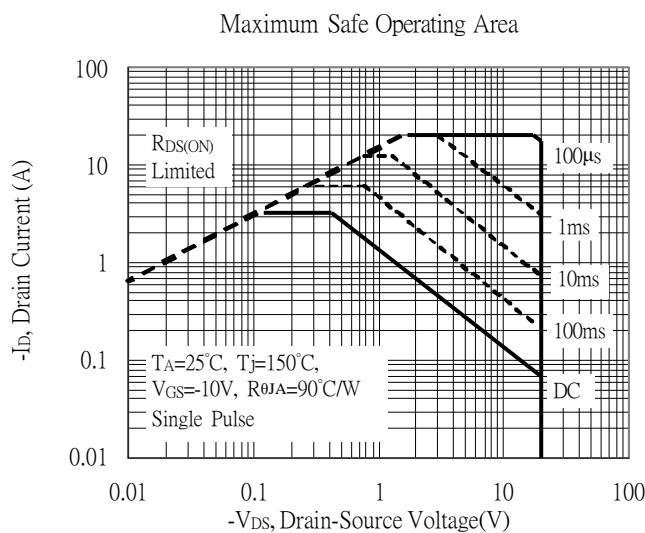
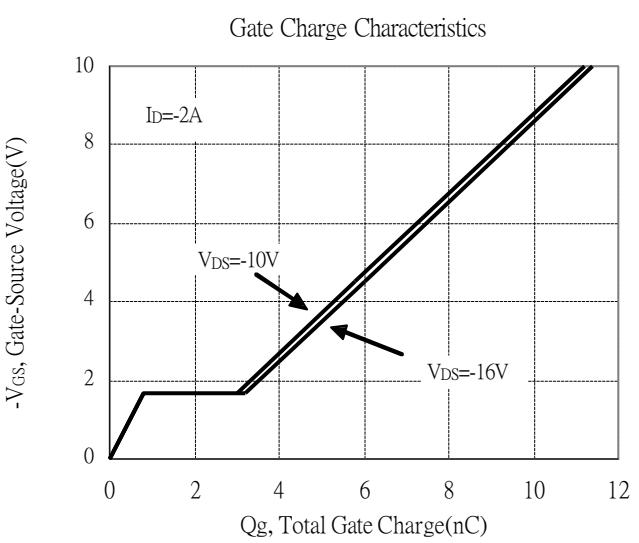
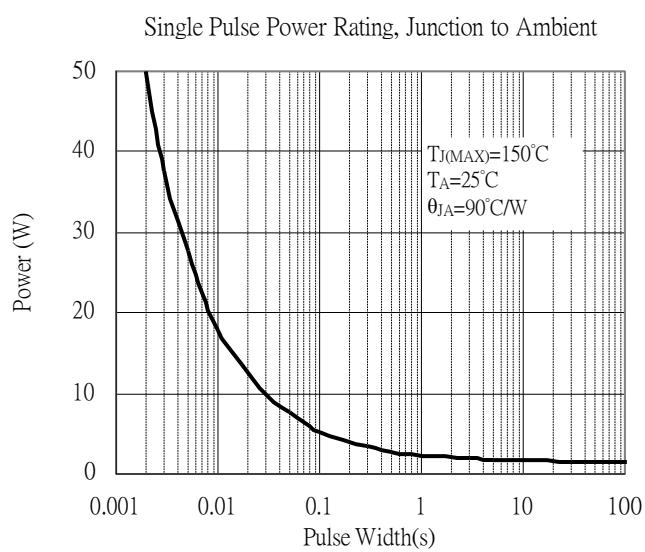
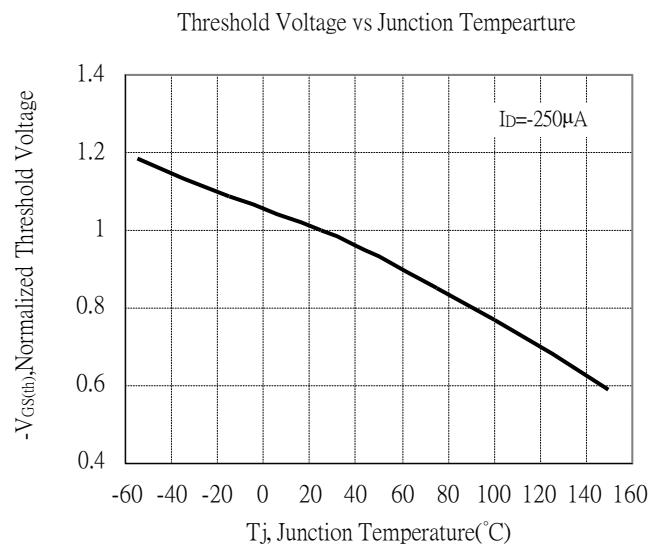
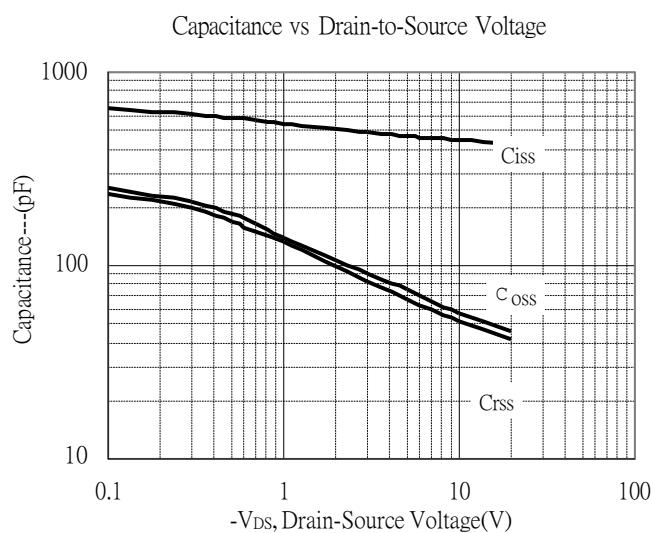
Static Drain-Source On-State Resistance vs Gate-Source Voltage



Drain-Source On-State Resistance vs Junction Temperature

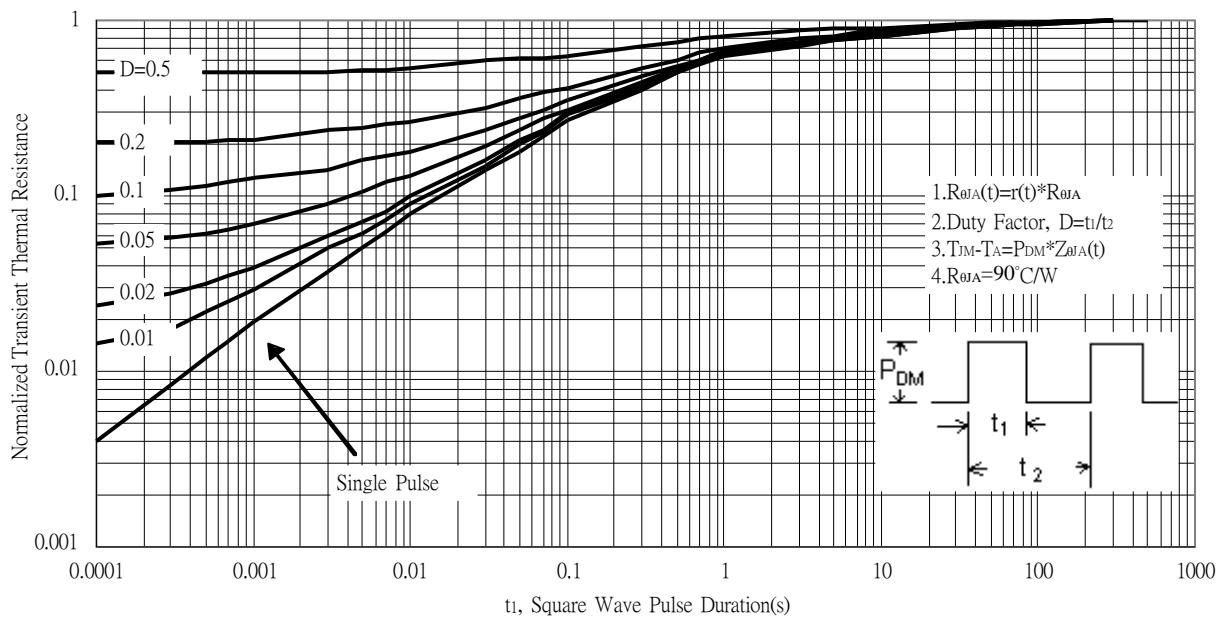


## P-channel Typical Characteristics(Cont.)

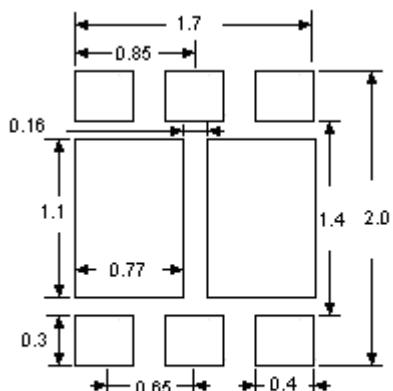


## P-channel Typical Characteristics(Cont.)

Transient Thermal Response Curves



## Recommended Soldering Footprint

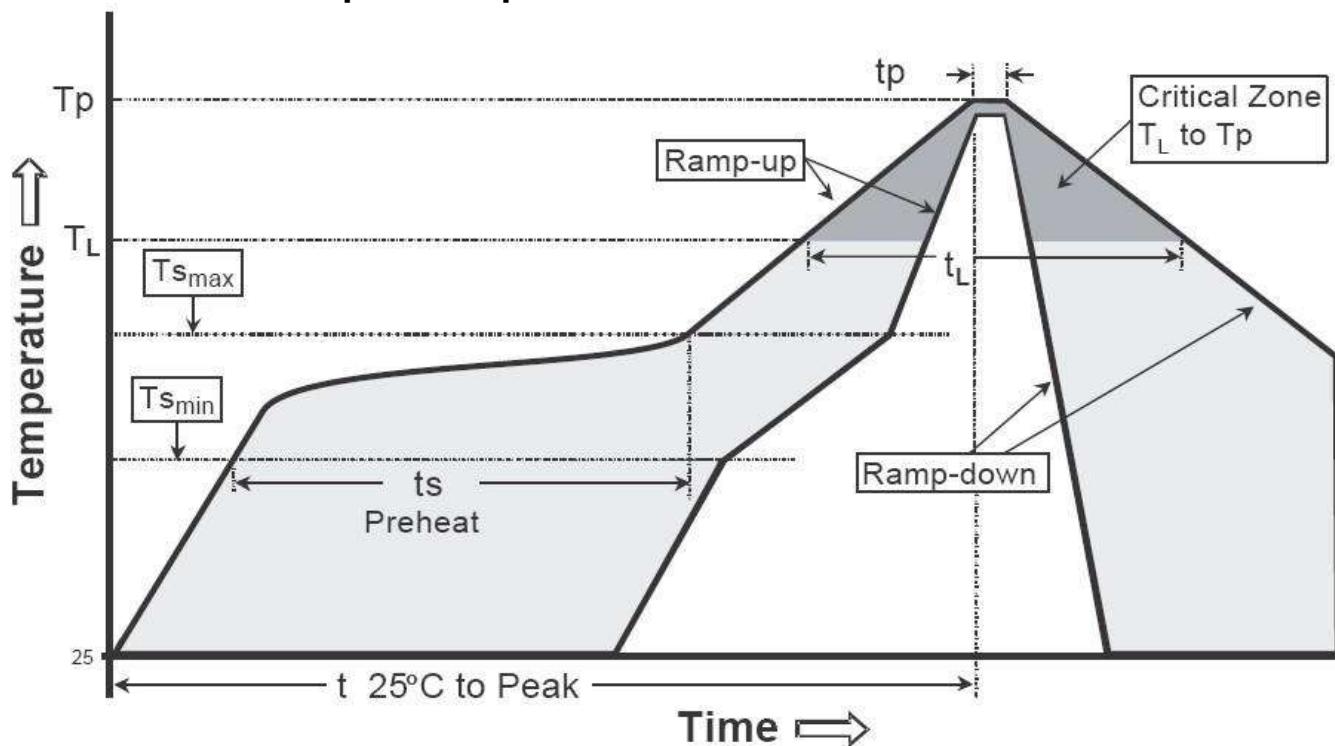


Unit : mm

### Recommended wave soldering condition

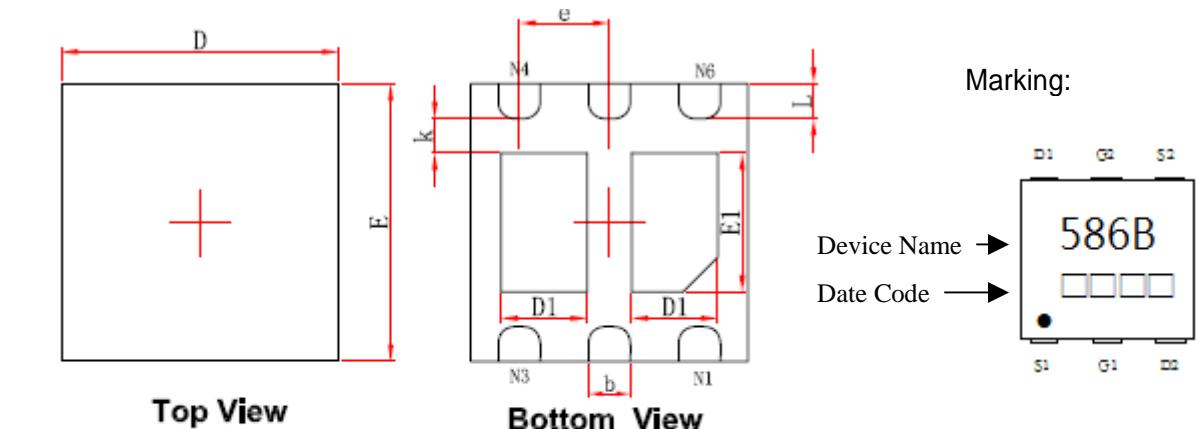
Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

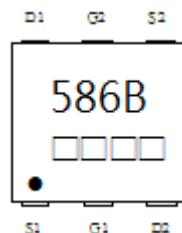
### Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate ( $T_{smax}$ to $T_p$ )	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min( $T_s$ min)	100°C	150°C
-Temperature Max( $T_s$ max)	150°C	200°C
-Time( $t_{s min}$ to $t_{s max}$ )	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature ( $T_L$ )	183°C	217°C
-Time ( $t_L$ )	60-150 seconds	60-150 seconds
Peak Temperature( $T_p$ )	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature( $t_p$ )	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.


**Marking:**

 Device Name →  
 Date Code →

 6-Lead DFN2x2-6L Plastic  
 Surface Mounted Package  
 Code: DFA6

**Style:**

- Pin 1. Source1 (S1)
- Pin 2. Gate 1 (G1)
- Pin 3. Drain2 (D2)
- Pin 4. Source2 (S2)
- Pin 5. Gate2 (G2)
- Pin 6. Drain1 (D1)

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.700	0.900	0.028	0.035	E1	0.900	1.100	0.035	0.043
A1	0.000	0.050	0.000	0.002	k	0.200	-	0.008	-
A3	0.203	REF	0.008	REF	b	0.250	0.350	0.010	0.014
D	1.950	2.050	0.077	0.081	e	0.650	TYP	0.026	TYP
E	1.950	2.050	0.077	0.081	L	0.200	0.300	0.008	0.012
D1	0.570	0.770	0.022	0.030					