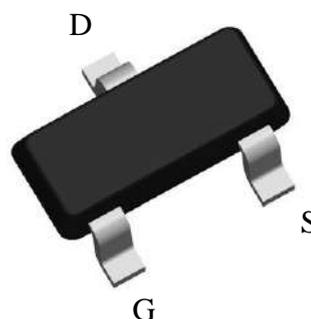


## P-Channel Enhancement Mode MOSFET

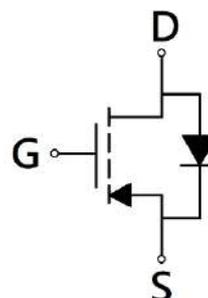
### Features:

- Low On Resistance
- Low Gate Charge
- Fast Switching Characteristic

SOT-23



$BV_{DSS}$	-40V
$I_D @ V_{GS} = -10V, T_A = 25^\circ C$	-3.5A
$R_{DS(ON) \text{ typ. } @ V_{GS} = -10V, I_D = -3A}$	43m $\Omega$
$R_{DS(ON) \text{ typ. } @ V_{GS} = -4.5V, I_D = -2A}$	54m $\Omega$



G : Gate S : Source D : Drain

### Ordering Information

Device	Package	Shipping
KWB040P04	SOT-23 (Pb-free lead plating and halogen-free package)	3000 pcs / Tape & Reel

### Absolute Maximum Ratings (T<sub>A</sub>=25°C)

Parameter	Symbol	Limits	Unit	
Drain-Source Voltage	V <sub>DS</sub>	-40	V	
Gate-Source Voltage	V <sub>GS</sub>	±20		
Continuous Drain Current @ V <sub>GS</sub> =-10V, T <sub>A</sub> =25°C	I <sub>D</sub>	-3.5	A	
Continuous Drain Current @ V <sub>GS</sub> =-10V, T <sub>A</sub> =70°C		-2.8		
Pulsed Drain Current	I <sub>DM</sub>	-14		
Continuous Body Diode Forward Current @ T <sub>A</sub> =25°C	I <sub>S</sub>	-1		
Total Power Dissipation	P <sub>D</sub>	T <sub>A</sub> =25°C	1.2	W
		T <sub>A</sub> =70°C	0.8	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55~+150	°C

### Thermal Data

Parameter	Symbol	Steady State	Unit
Thermal Resistance, Junction-to-ambient	R <sub>θJA</sub>	100	°C/W

Note:

- \*a. The value of R<sub>θJA</sub> is measured with the device mounted on 1 in<sup>2</sup>FR -4 board with 2 oz. copper, in a still air environment with T<sub>A</sub>=25°C. The power dissipation P<sub>D</sub> is based on R<sub>θJA</sub> and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
- \*b. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C. Ratings are based on low frequency and low duty cycles to keep initial T<sub>J</sub>=25°C.

**Electrical Characteristics (T<sub>A</sub>=25°C, unless otherwise specified)**

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
BV <sub>DSS</sub>	-40	-	-	V	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA
V <sub>GS(th)</sub>	-1	-	-2.5		V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA
G <sub>FS</sub>	-	7.4	-	S	V <sub>DS</sub> =-10V, I <sub>D</sub> =-3A
I <sub>GSS</sub>	-	-	±100	nA	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V
I <sub>DSS</sub>	-	-	-1	μA	V <sub>DS</sub> =-32V, V <sub>GS</sub> =0V
R <sub>DS(ON)</sub>	-	43	56	mΩ	V <sub>GS</sub> =-10V, I <sub>D</sub> =-3A
	-	54	75		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-2A
<b>Dynamic</b>					
C <sub>iss</sub>	-	930	-	pF	V <sub>DS</sub> =-20V, V <sub>GS</sub> =0V, f=1MHz
C <sub>oss</sub>	-	80	-		
C <sub>rss</sub>	-	60	-		
R <sub>g</sub>	-	17	-	Ω	f=1MHz
Q <sub>g</sub> *1, 2	-	19	-	nC	V <sub>DS</sub> =-20V, I <sub>D</sub> =-3A, V <sub>GS</sub> =-10V
Q <sub>gs</sub> *1, 2	-	2.8	-		
Q <sub>gd</sub> *1, 2	-	3.6	-		
t <sub>d(ON)</sub> *1, 2	-	6.8	-	ns	V <sub>DS</sub> =-20V, I <sub>D</sub> =-3A, V <sub>GS</sub> =-10V, R <sub>GS</sub> =1Ω
t <sub>r</sub> *1, 2	-	19	-		
t <sub>d(OFF)</sub> *1, 2	-	65	-		
t <sub>f</sub> *1, 2	-	31	-		
<b>Source-Drain Diode</b>					
V <sub>SD</sub> *1	-	-0.8	-1.2	V	I <sub>S</sub> =-3A, V <sub>GS</sub> =0V
t <sub>rr</sub>	-	9.5	-	ns	I <sub>F</sub> =-3A, dI <sub>F</sub> /dt=100A/μs
Q <sub>rr</sub>	-	5	-	nC	

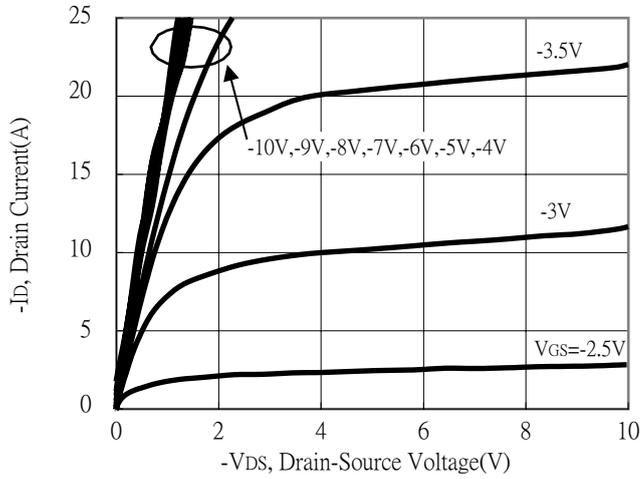
Note:

\*1. Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

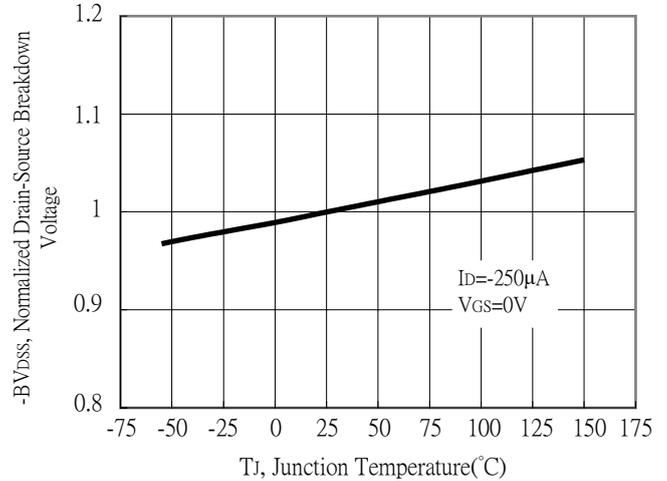
\*2. Independent of operating temperature

## Typical Characteristics

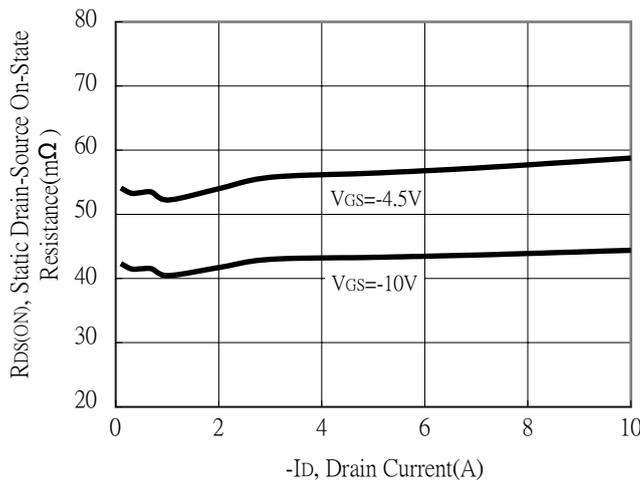
Typical Output Characteristics



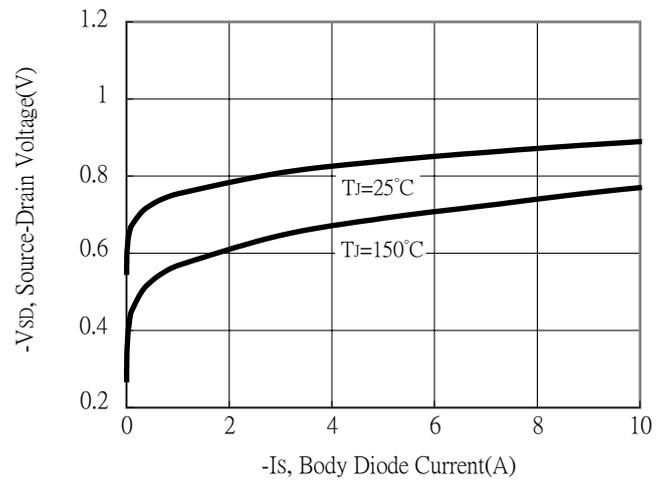
Breakdown Voltage vs Ambient Temperature



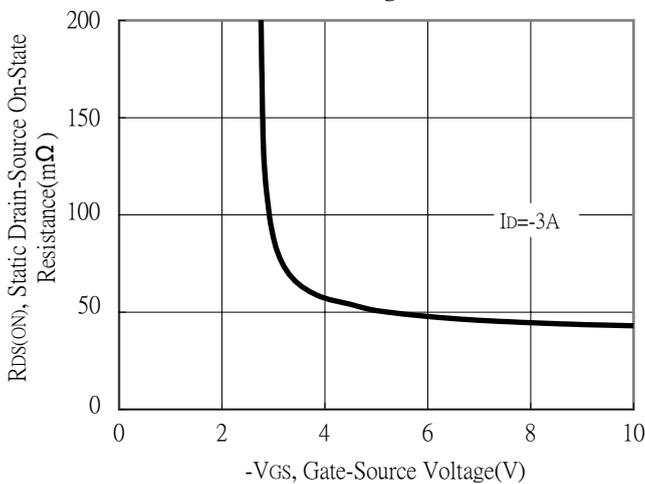
Static Drain-Source On-State resistance vs Drain Current



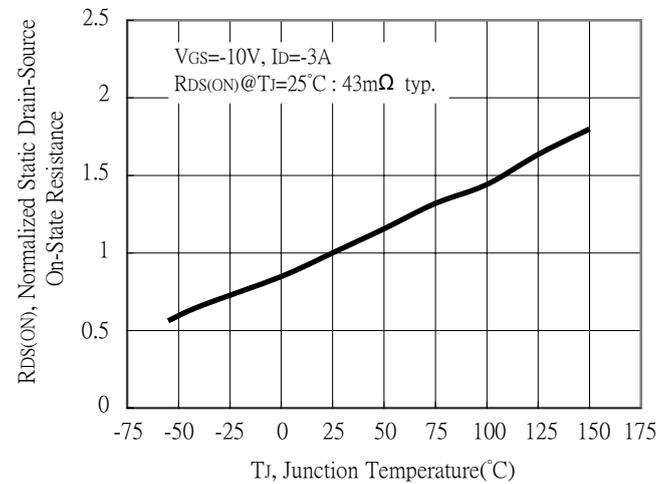
Body Diode Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

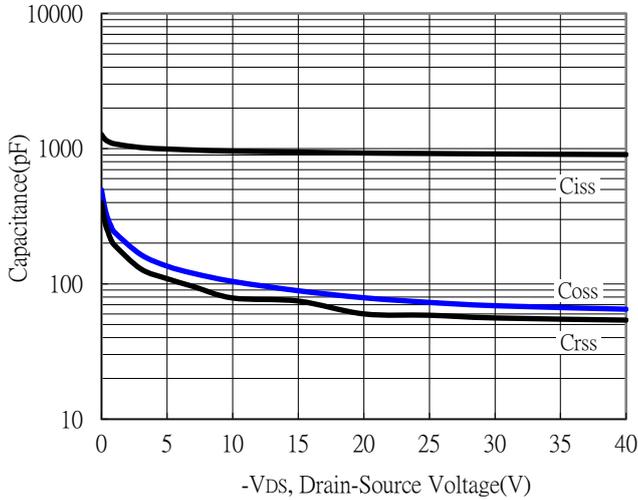


Drain-Source On-State Resistance vs Junction Temperature

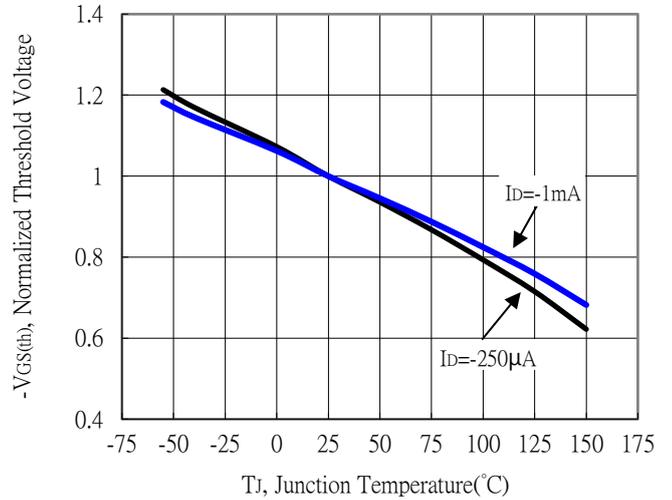


**Typical Characteristics (Cont.)**

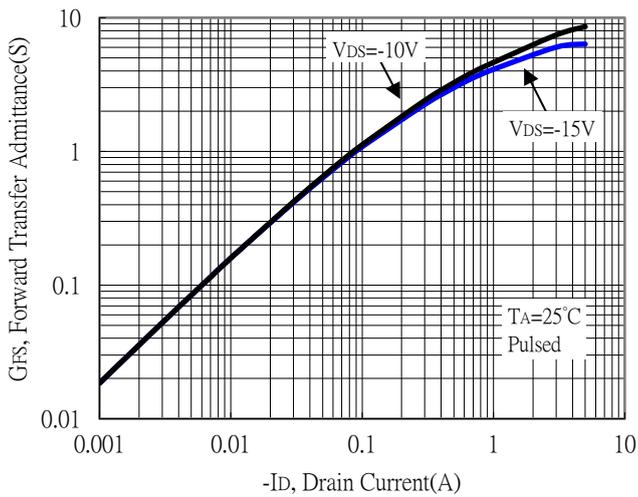
Capacitance vs Drain-to-Source Voltage



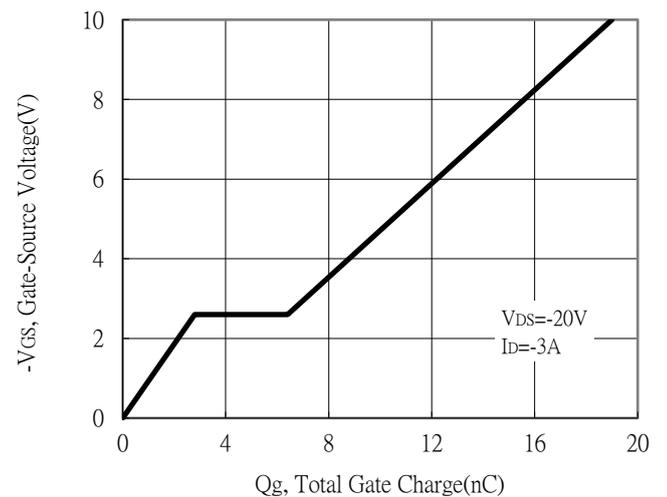
Threshold Voltage vs Junction Temperature



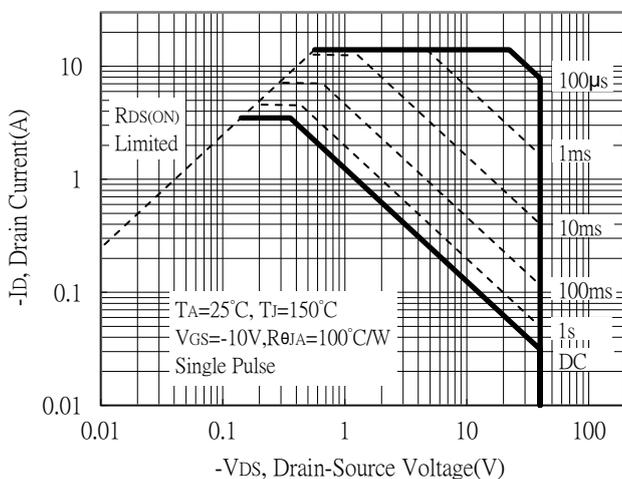
Forward Transfer Admittance vs Drain Current



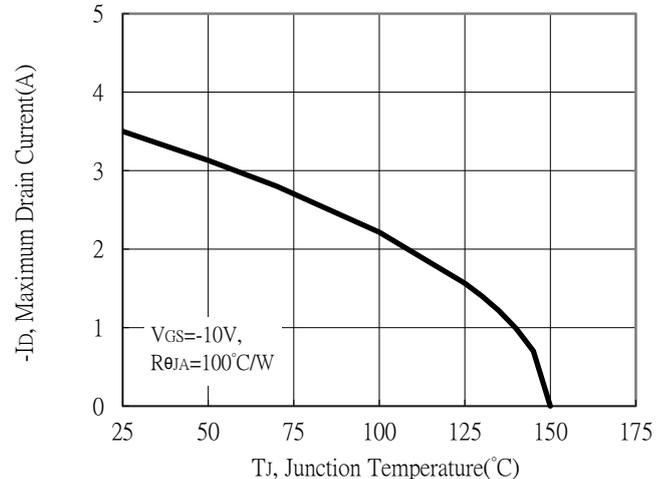
Gate Charge Characteristics



Maximum Safe Operating Area

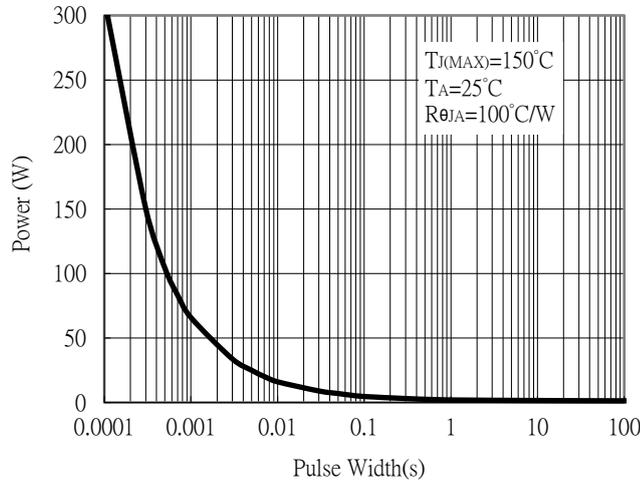


Maximum Drain Current vs Junction Temperature

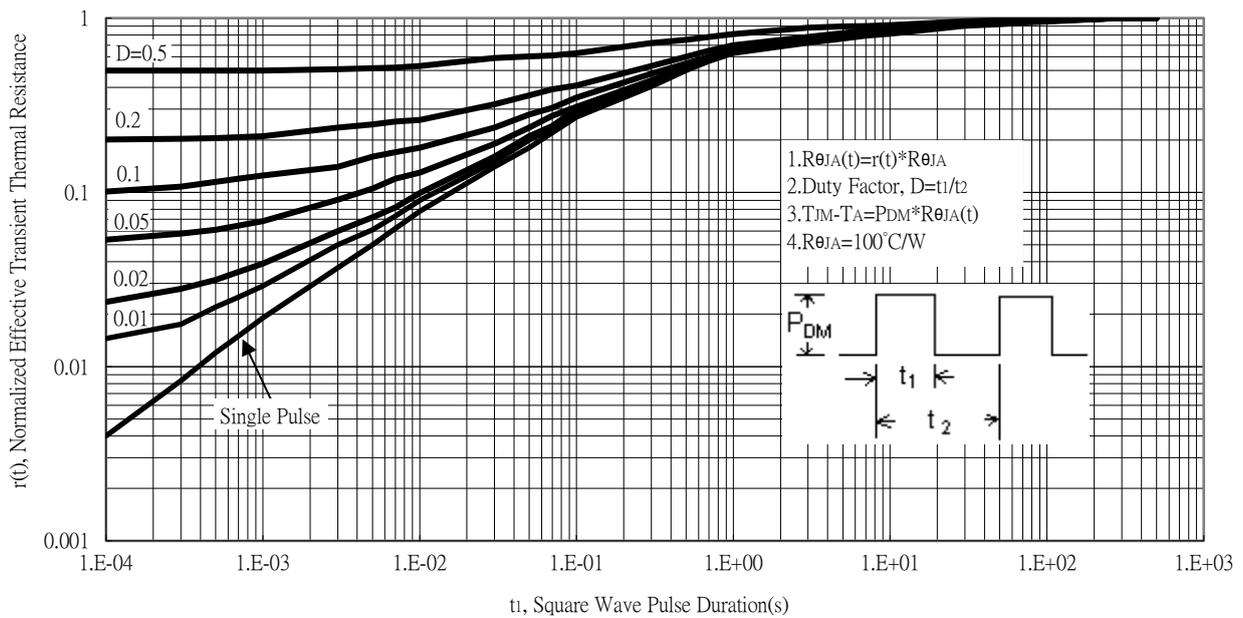


### Typical Characteristics (Cont.)

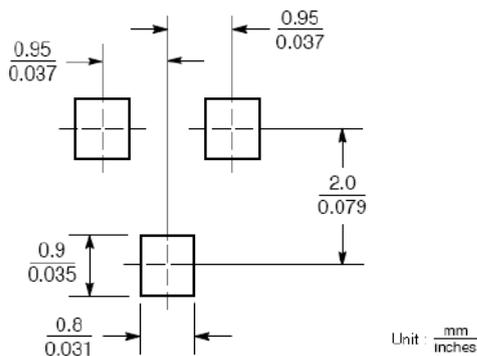
Single Pulse Power Rating, Junction to Ambient



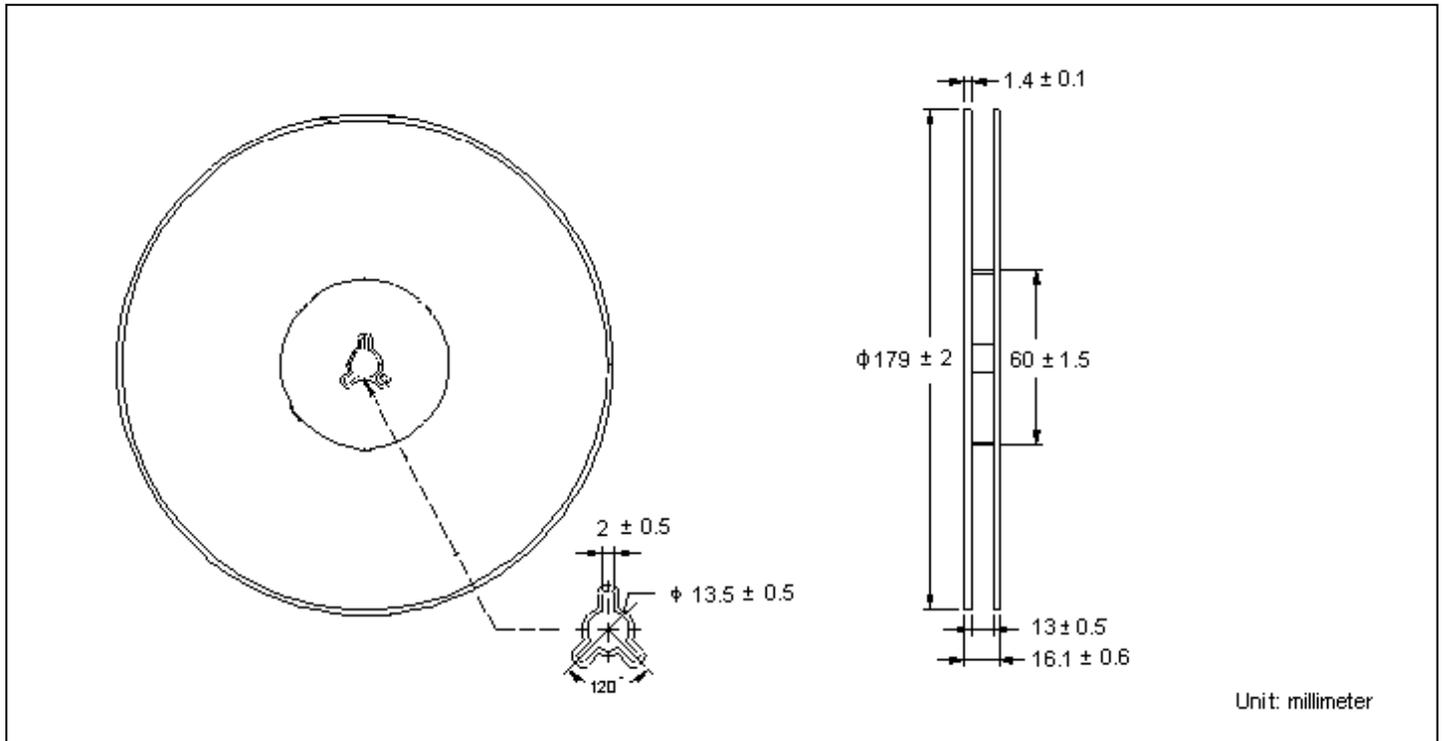
Transient Thermal Response Curves



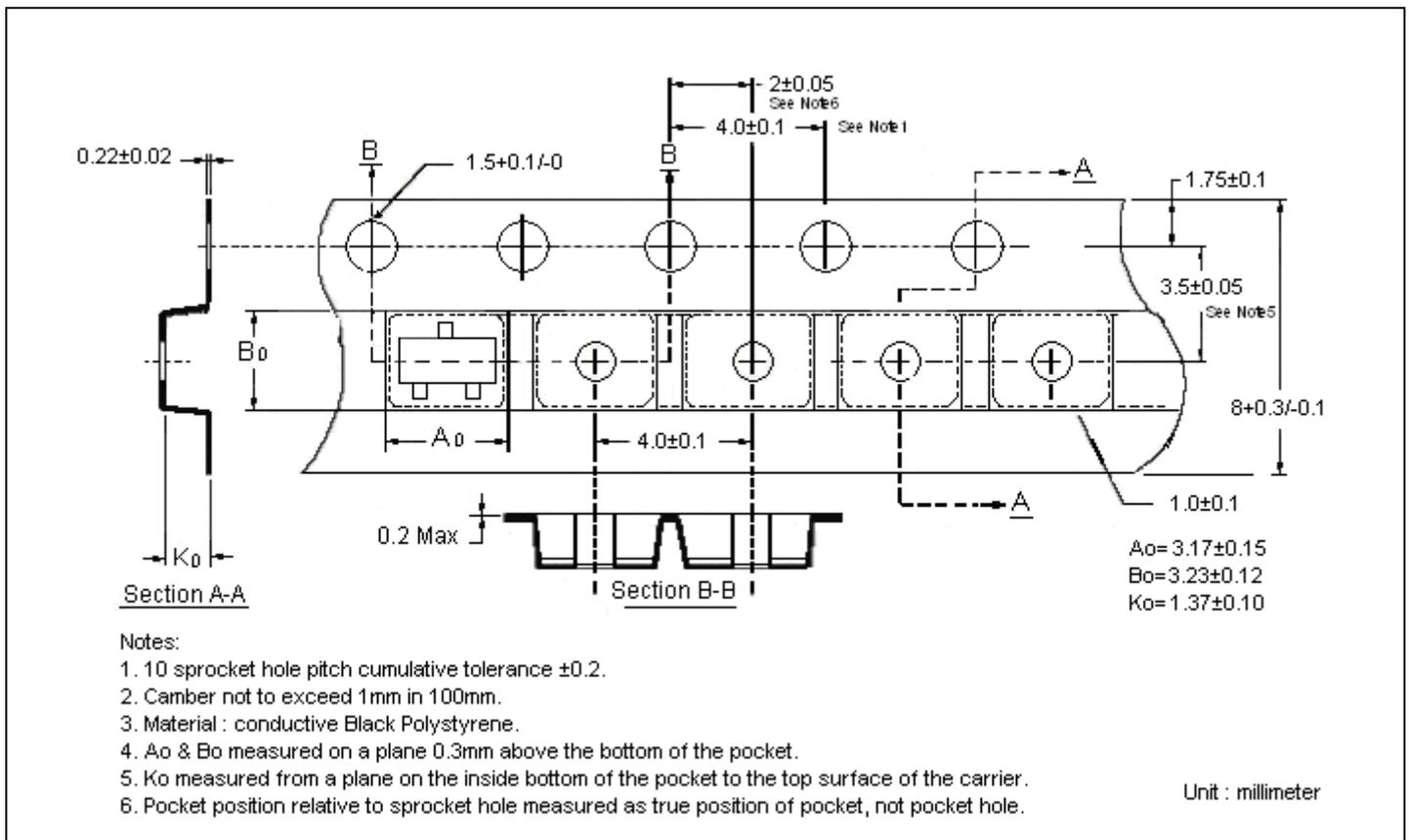
### Recommended Soldering Footprint



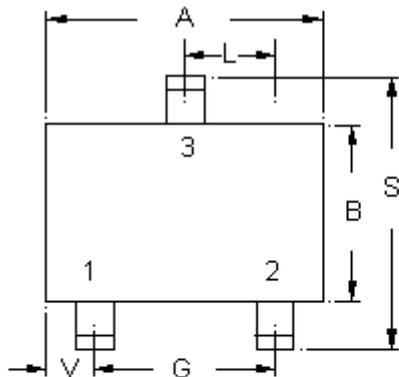
## Reel Dimension



## Carrier Tape Dimension

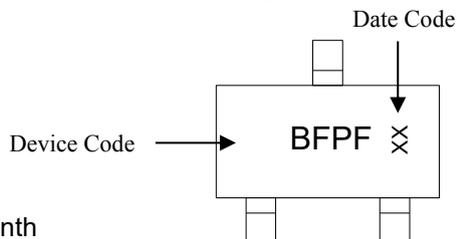


**SOT-23 Dimension**

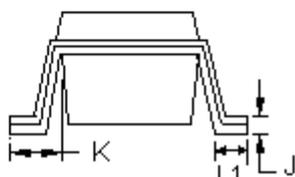
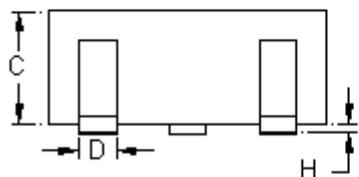


Date Code: Year+Month  
 Year: 3→2003, 4→2004  
 Month: 1→1, 2→2, . . .  
 9→9, A→10, B→11, C→12

Marking:



3-Lead SOT-23 Plastic  
 Surface Mounted Package



Style: Pin 1.Gate 2.Source 3.Drain

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.1102	0.1204	2.80	3.04	J	0.0032	0.0079	0.08	0.20
B	0.0472	0.0669	1.20	1.70	K	0.0118	0.0266	0.30	0.67
C	0.0335	0.0512	0.89	1.30	L	0.0335	0.0453	0.85	1.15
D	0.0118	0.0197	0.30	0.50	S	0.0830	0.1161	2.10	2.95
G	0.0669	0.0910	1.70	2.30	V	0.0098	0.0256	0.25	0.65
H	0.0000	0.0040	0.00	0.10	L1	0.0118	0.0197	0.30	0.50