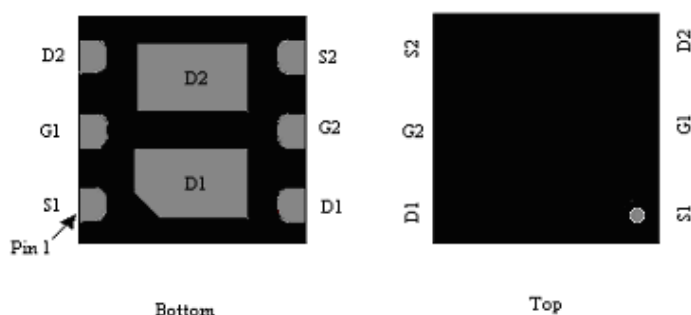


Dual P-Channel Enhancement Mode MOSFET

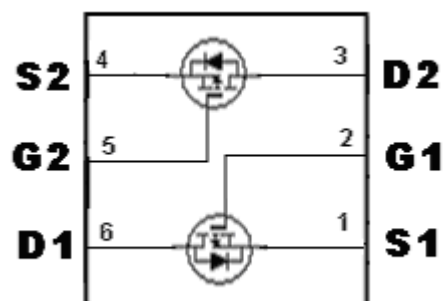
Features:

- Simple drive requirement
- Low gate charge
- Low on-resistance
- Fast switching speed
- Pb-free lead plating and halogen-free package

DFN2x2-6L



BV _{DSS}	-14V
I _D @V _{GS} =-4.5 V, T _A =25°C	-4.3A
R _{DS(on)} @V _{GS} =-4.5V, I _D =-3.6A	39mΩ (TYP.)
R _{DS(on)} @V _{GS} =-2.5V, I _D =-3.2A	55mΩ (TYP.)
R _{DS(on)} @V _{GS} =-1.8V, I _D =-1.0A	69mΩ (TYP.)



G : Gate S : Source D : Drain

Ordering Information

Device	Package	Shipping
KWA050B01	DFN2x2-6L (Pb-free lead plating and halogen-free package)	3000 pcs / Tape & Reel

Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	-14	V
Gate-Source Voltage	V _{GS}	±8	
Continuous Drain Current @T _A =25 °C, V _{GS} =-4.5V (Note 1)	I _D	-4.3	A
Continuous Drain Current @T _A =70 °C, V _{GS} =-4.5V (Note 1)		-3.4	
Pulsed Drain Current (Note 2)	I _{DM}	-26	
Total Power Dissipation (Note 1)	P _D	1.38	W
Linear Derating Factor		0.01	W / °C
Operating Junction and Storage Temperature	T _j , T _{stg}	-55~+150	°C

Note : 1.Surface mounted on 1 in² copper pad of FR-4 board, t≤5 sec
2.Pulse width limited by maximum junction temperature

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	R _{θJC}	75	°C/W
Thermal Resistance, Junction-to-ambient, max	R _{θJA}	90 (Note)	

Note :.Surface mounted on 1 in² copper pad of FR-4 board, t≤5 sec; 195°C/W when mounted on minimum copper pad

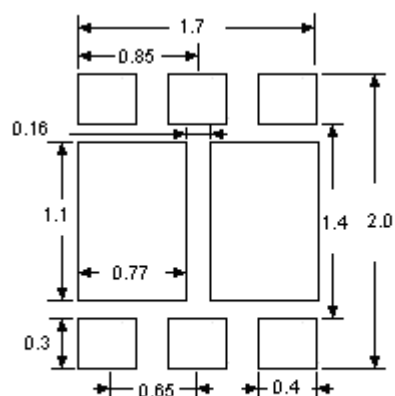
Electrical Characteristics (Tj=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	-14	-	-	V	V _{GS} =0V, I _D =-250μA
ΔBV _{DSS} /ΔT _j	-	-5	-	mV/°C	Reference to 25°C, I _D =-250μA
V _{GS(th)}	-	-0.8	-1.2	V	V _{DS} =V _{GS} , I _D =-250μA
I _{GSS}	-	-	±100	nA	V _{GS} =±8V, V _{DS} =0V
I _{DSS}	-	-	-1	μA	V _{DS} =-12V, V _{GS} =0V
	-	-	-25		V _{DS} =-12V, V _{GS} =0V, T _j =70°C
*R _{DS(ON)}	-	39	55	mΩ	V _{GS} =-4.5V, I _D =-3.6A
	-	55	77		V _{GS} =-2.5V, I _D =-3.2A
	-	69	-		V _{GS} =-1.8V, I _D =-1.0A
*G _{FS}	-	7.4	-	S	V _{DS} =-5V, I _D =-3A
Dynamic					
C _{iss}	-	678	-	pF	V _{DS} =-10V, V _{GS} =0V, f=1MHz
C _{oss}	-	130	-		
C _{rss}	-	123	-		
*t _{d(ON)}	-	11.6	-	ns	V _{DS} =-10V, I _D =-1A, V _{GS} =-4.5V, R _G =6Ω
*t _r	-	22.8	-		
*t _{d(OFF)}	-	56	-		
*t _f	-	33.2	-		

*Qg	-	8.8	-	nC	V _{DS} =-10V, I _D =-1.5A, V _{GS} =-4.5V
*Qgs	-	0.9	-		
*Qgd	-	2.7	-		
Rg	-	15	-	^	f=1MHz
Source-Drain Diode					
*V _{SD}	-	-0.9	-1.2	V	V _{GS} =0V, I _S =-3.4A
*trr	-	24.7	-	ns	I _F =-3.4A, V _{GS} =0V, dI _F /dt=100A/μs
*Qrr	-	4.8	-	nC	

*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

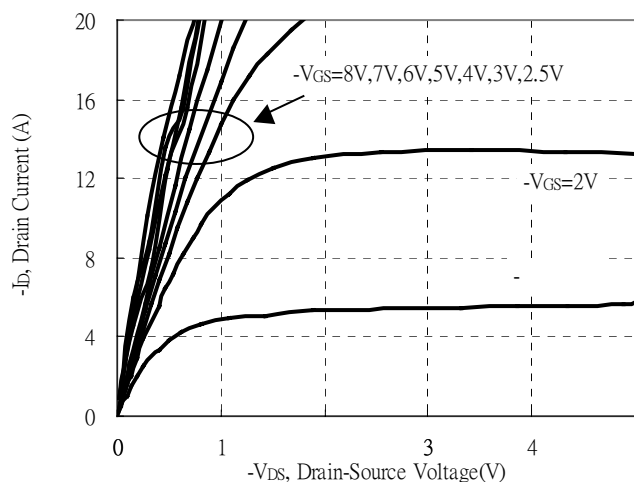
Recommended Soldering Footprint



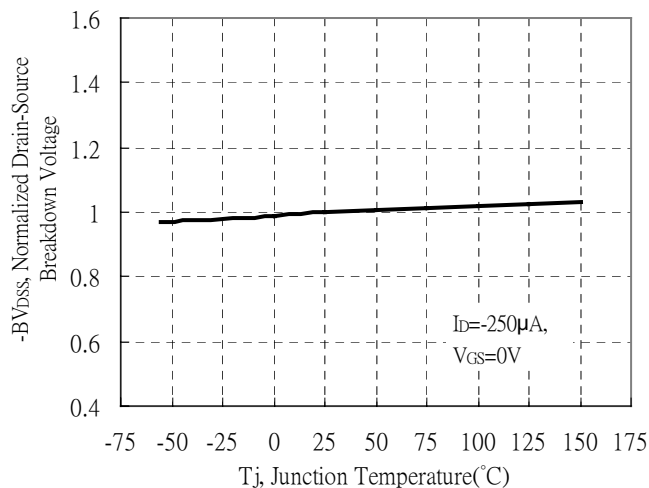
Unit : mm

Typical Characteristics

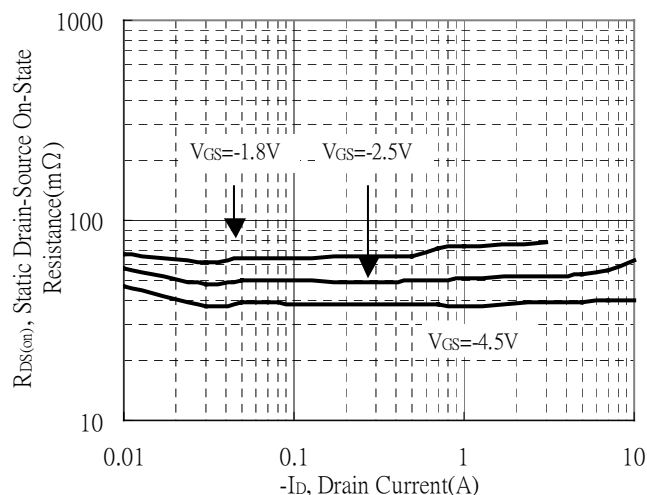
Typical Output Characteristics



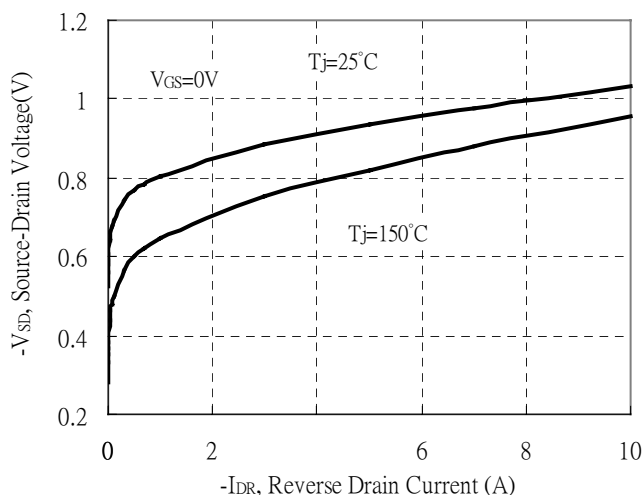
Breakdown Voltage vs Ambient Temperature



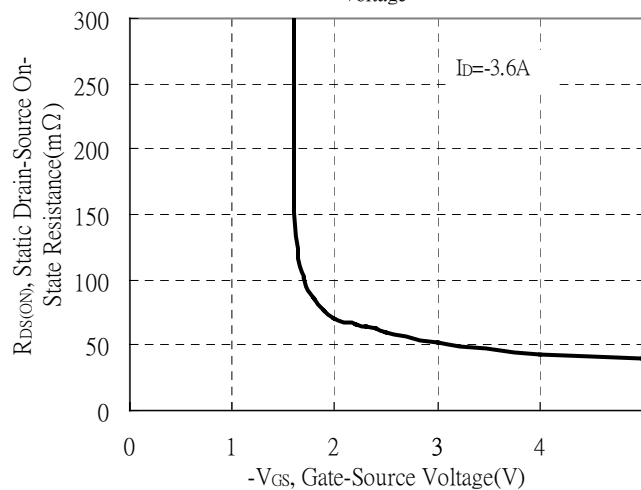
Static Drain-Source On-State resistance vs Drain Current



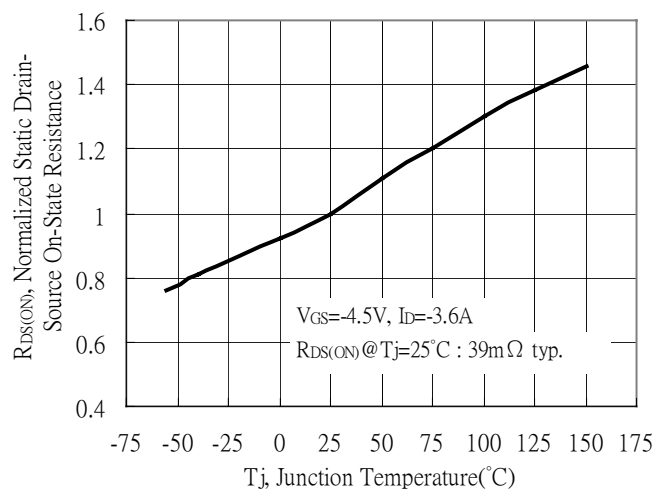
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

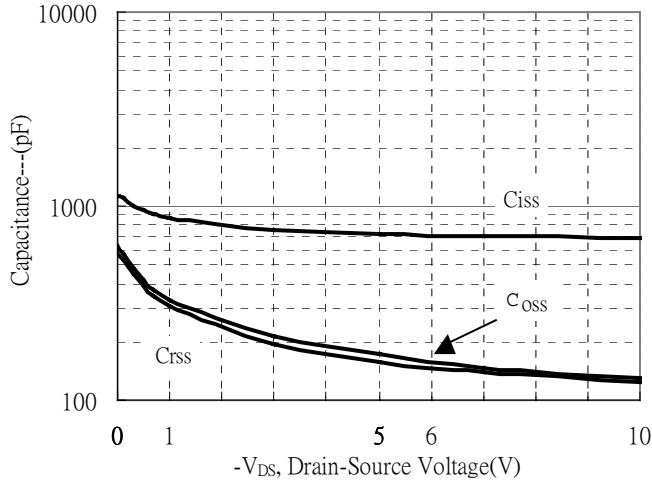


Drain-Source On-State Resistance vs Junction Temperature

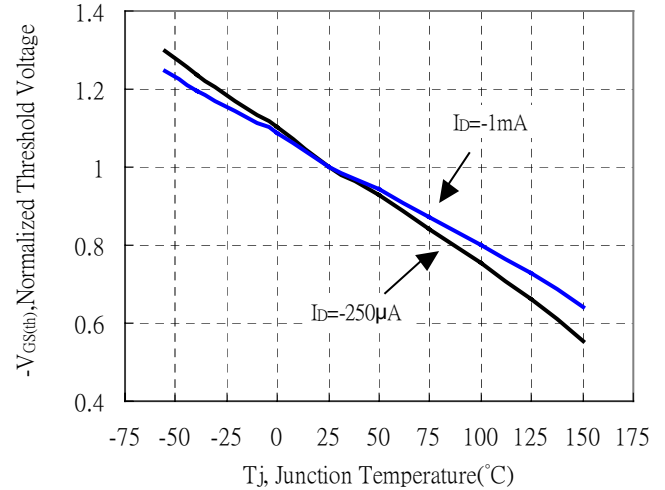


Typical Characteristics(Cont.)

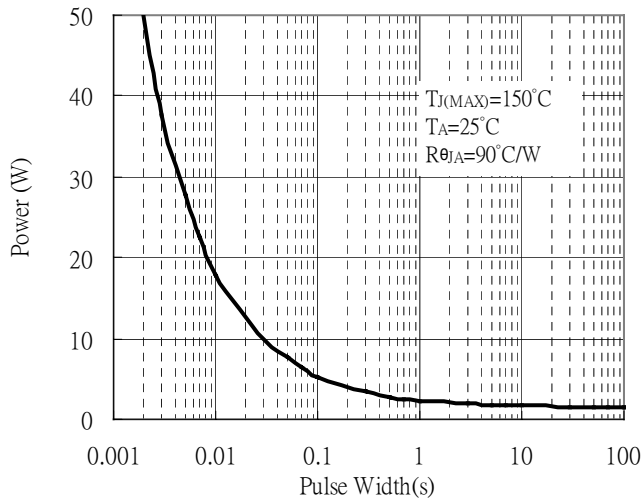
Capacitance vs Drain-to-Source Voltage



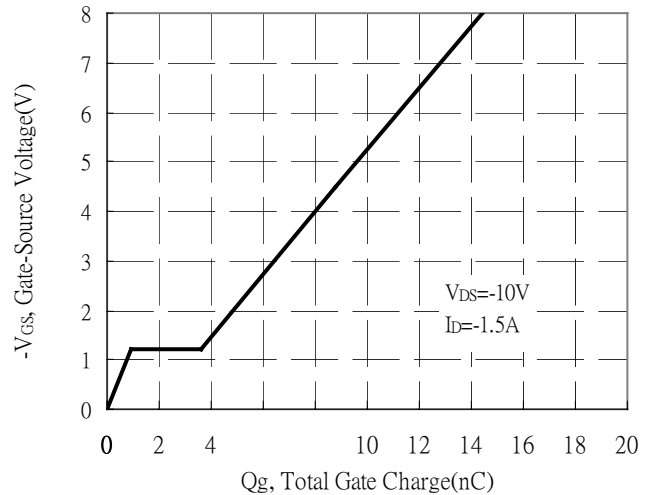
Threshold Voltage vs Junction Temperature



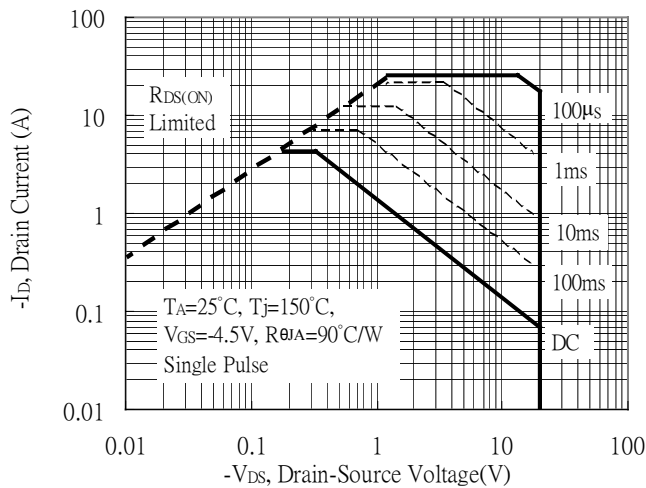
Single Pulse Power Rating, Junction to Ambient



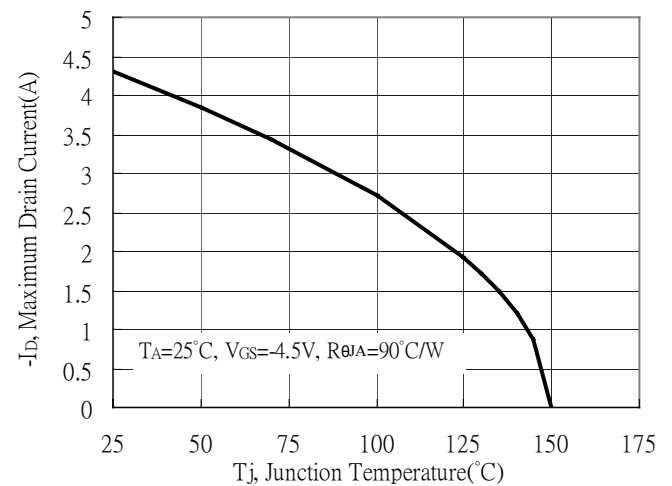
Gate Charge Characteristics



Maximum Safe Operating Area

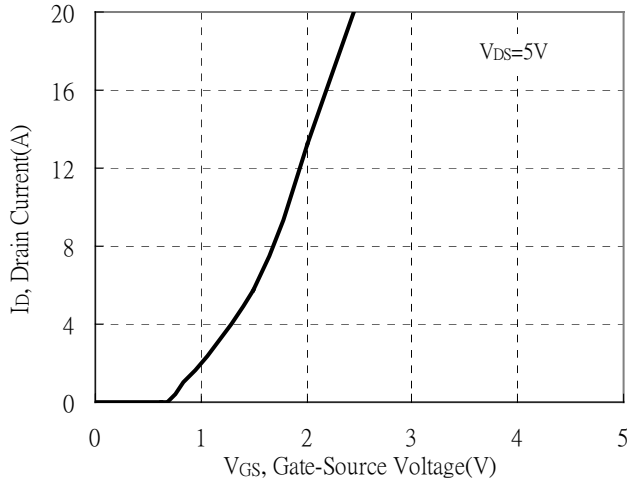


Maximum Drain Current vs Junction Temperature

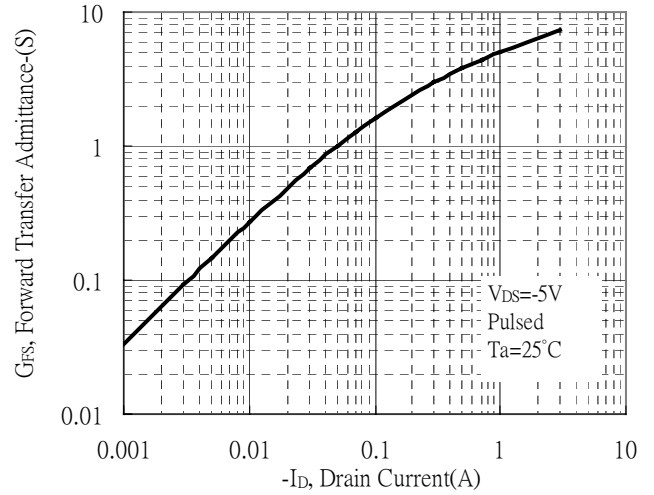


Typical Characteristics(Cont.)

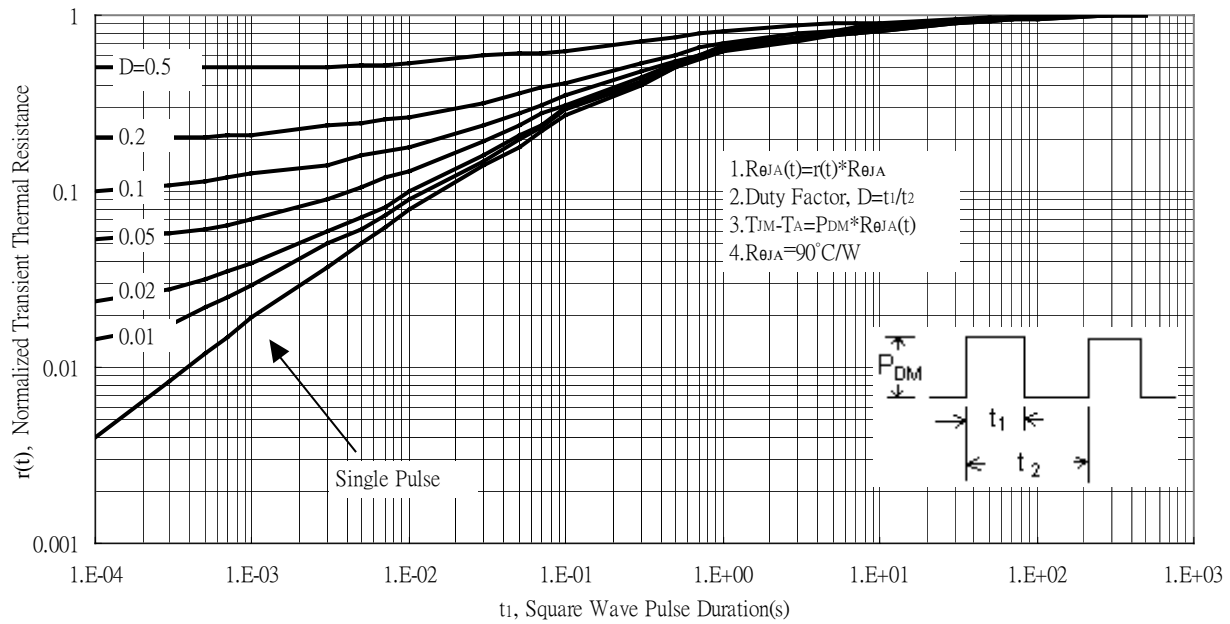
Typical Transfer Characteristics



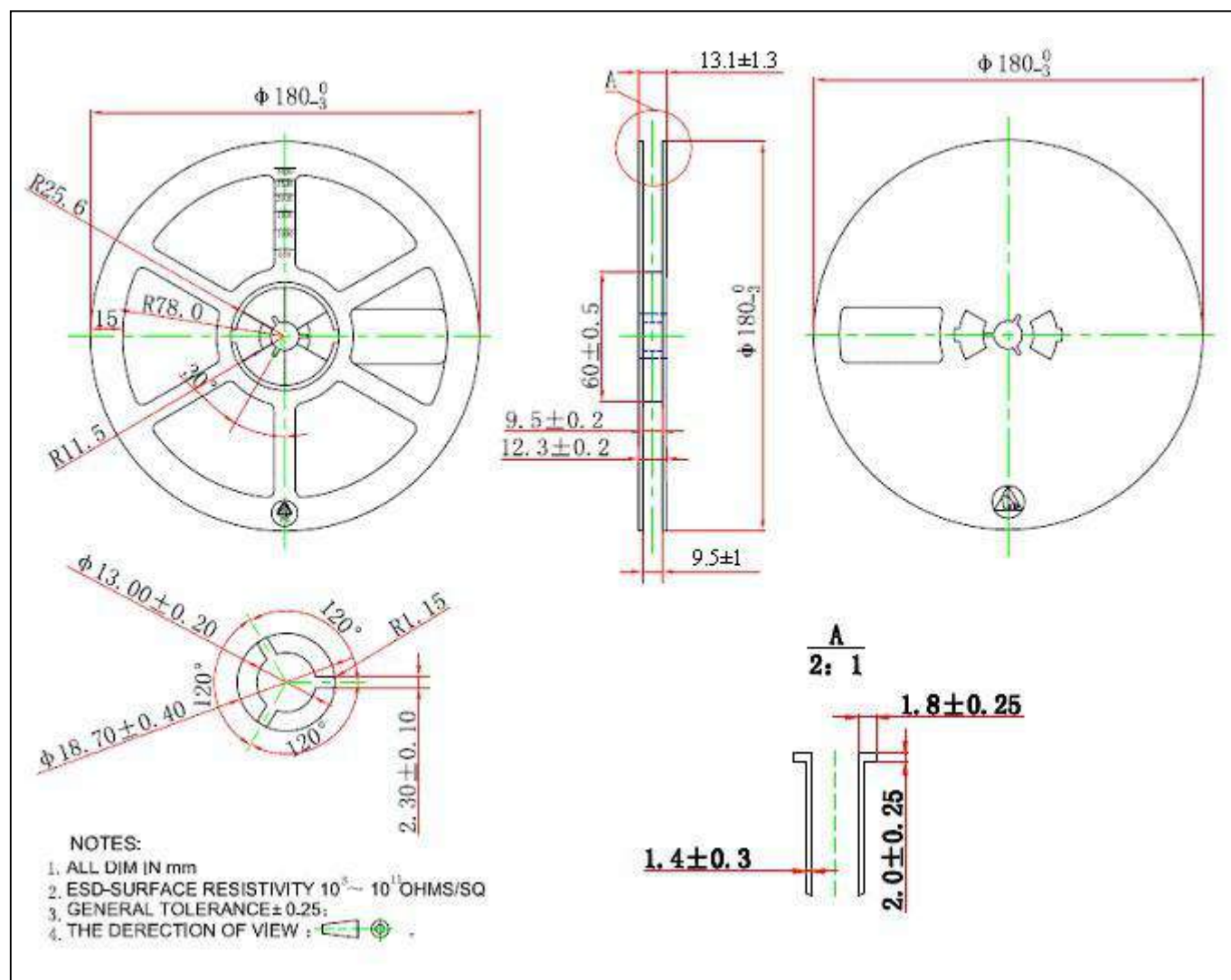
Forward Transfer Admittance vs Drain Current



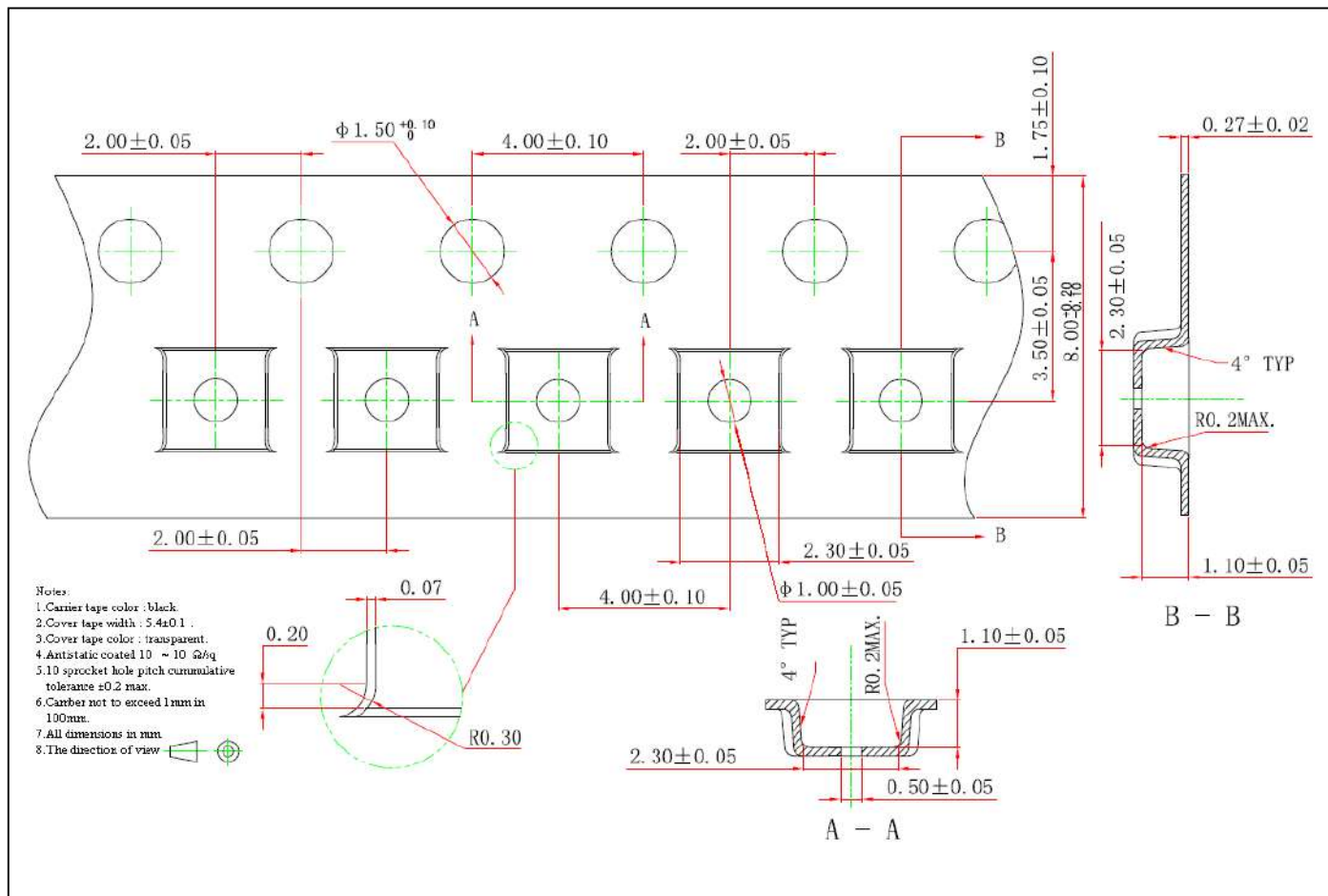
Transient Thermal Response Curves



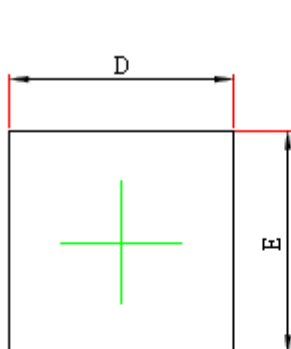
Reel Dimension



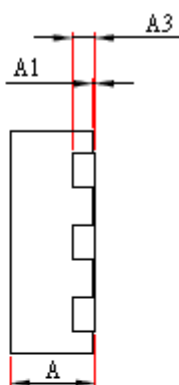
Carrier Tape Dimension



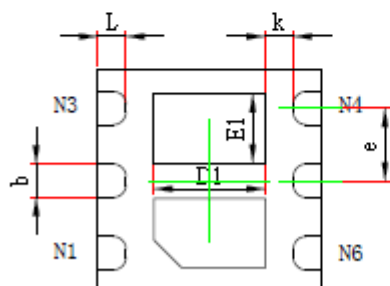
DFN2x2-6L Dimension



TOP VIEW



SIDE VIEW



BOTTOM VIEW

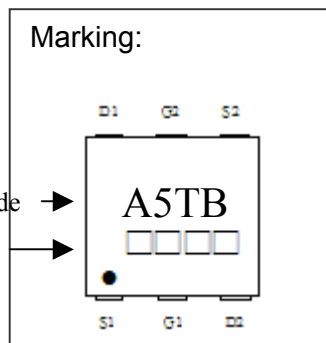
Style:

- Pin 1. Source1 (S1)
- Pin 2. Gate 1 (G1)
- Pin 3. Drain2 (D2)
- Pin 4. Source2 (S2)
- Pin 5. Gate2 (G2)
- Pin 6. Drain1 (D1)

Marking:

Device Code →

Date Code →



6-Lead DFN2x2-6L Plastic
Surface Mounted Package

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.700	0.800	0.028	0.031	E1	0.520	0.720	0.020	0.028
A1	0.000	0.050	0.000	0.002	k	0.200	-	0.008	-
A3	0.203	REF	0.008	REF	b	0.250	0.350	0.010	0.014
D	1.900	2.100	0.075	0.083	e	0.650	TYP	0.026	TYP
E	1.900	2.100	0.075	0.083	L	0.200	0.300	0.008	0.012
D1	0.900	1.100	0.035	0.043					