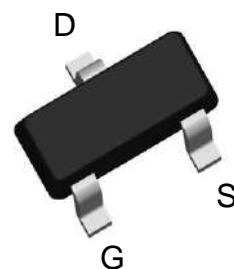


## N-Channel Enhancement Mode MOSFET

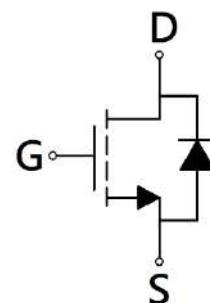
### Features:

- Low On Resistance
- Low Gate Charge
- Fast Switching Characteristic

SOT-323



$BV_{DSS}$	30V
$I_D @ V_{GS}=10V, T_A=25^\circ C$	1.6A
$R_{DS(ON)} \text{ typ.} @ V_{GS}=10V, I_D=1.6A$	60m $\Omega$
$R_{DS(ON)} \text{ typ.} @ V_{GS}=4.5V, I_D=1.5A$	65m $\Omega$
$R_{DS(ON)} \text{ typ.} @ V_{GS}=2.5V, I_D=1A$	82m $\Omega$



G : Gate S : Source D : Drain

### Ordering Information

Device	Package	Shipping
KWA040N-3	SOT-323 (Pb-free lead plating and halogen-free package)	3000 pcs / Tape & Reel



### Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ )

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	
Continuous Drain Current @ $V_{GS}=10\text{V}$ , $T_A=25^\circ\text{C}$	$I_D$	1.6	A
Continuous Drain Current @ $V_{GS}=10\text{V}$ , $T_A=70^\circ\text{C}$		1.3	
Pulsed Drain Current	$I_{DM}$	6.4	A
Continuous Body Diode Forward Current @ $T_A=25^\circ\text{C}$	$I_S$	0.3	
Avalanche Current @ $L=0.1\text{mH}$	$I_{AS}$	7	mJ
Avalanche Energy @ $L=0.5\text{mH}$	$E_{AS}$	4	
Total Power Dissipation	$P_D$	0.34	W
$T_A=70^\circ\text{C}$		0.22	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55~+150	°C

### Thermal Data

Parameter	Symbol	Steady State	Unit
Thermal Resistance, Junction-to-ambient	$R_{\theta JA}$	367	°C/W

Note:

\*a. The value of  $R_{\theta JA}$  is measured with the device mounted on 1 in<sup>2</sup> FR -4 board with 2 oz. copper, in a still air environment with  $T_A=25^\circ\text{C}$ . The power dissipation  $P_D$  is based on  $R_{\theta JA}$  and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.

\*b. Repetitive rating, pulse width limited by junction temperature  $T_{J(MAX)}=150^\circ\text{C}$ . Ratings are based on low frequency and low duty cycles to keep initial  $T_J=25^\circ\text{C}$ .

### Electrical Characteristics ( $T_A=25^\circ\text{C}$ , unless otherwise specified)

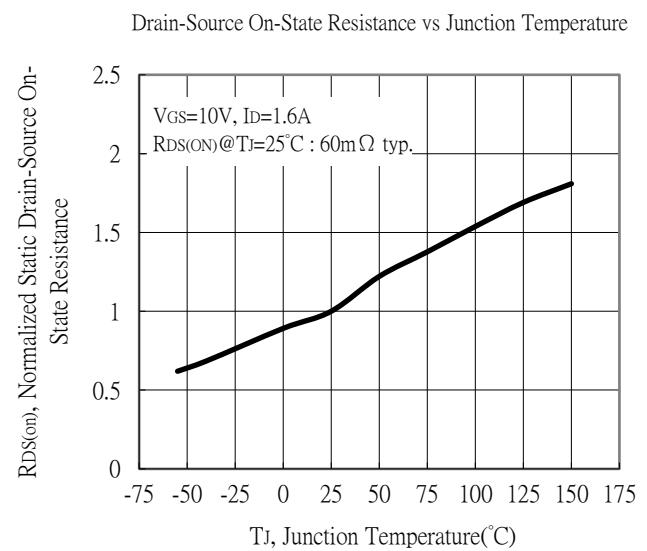
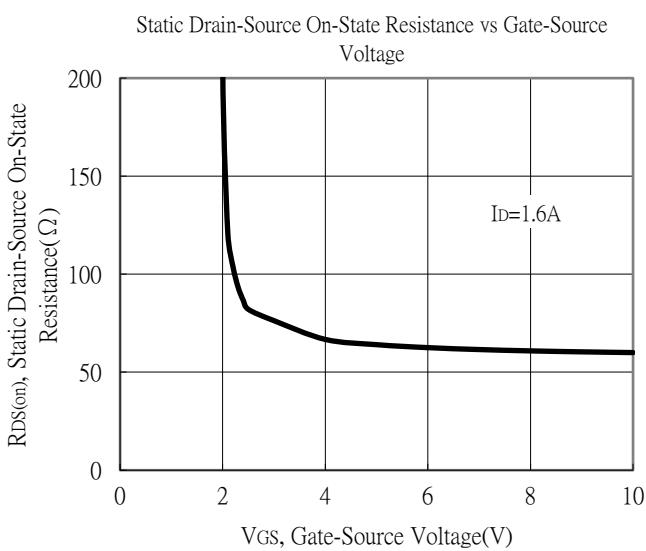
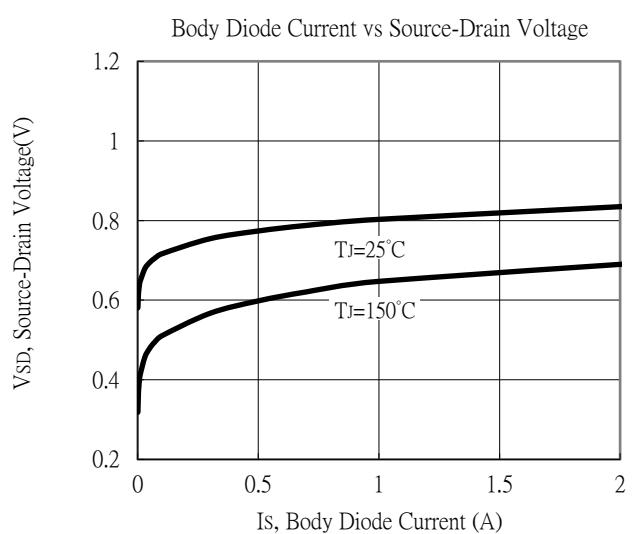
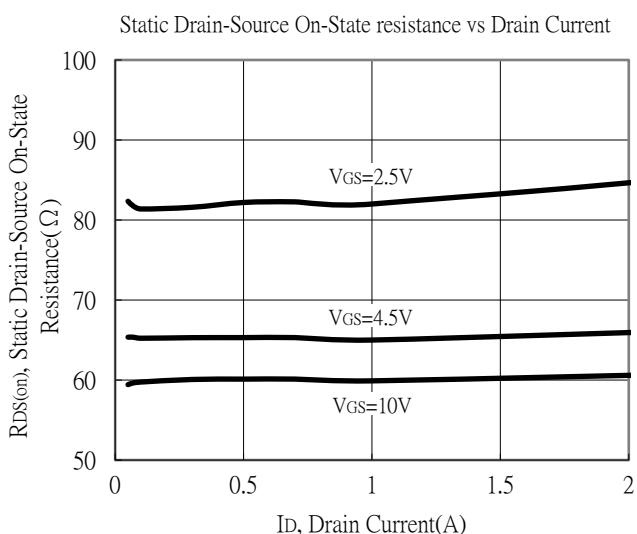
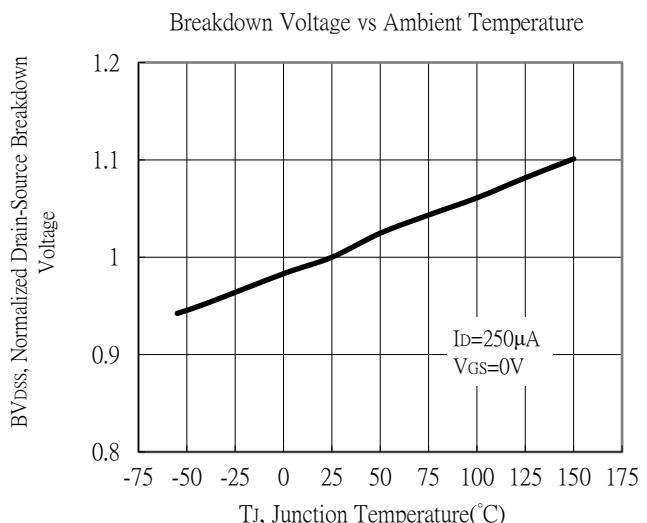
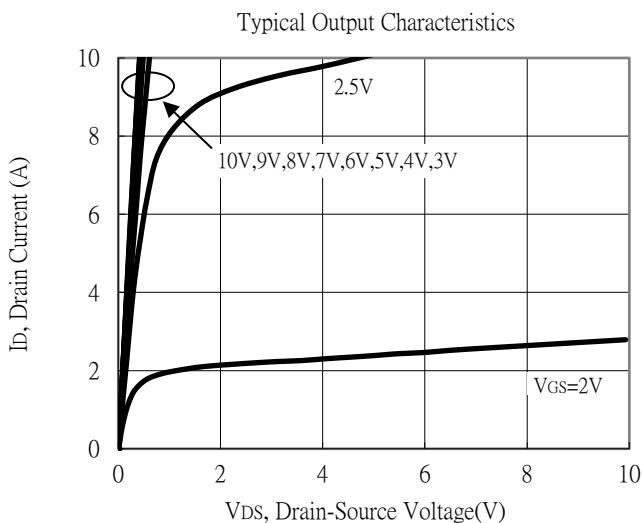
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
BV <sub>DSS</sub>	30	-	-	V	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA
V <sub>GS(th)</sub>	0.5	-	1.5		V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA
G <sub>FS</sub>	-	2	-	S	V <sub>DS</sub> =10V, I <sub>D</sub> =1A
I <sub>GSS</sub>	-	-	±100	nA	V <sub>GS</sub> =±12V, V <sub>DS</sub> =0V
I <sub>DSS</sub>	-	-	1	μA	V <sub>DS</sub> =24V, V <sub>GS</sub> =0V
R <sub>DSS(ON)</sub>	-	60	78	mΩ	V <sub>GS</sub> =10V, I <sub>D</sub> =1.6A
	-	65	90		V <sub>GS</sub> =4.5V, I <sub>D</sub> =1.5A
	-	82	115		V <sub>GS</sub> =2.5V, I <sub>D</sub> =1A
<b>Dynamic</b>					
C <sub>iss</sub>	-	315	-	pF	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V, f=1MHz
C <sub>oss</sub>	-	46	-		
C <sub>rss</sub>	-	33	-		
R <sub>g</sub>	-	2	-	Ω	f=1MHz
Q <sub>g</sub> *1, 2	-	10	-	nC	V <sub>DS</sub> =15V, I <sub>D</sub> =1A, V <sub>GS</sub> =10V
Q <sub>gs</sub> *1, 2	-	1.6	-		
Q <sub>gd</sub> *1, 2	-	0.8	-		
t <sub>d(ON)</sub> *1, 2	-	3.5	-	ns	V <sub>DS</sub> =15V, I <sub>D</sub> =1A, V <sub>GS</sub> =10V, R <sub>GS</sub> =6Ω
t <sub>r</sub> *1, 2	-	16	-		
t <sub>d(OFF)</sub> *1, 2	-	25	-		
t <sub>f</sub> *1, 2	-	4.2	-		
<b>Source-Drain Diode</b>					
V <sub>SD</sub> *1	-	0.8	1.2	V	I <sub>S</sub> =1A, V <sub>GS</sub> =0V
trr	-	7.7	-	ns	I <sub>F</sub> =1A, dI <sub>F</sub> /dt=100A/μs
Qrr	-	3.3	-	nC	

Note:

\*1. Pulse Test : Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$

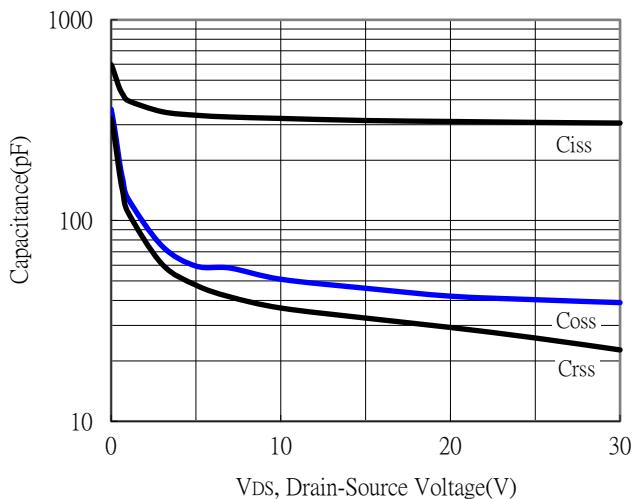
\*2. Independent of operating temperature

## Typical Characteristics

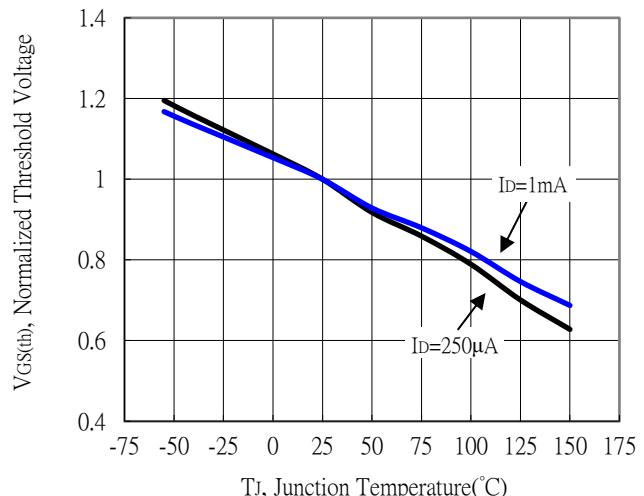


## Typical Characteristics (Cont.)

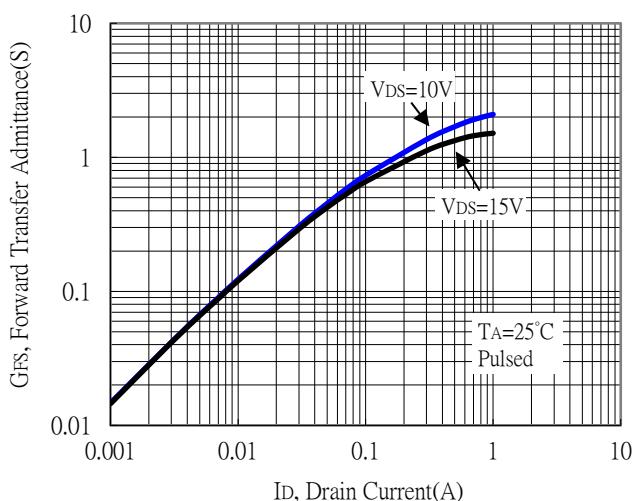
Capacitance vs Drain-to-Source Voltage



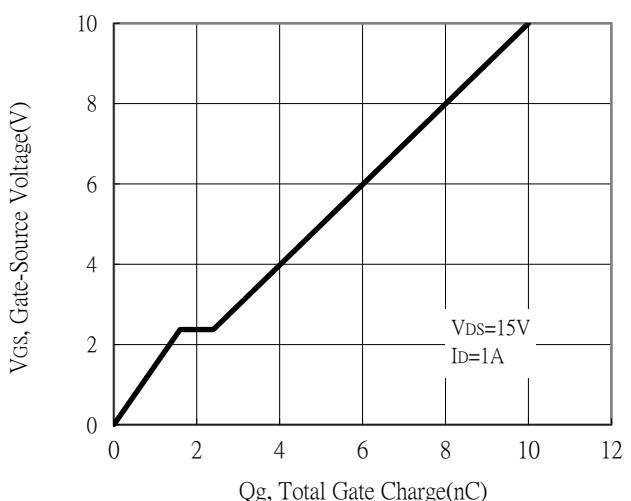
Threshold Voltage vs Junction Temperature



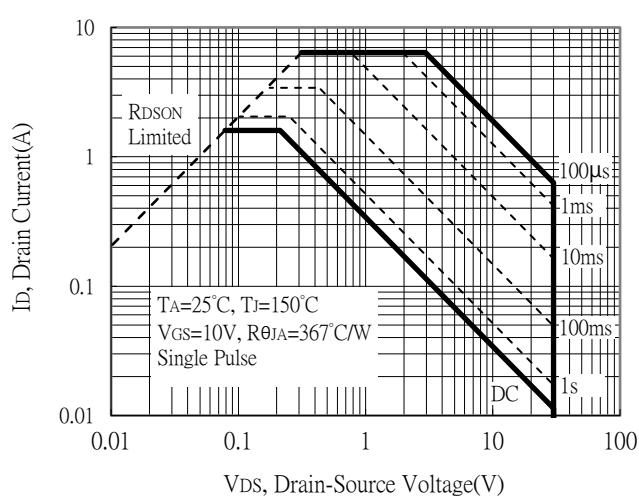
Forward Transfer Admittance vs Drain Current



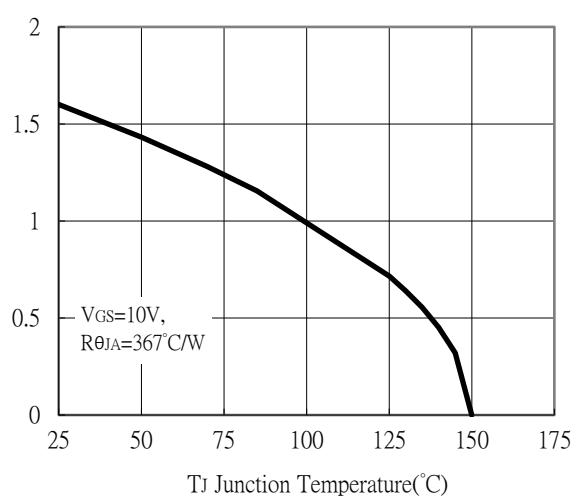
Gate Charge Characteristics



Maximum Safe Operating Area

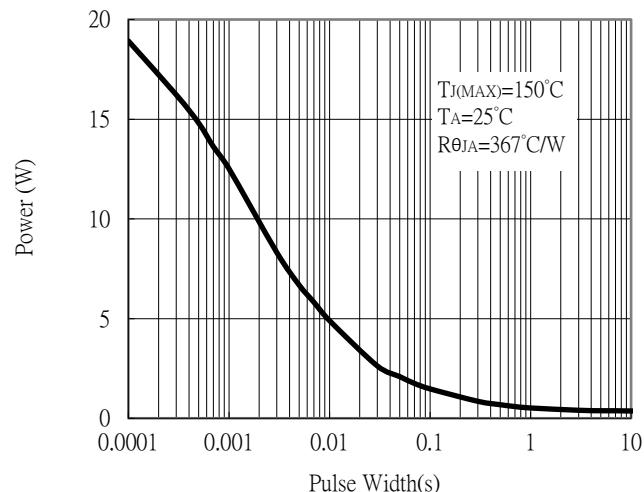


Maximum Drain Current vs Junction Temperature

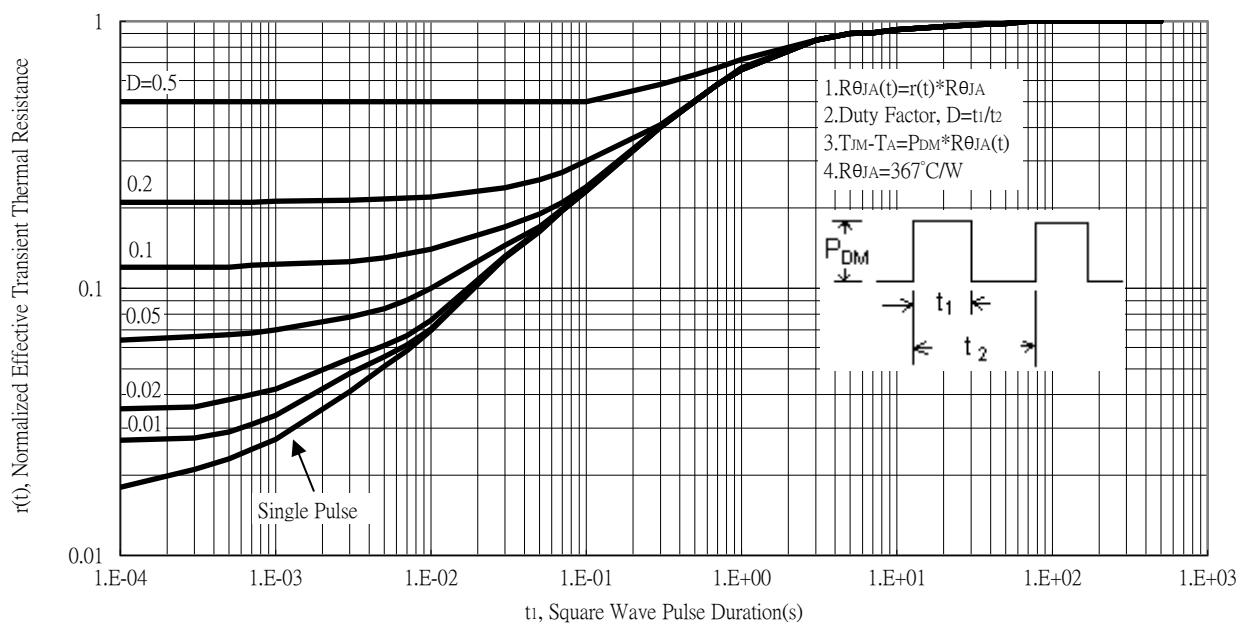


## Typical Characteristics (Cont.)

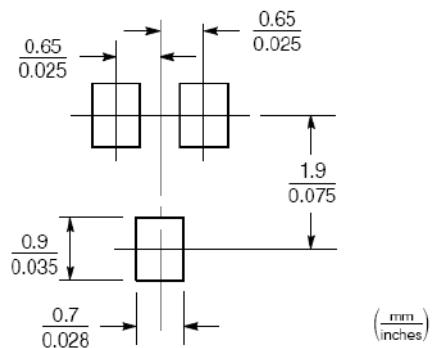
Single Pulse Power Rating, Junction to Ambient



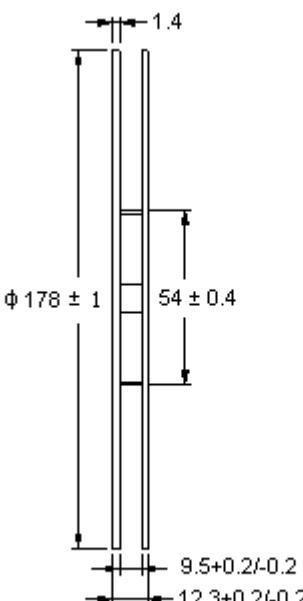
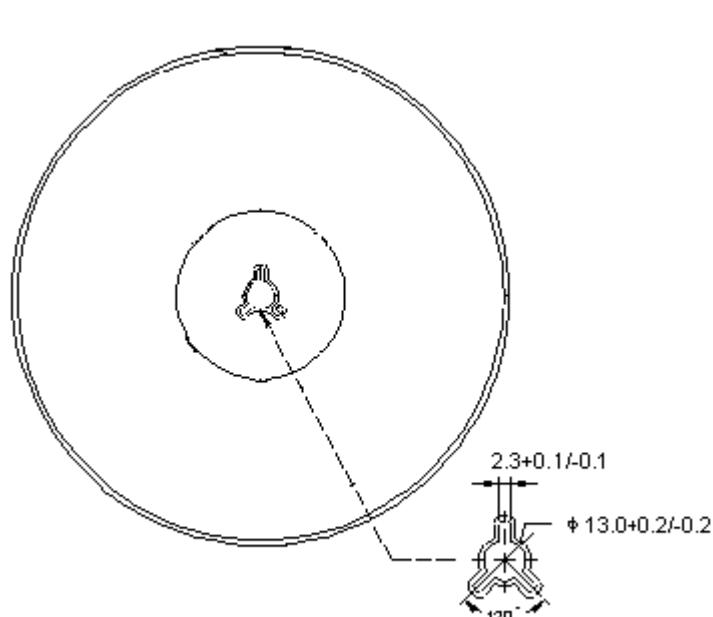
Transient Thermal Response Curves



## Recommended Soldering Footprint

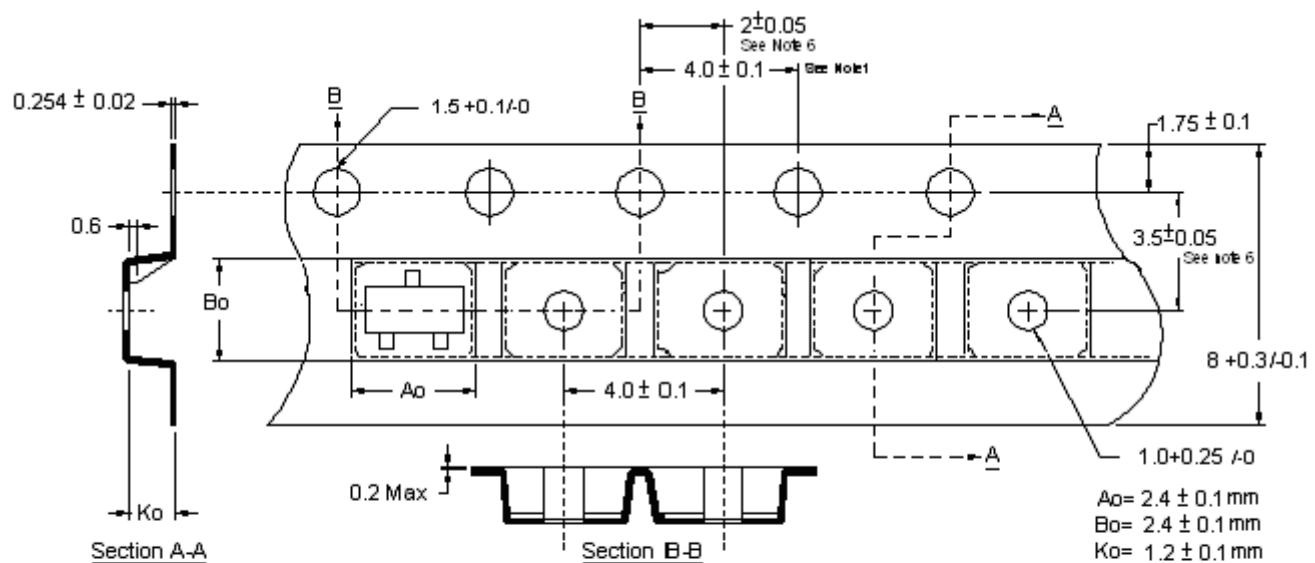


## Reel Dimension



Unit: millimeter

## Carrier Tape Dimension

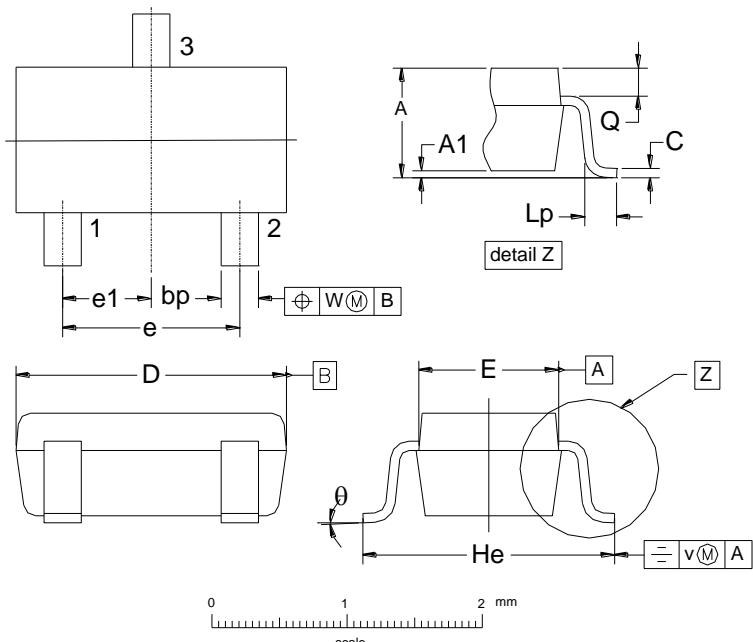


Notes:

1. 10 sprocket hole pitch cumulative tolerance  $\pm 0.2$ .
2. Camber not to exceed 1mm in 100mm.
3. Material : Conductive black polystyrene.
4.  $A_o$  &  $B_o$  measured on a plane 0.3mm above the bottom of the pocket.
5.  $Ko$  measured from a plane on the inside bottom of the pocket to the top surface of the carrier.
6. Pocket position relative to sprocket hole measured as true position of pocket, not pocket hole.

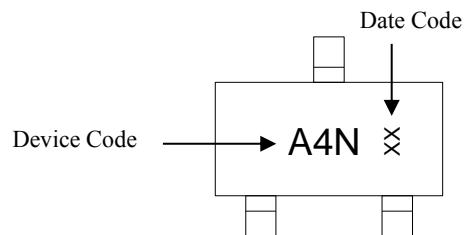
Unit : millimeter

## SOT-323 Dimension



Style: Pin 1.Gate 2.Source 3.Drain

Marking:



3-Lead SOT-323 Plastic Surface Mounted Package

Date Code: Year+Month

Year: 3→2003, 4→2004

Month: 1→1, 2→2, . . .

9→9, A→10, B→11, C→12

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.0315	0.0433	0.80	1.10	e1	0.0256	-	0.65	-
A1	0.0000	0.0039	0.00	0.10	He	0.0787	0.0886	2.00	2.25
bp	0.0118	0.0157	0.30	0.40	Lp	0.0059	0.0177	0.15	0.45
C	0.0039	0.0098	0.10	0.25	Q	0.0051	0.0091	0.13	0.23
D	0.0709	0.0866	1.80	2.20	v	0.0079	-	0.2	-
E	0.0453	0.0531	1.15	1.35	w	0.0079	-	0.2	-
e	0.0512	-	1.3	-	θ	-	-	10°	0°