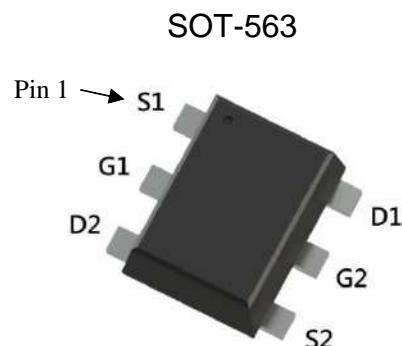


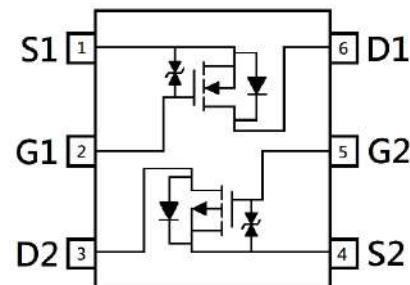
N- AND P-Channel Enhancement Mode Power MOSFET

Features:

- Low On Resistance
- Low Gate Charge
- RoHS compliant package
- ESD protected gate, typical 4kV (HBM)



	N-CH	P-CH
BV_{DSS}	20V	-20V
$I_D @ V_{GS} = (-)4.5V, T_A = 25^\circ C$	0.83A	-0.52A
$R_{DS(ON)} \text{ typ. } @ V_{GS} = (-)4.5V$	0.32Ω	0.75Ω
$R_{DS(ON)} \text{ typ. } @ V_{GS} = (-)2.5V$	0.4Ω	0.9Ω
$R_{DS(ON)} \text{ typ. } @ V_{GS} = (-)1.8V$	0.7Ω	1.1Ω



G : Gate S : Source D : Drain

Ordering Information

Device	Package	Shipping
KWA300C02K	SOT-563 (Pb-free lead plating and halogen-free package)	3000 pcs / Tape & Reel



Absolute Maximum Ratings ($T_A=25^\circ\text{C}$)

Parameter	Symbol	Limits		Unit
		N-CH	P-CH	
Drain-Source Voltage	V_{DS}	20	-20	V
Gate-Source Voltage	V_{GS}	± 8	± 8	
Continuous Drain Current @ $V_{GS}=(-)4.5\text{V}$, $T_A=25^\circ\text{C}$	I_D	0.83	-0.52	A
Continuous Drain Current @ $V_{GS}=(-)4.5\text{V}$, $T_A=70^\circ\text{C}$		0.66	-0.42	
Pulsed Drain Current	I_{DM}	3.32	-2.08	
Continuous Body Diode Forward Current @ $T_A=25^\circ\text{C}$	I_S	0.42	-0.42	
ESD susceptibility	V_{ESD}	4000	4000	V
Total Power Dissipation	P_D	0.51		W
$T_A=70^\circ\text{C}$		0.33		
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55~+150		°C

Thermal Data

Parameter	Symbol	Steady State	Unit
Thermal Resistance, Junction-to-ambient	$R_{\theta JA}$	245	$^\circ\text{C}/\text{W}$

Note:

*a. Repetitive rating, pulse width limited by junction temperature $T_J(\text{MAX})=150^\circ\text{C}$. Ratings are based on low frequency and low duty cycles to keep initial $T_J=25^\circ\text{C}$.

*b. Human body model, $1.5\text{k}\Omega$ in series with 100pF .

N-Channel Electrical Characteristics ($T_A=25^\circ C$, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	20	-	-	V	$V_{GS}=0V, I_D=250\mu A$
V _{GS(th)}	0.4	-	1.2		$V_{DS}=V_{GS}, I_D=250\mu A$
G _{FS}	-	0.8	-	S	$V_{DS}=5V, I_D=0.2A$
I _{GSS}	-	-	± 10	μA	$V_{GS}=\pm 8V, V_{DS}=0V$
I _{DSS}	-	-	1		$V_{DS}=16V, V_{GS}=0V$
R _{D(S(ON))}	-	0.32	0.45	Ω	$V_{GS}=4.5V, I_D=0.2A$
	-	0.4	0.6		$V_{GS}=2.5V, I_D=0.2A$
	-	0.7	1.2		$V_{GS}=1.8V, I_D=10mA$
Dynamic					
C _{iss}	-	33	-	pF	$V_{DS}=10V, V_{GS}=0V, f=1MHz$
C _{oss}	-	15	-		
C _{rss}	-	13	-		
Q _g *1, 2	-	0.9	-	nC	$V_{DS}=20V, I_D=0.6A, V_{GS}=4.5V$
Q _{gs} *1, 2	-	0.2	-		
Q _{gd} *1, 2	-	0.2	-		
t _{d(ON)} *1, 2	-	5	-	ns	$V_{DS}=10V, I_D=0.2A, V_{GS}=4.5V, R_{GS}=10\Omega$
t _r *1, 2	-	17	-		
t _{d(OFF)} *1, 2	-	20	-		
t _f *1, 2	-	20	-		
Source-Drain Diode					
V _{SD} *1	-	0.85	1.2	V	I _s =0.2A, V _{GS} =0V
trr	-	5	-	ns	I _F =0.5A, dI _F /dt=100A/ μs
Qrr	-	1	-	nC	

Note:

*1. Pulse Test : Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$

*2. Independent of operating temperature

P-Channel Electrical Characteristics ($T_A=25^\circ C$, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	-20	-	-	V	V _{GS} =0V, I _D =-250μA
V _{GS(th)}	-0.4	-	-1.2		V _{DS} =V _{GS} , I _D =-250μA
G _{FS}	-	1	-	S	V _{DS} =-5V, I _D =-0.4A
I _{GSS}	-	-	±10		V _{GS} =±8V, V _{DS} =0V
I _{DSS}	-	-	-1	μA	V _{DS} =-16V, V _{GS} =0V
R _{DSS(ON)}	-	0.75	1.1		V _{GS} =-4.5V, I _D =-0.2A
	-	0.9	1.5		V _{GS} =-2.5V, I _D =-0.2A
	-	1.1	2.5		V _{GS} =-1.8V, I _D =-10mA
Dynamic					
C _{iss}	-	45	-	pF	V _{DS} =-10V, V _{GS} =0V, f=1MHz
C _{oss}	-	15	-		
C _{rss}	-	8	-		
Q _g *1, 2	-	1	-	nC	V _{DS} =-20V, I _D =-0.4A, V _{GS} =-4.5V
Q _{gs} *1, 2	-	0.2	-		
Q _{gd} *1, 2	-	0.2	-		
t _{d(ON)} *1, 2	-	11	-	ns	V _{DS} =-10V, I _D =-0.2A, V _{GS} =-4.5V, R _{GS} =10Ω
t _r *1, 2	-	20	-		
t _{d(OFF)} *1, 2	-	45	-		
t _f *1, 2	-	35	-		
Source-Drain Diode					
V _{SD} *1	-	-0.9	-1.2	V	I _S =-0.4A, V _{GS} =0V

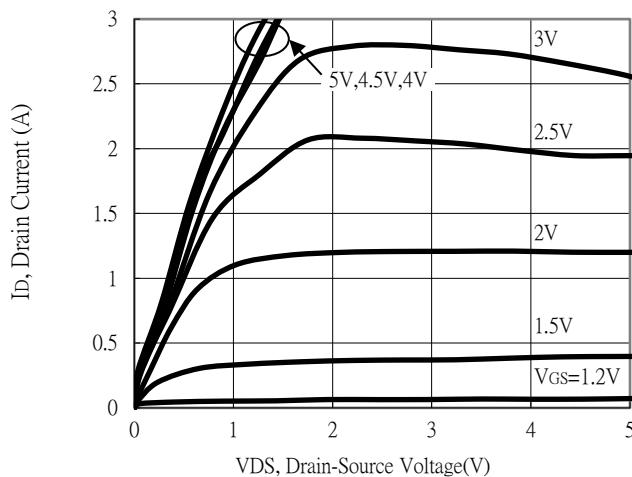
Note:

*1. Pulse Test : Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$

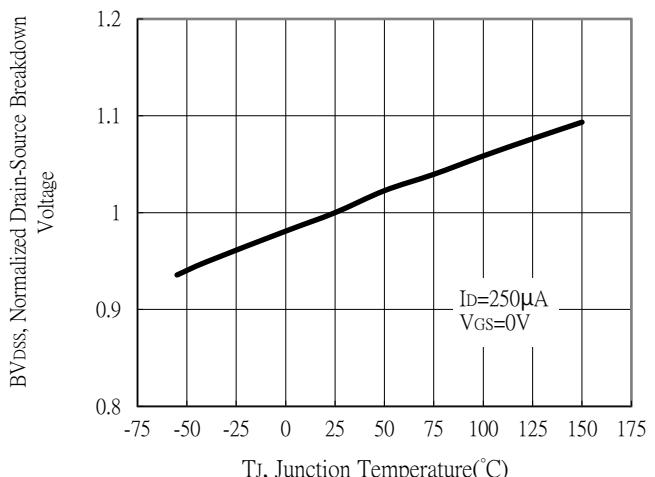
*2. Independent of operating temperature

Typical Characteristics : Q1(N-channel)

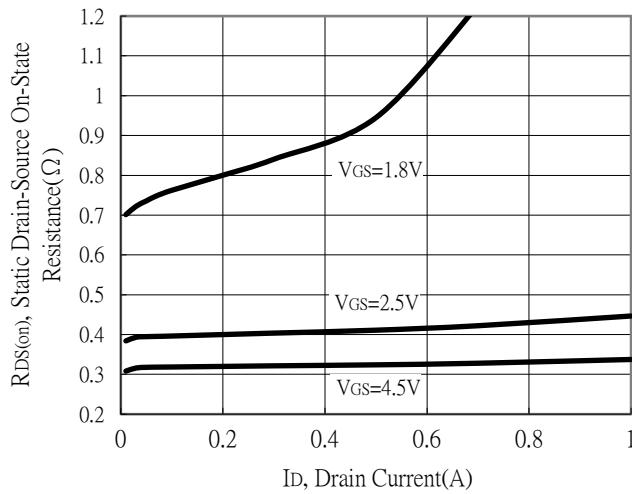
Typical Output Characteristics



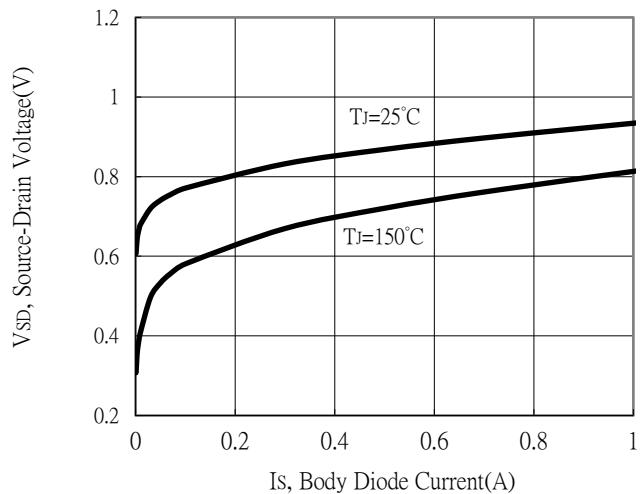
Breakdown Voltage vs Junction Temperature



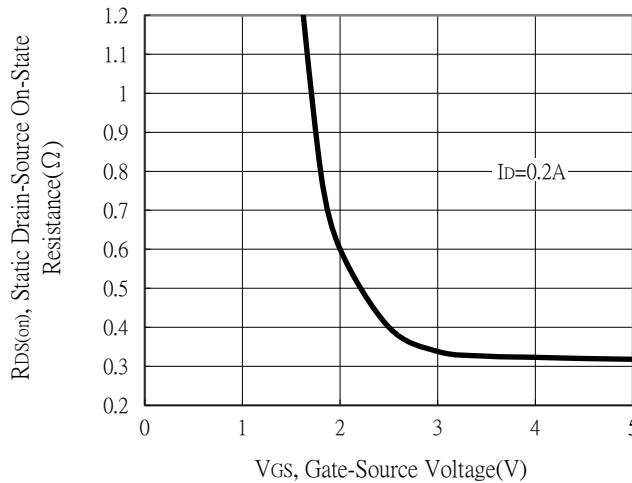
Static Drain-Source On-State resistance vs Drain Current



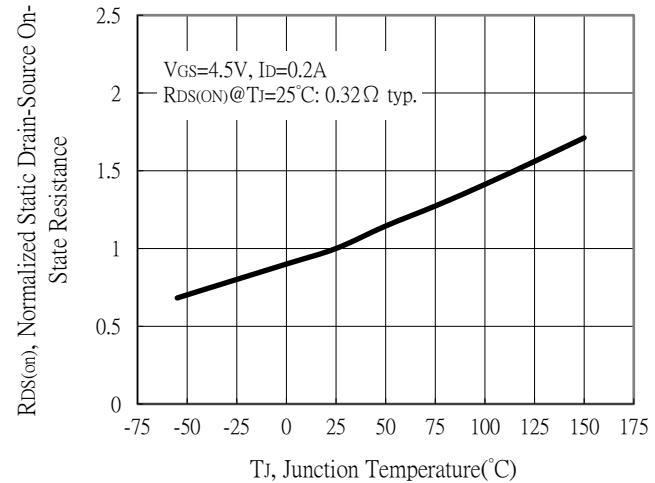
Body Diode Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

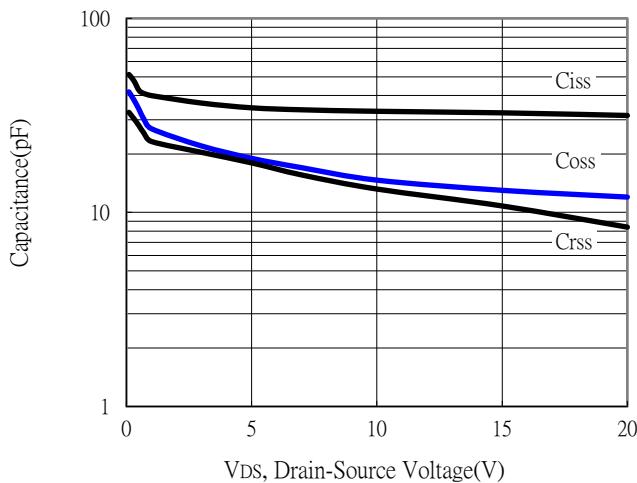


Drain-Source On-State Resistance vs Junction Temperature

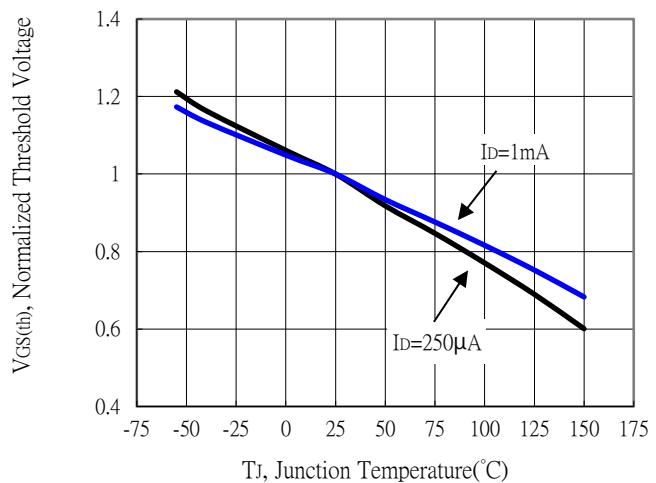


Typical Characteristics (Cont.) : Q1(N-channel)

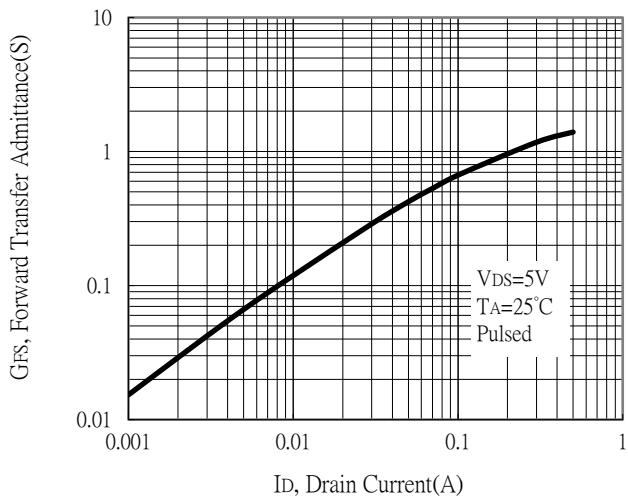
Capacitance vs Drain-to-Source Voltage



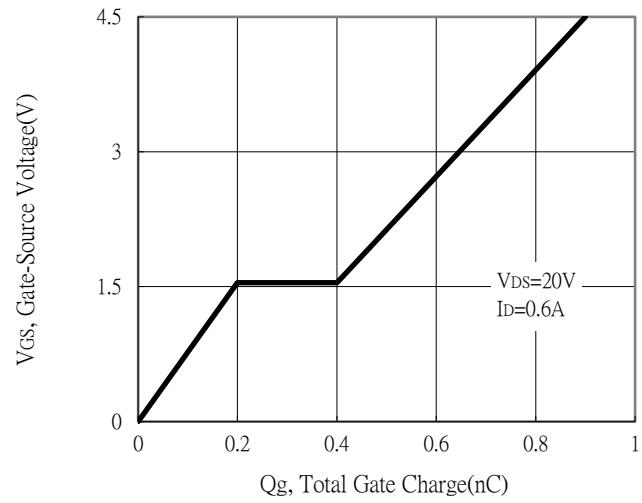
Threshold Voltage vs Junction Temperature



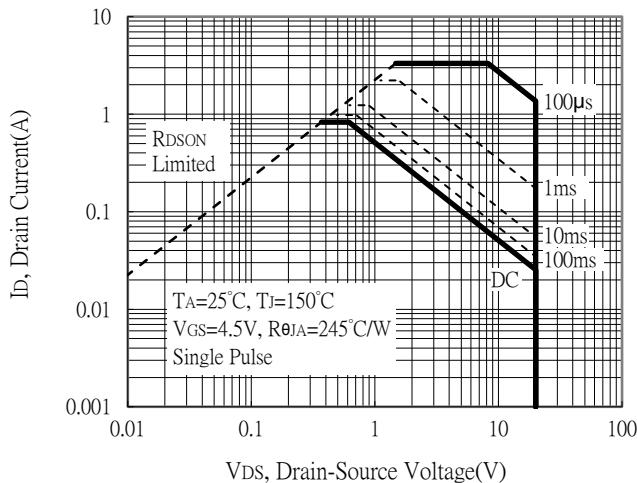
Forward Transfer Admittance vs Drain Current



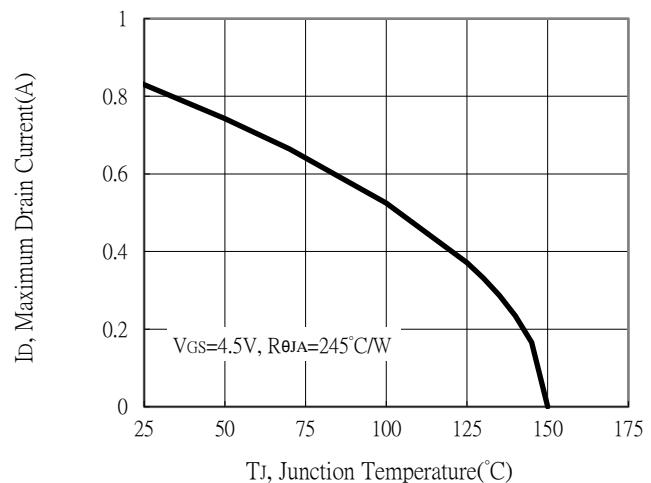
Gate Charge Characteristics



Maximum Safe Operating Area

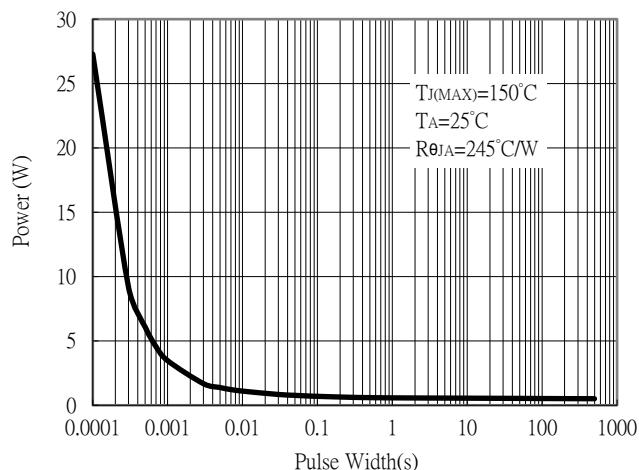


Maximum Drain Current vs Junction Temperature

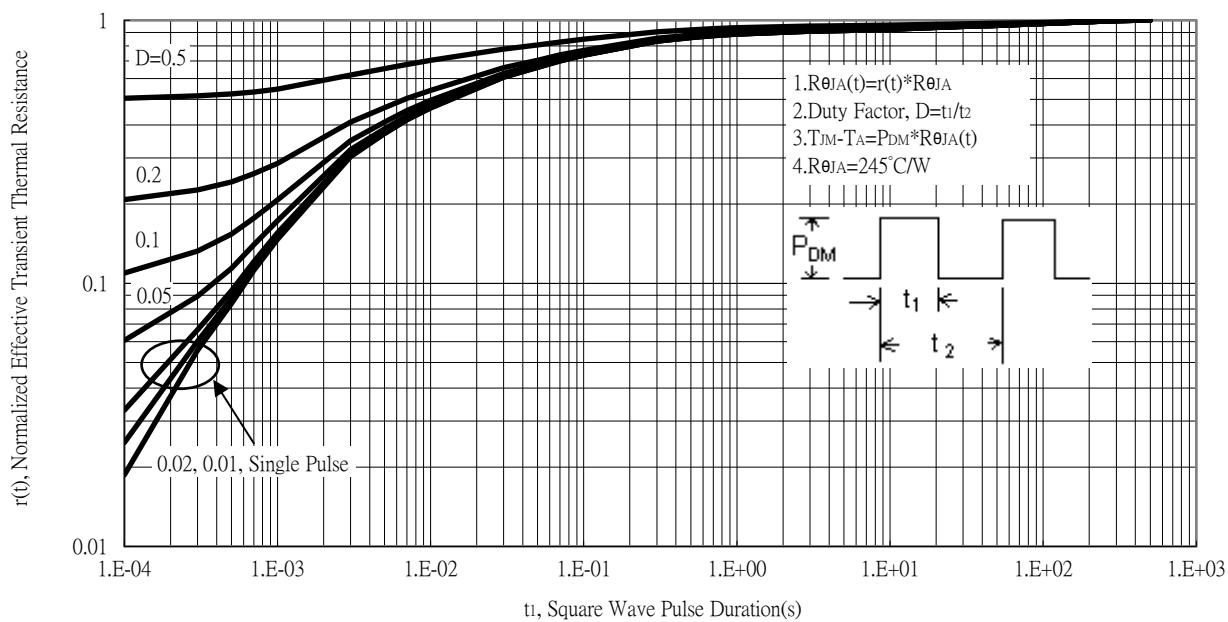


Typical Characteristics (Cont.) : Q1(N-channel)

Single Pulse Power Rating, Junction to Ambient

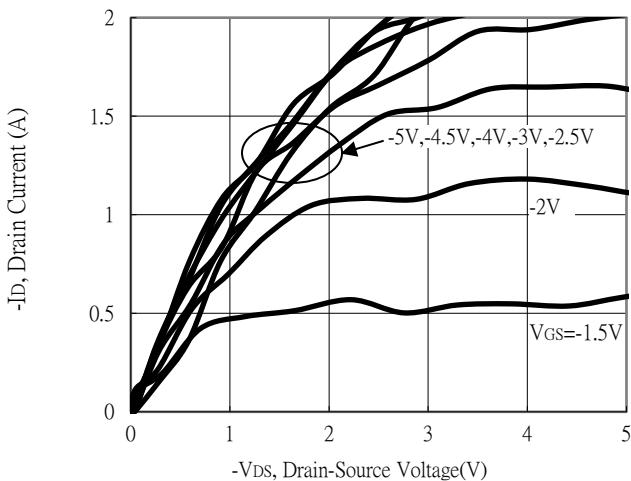


Transient Thermal Response Curves

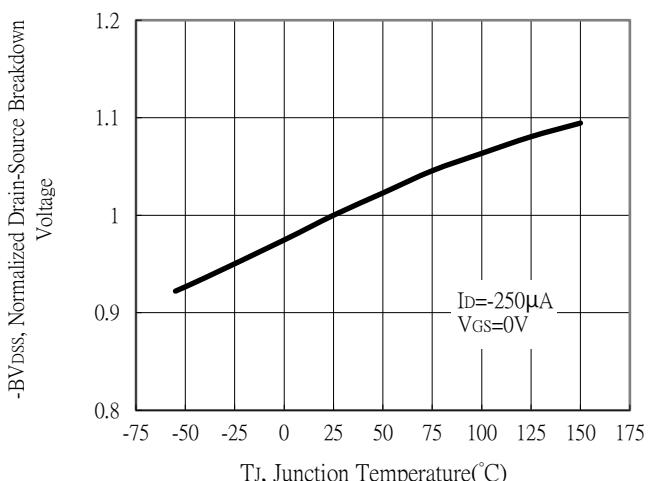


Typical Characteristics : Q2(P-channel)

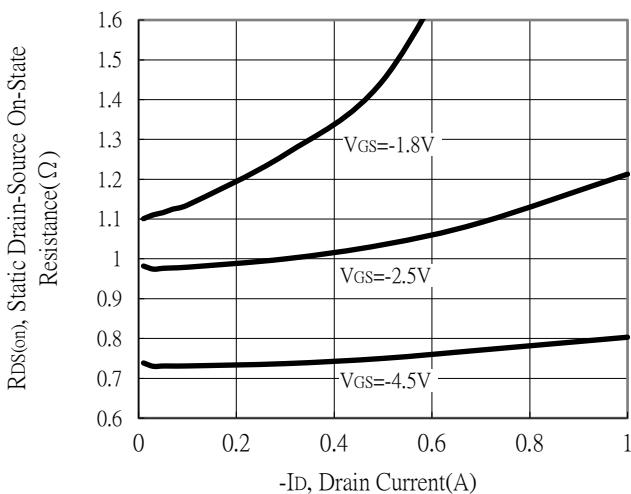
Typical Output Characteristics



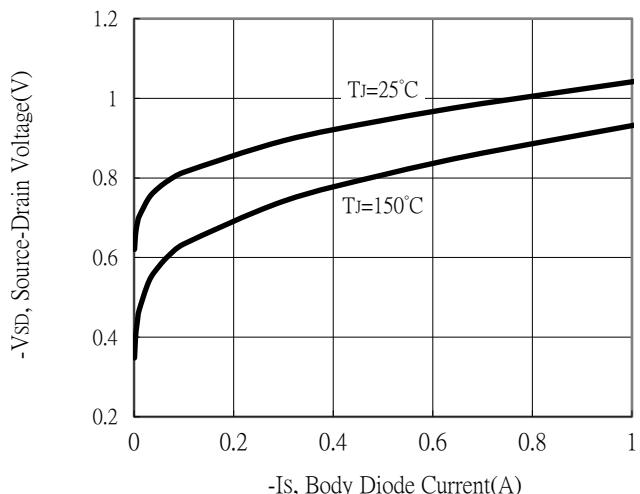
Breakdown Voltage vs Junction Temperature



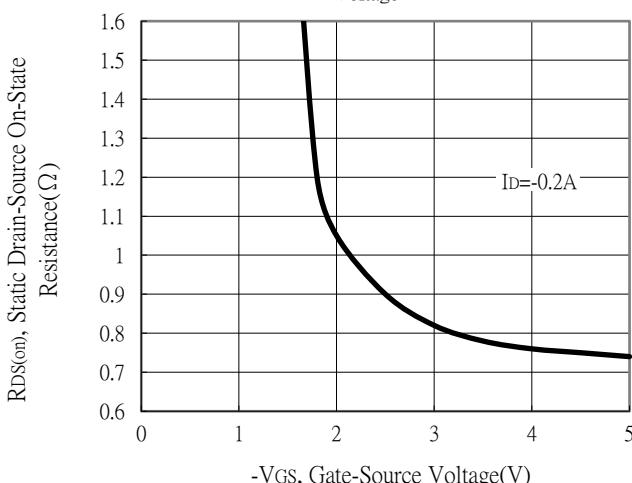
Static Drain-Source On-State resistance vs Drain Current



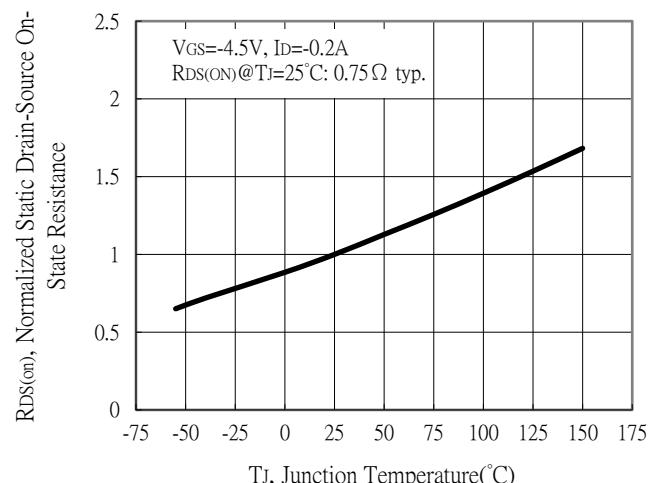
Body Diode Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

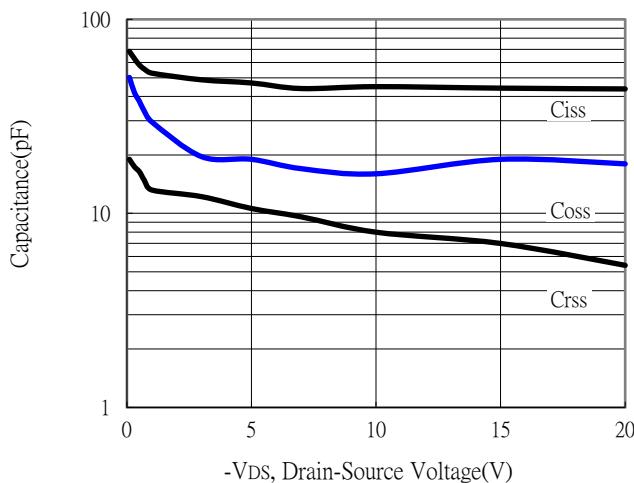


Drain-Source On-State Resistance vs Junction Temperature

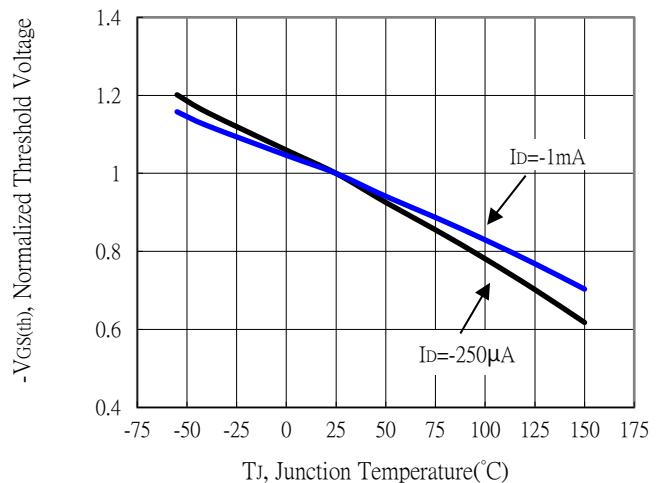


Typical Characteristics (Cont.) : Q2(P-channel)

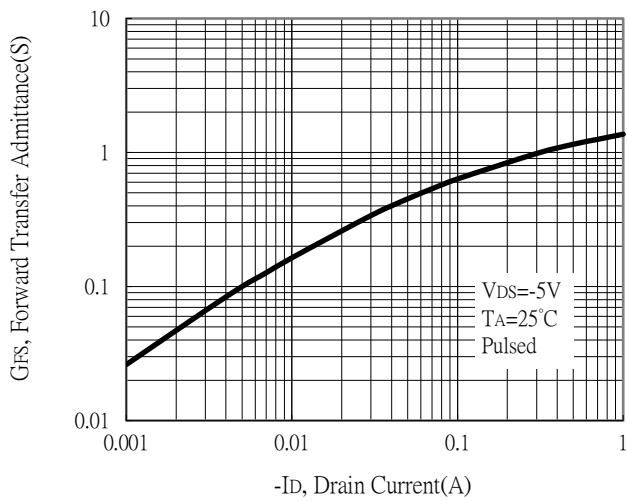
Capacitance vs Drain-to-Source Voltage



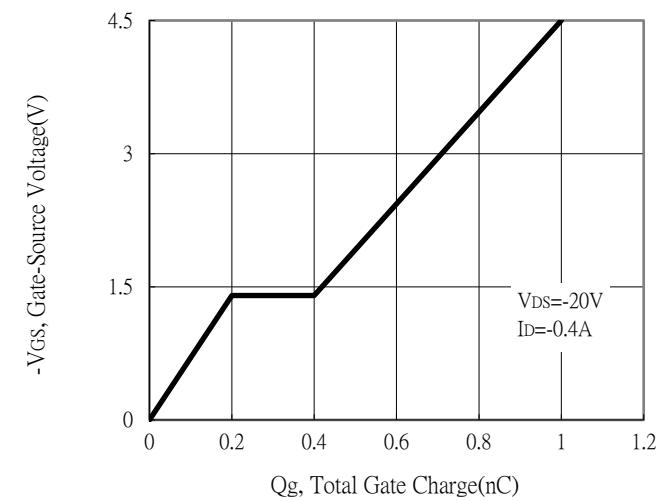
Threshold Voltage vs Junction Temperature



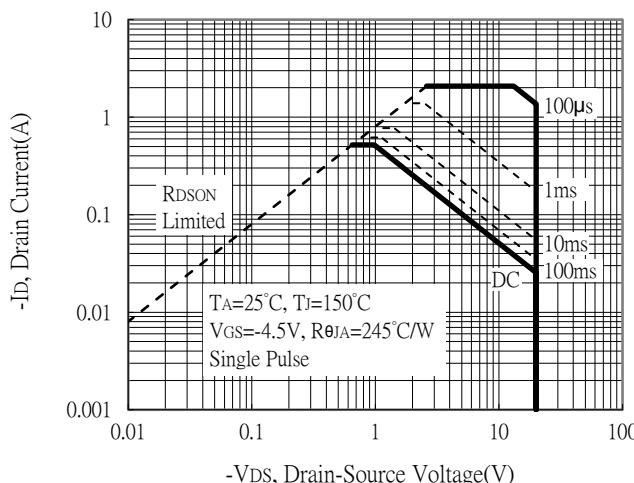
Forward Transfer Admittance vs Drain Current



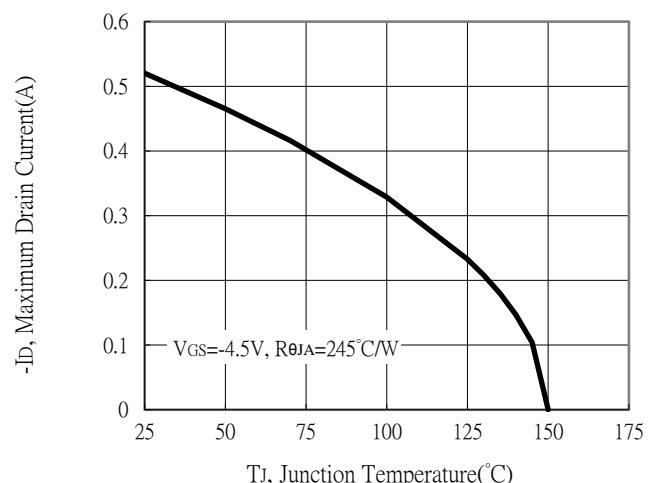
Gate Charge Characteristics



Maximum Safe Operating Area

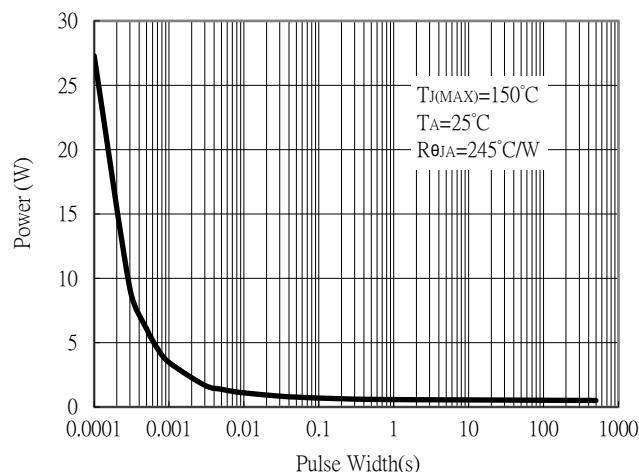


Maximum Drain Current vs Junction Temperature

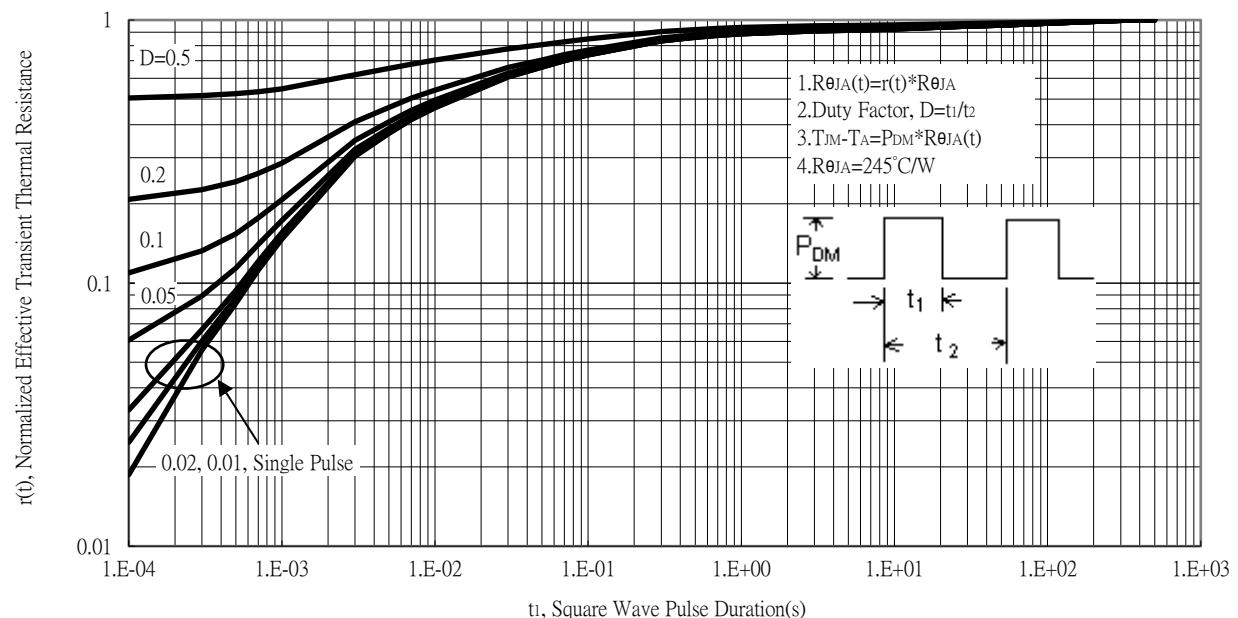


Typical Characteristics (Cont.) : Q2(P-channel)

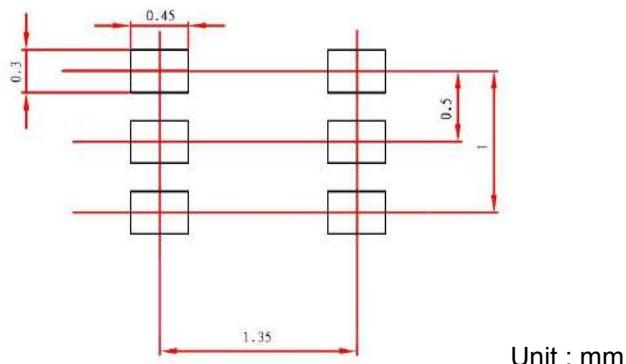
Single Pulse Power Rating, Junction to Ambient



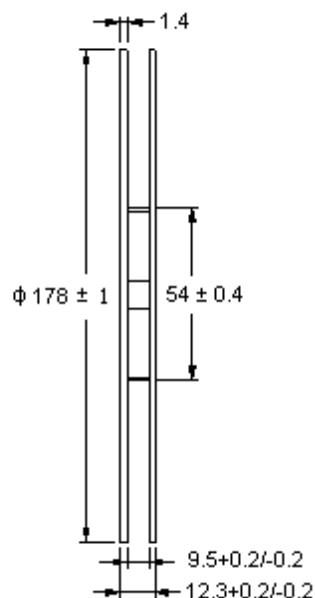
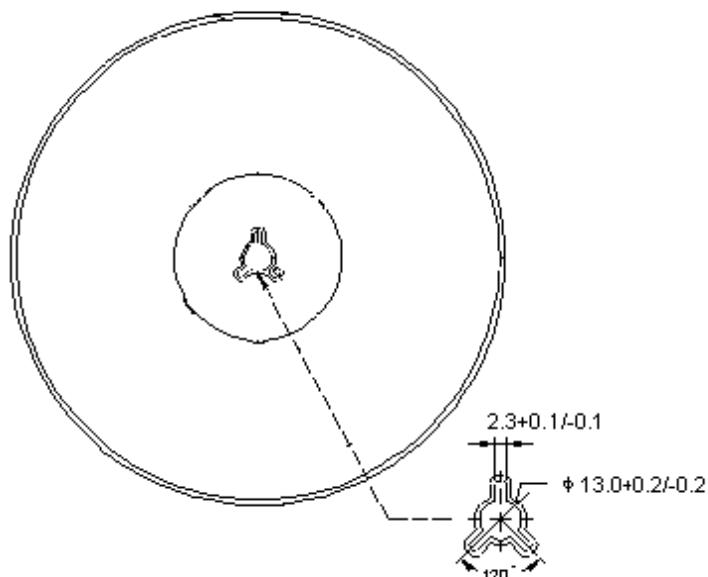
Transient Thermal Response Curves



Recommended Soldering Footprint

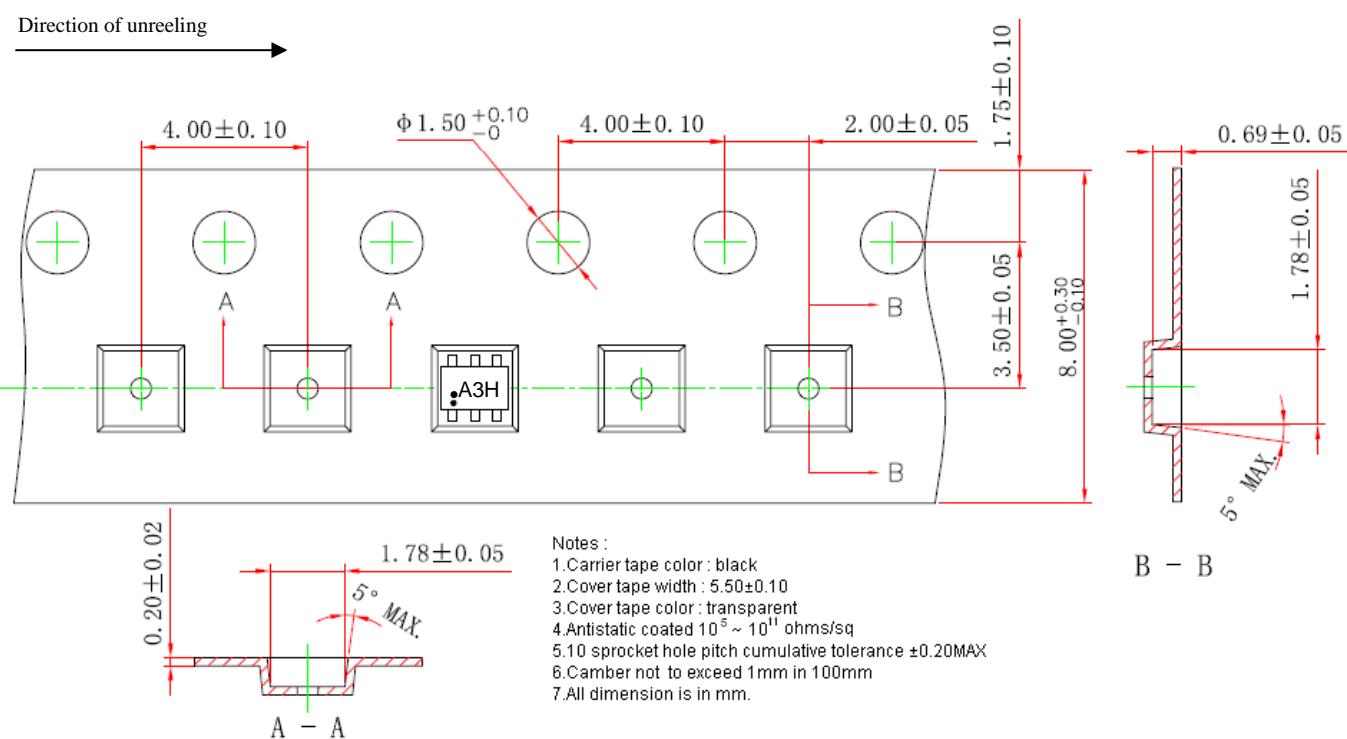


Reel Dimension

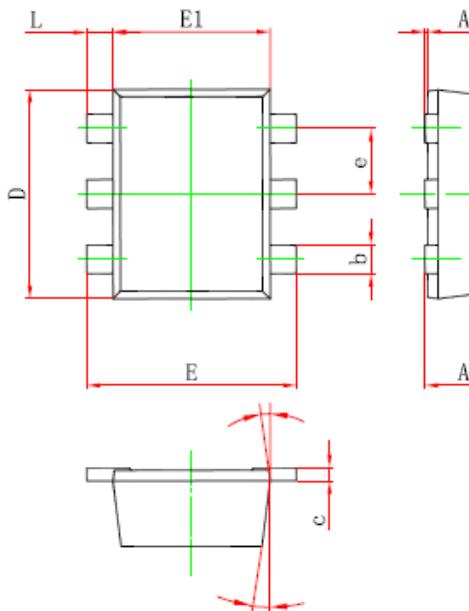


Unit: millimeter

Carrier Tape Dimension

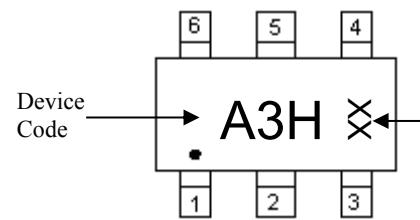


SOT-563 Dimension



6-Lead SOT-563 Plastic
Surface Mounted Package

Marking:



Date Code: Year+Month
 Year: 6→2006, 7→2007
 Month: 1→1, 2→2, . . .
 9→9, A→10, B→11, C→12

Style:
 Pin 1. Source1 (S1)
 Pin 2. Gate1 (G1)
 Pin 3. Drain2 (D2)
 Pin 4. Source2 (S2)
 Pin 5. Gate2 (G2)
 Pin 6. Drain1 (D1)

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.021	0.024	0.525	0.600	E1	0.043	0.051	1.100	1.300
A1	0.000	0.002	0.000	0.050	E	0.059	0.067	1.500	1.700
e	0.018	0.022	0.450	0.550	L	0.004	0.012	0.100	0.300
c	0.004	0.006	0.090	0.160	L1	0.008	0.016	0.200	0.400
D	0.059	0.067	1.500	1.700	θ	7°	REF	7°	REF
b	0.007	0.011	0.170	0.270					