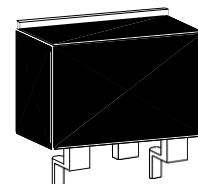


N-Channel Enhancement Mode Power MOSFET

Features:

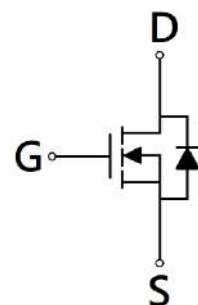
- Low On Resistance
- Low Gate Charge
- Fast Switching Characteristic

TO-263



G D S

BV _{DSS}	100V
I _D @V _{GS} =10V, T _c =25°C	33A
I _D @V _{GS} =10V, T _A =25°C	10A
R _{DSON} (typ.) @V _{GS} =10V, I _D =10A	12.2mΩ



G : Gate S : Source D : Drain

Ordering Information

Device	Package	Shipping
KUE013N10BR	TO-263 (Pb-free lead plating and RoHS compliant package)	800 pcs / Tape & Reel



Absolute Maximum Ratings ($T_A=25^\circ\text{C}$)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V _{DS}	100	V
Gate-Source Voltage	V _{GS}	±20	
Continuous Drain Current @ V _{GS} =10V, T _C =25°C	I _D	33	A
Continuous Drain Current @ V _{GS} =10V, T _C =100°C		21	
Continuous Drain Current @ V _{GS} =10V, T _A =25°C		10	
Continuous Drain Current @ V _{GS} =10V, T _A =70°C		8	
Pulsed Drain Current	I _{DM}	132	
Continuous Body Diode Forward Current @ T _C =25°C	I _S	30	
Avalanche Current @ L=0.1mH	I _{AS}	15	
Avalanche Energy @ L=0.5mH	E _{AS}	36	mJ
Total Power Dissipation	P _D	37	W
T _C =25°C		15	
T _C =100°C		3.1	
T _A =25°C		2	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55~+150	°C

Thermal Data

Parameter	Symbol	Steady State	Unit
Thermal Resistance, Junction-to-case	R _{θJC}	3.4	°C/W
Thermal Resistance, Junction-to-ambient	R _{θJA}	40	

Note:

- *a. The power dissipation P_D is based on T_{J(MAX)}=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
- *b. The value of R_{θJA} is measured with the device mounted on 1 in² FR -4 board with 2 oz. copper, in a still air environment with T_A=25°C. The power dissipation P_D is based on R_{θJA} and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
- *c. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C. Ratings are based on low frequency and low duty cycles to keep initial T_J=25°C.



Electrical Characteristics ($T_A=25^\circ\text{C}$, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	100	-	-	V	V _{GS} =0V, I _D =250μA
V _{GS(th)}	2	-	4		V _{DS} =V _{GS} , I _D =250μA
G _{FS}	-	14	-	S	V _{DS} =5V, I _D =10A
I _{GSS}	-	-	±100	nA	V _{GS} =±20V, V _{DS} =0V
I _{DSS}	-	-	1	μA	V _{DS} =80V, V _{GS} =0V
R _{DSS(ON)}	-	12.2	16	mΩ	V _{GS} =10V, I _D =10A
Dynamic					
C _{iss}	-	1280	-	pF	V _{DS} =50V, V _{GS} =0V, f=1MHz
C _{oss}	-	180	-		
C _{rss}	-	30	-		
R _g	-	0.5	-	Ω	f=1MHz
Q _g *1, 2	-	20	-	nC	V _{DS} =50V, I _D =10A, V _{GS} =10V
Q _{gs} *1, 2	-	7.4	-		
Q _{gd} *1, 2	-	4.5	-		
t _{d(ON)} *1, 2	-	17	-	ns	V _{DS} =50V, I _D =10A, V _{GS} =10V, R _{GS} =3Ω
t _r *1, 2	-	16	-		
t _{d(OFF)} *1, 2	-	25	-		
t _f *1, 2	-	6.4	-		
Source-Drain Diode					
V _{SD} *1	-	0.86	1.2	V	I _S =10A, V _{GS} =0V
t _{rr}	-	34	-	ns	I _F =10A, dI _F /dt=100A/μs
Q _{rr}	-	50	-	nC	

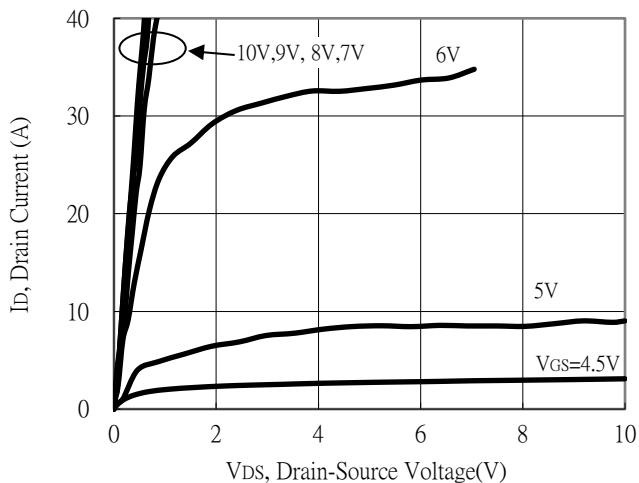
Note:

*1. Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

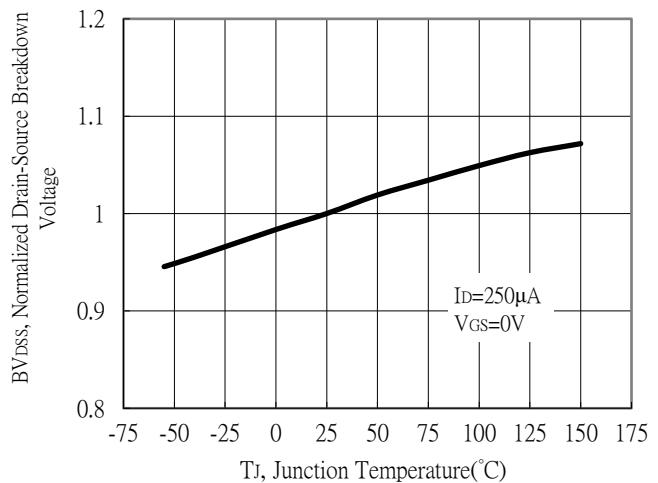
*2. Independent of operating temperature

Typical Characteristics

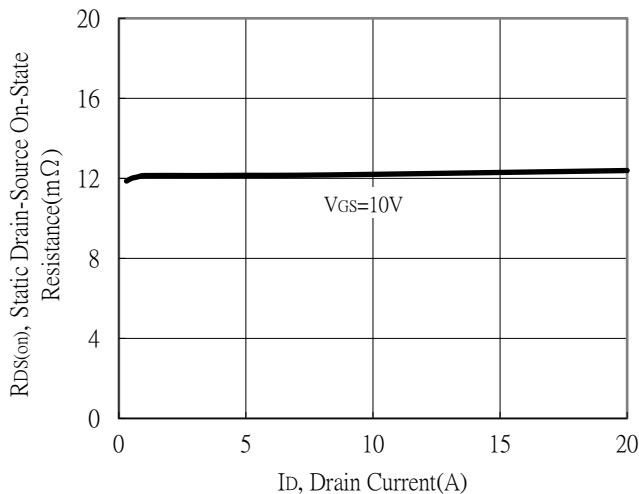
Typical Output Characteristics



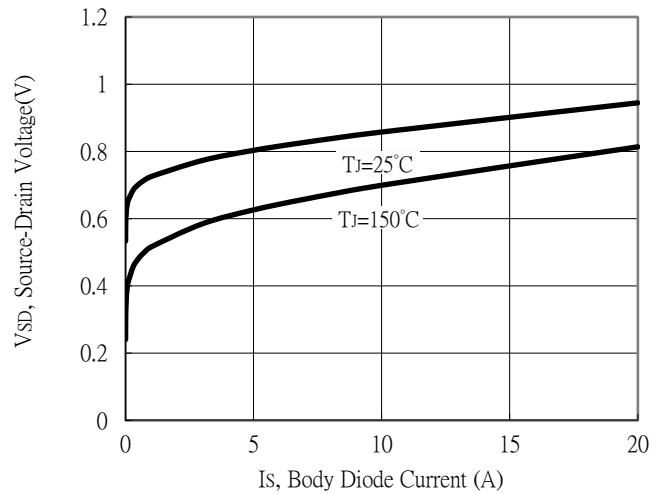
Breakdown Voltage vs Ambient Temperature



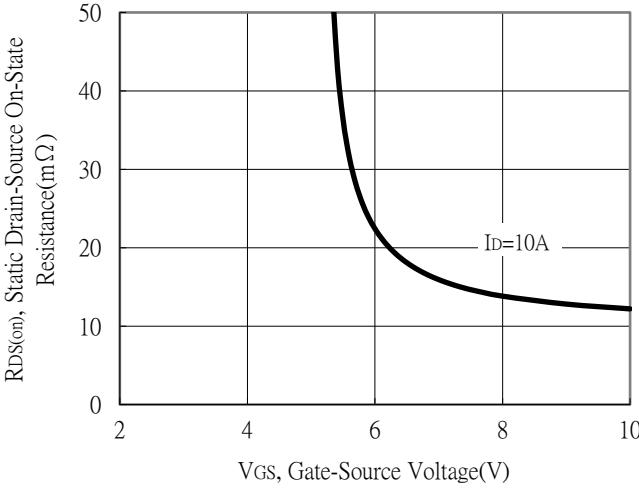
Static Drain-Source On-State resistance vs Drain Current



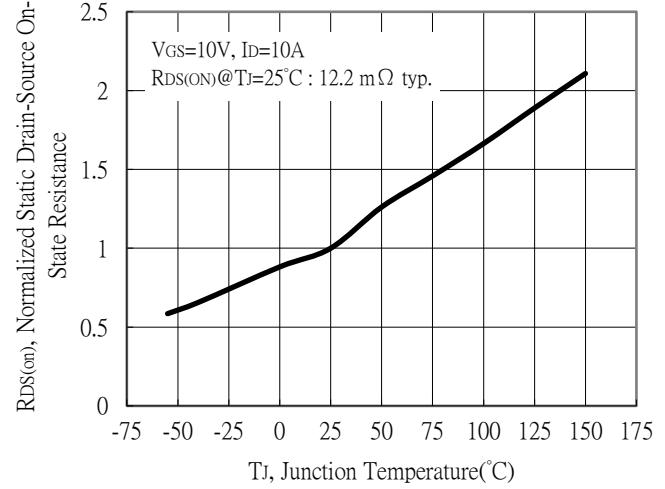
Body Diode Current vs Source-Drain Voltage



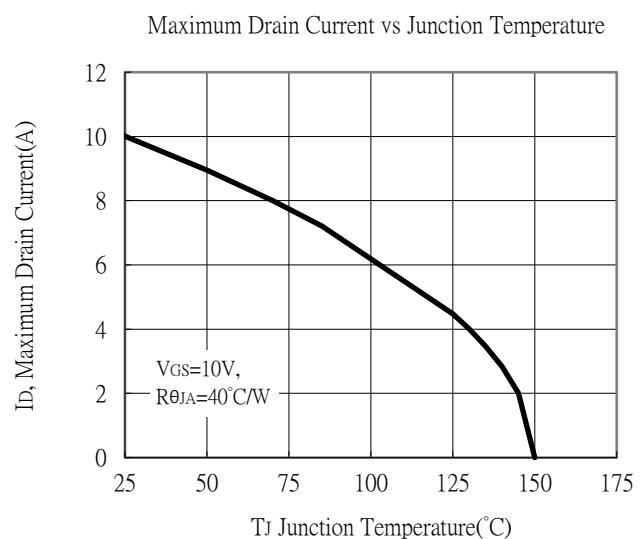
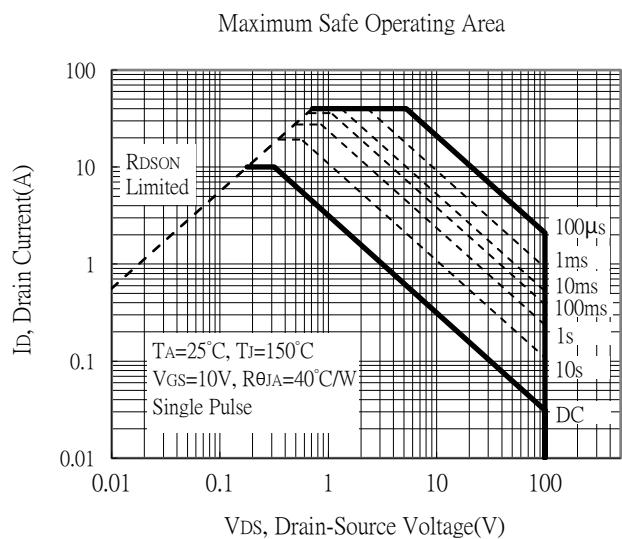
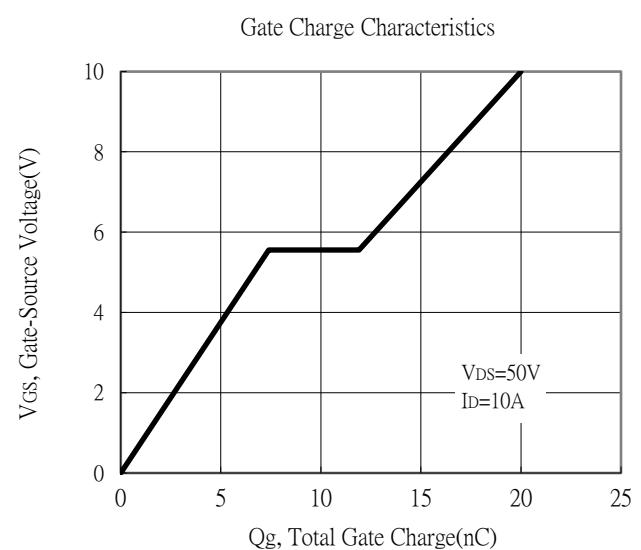
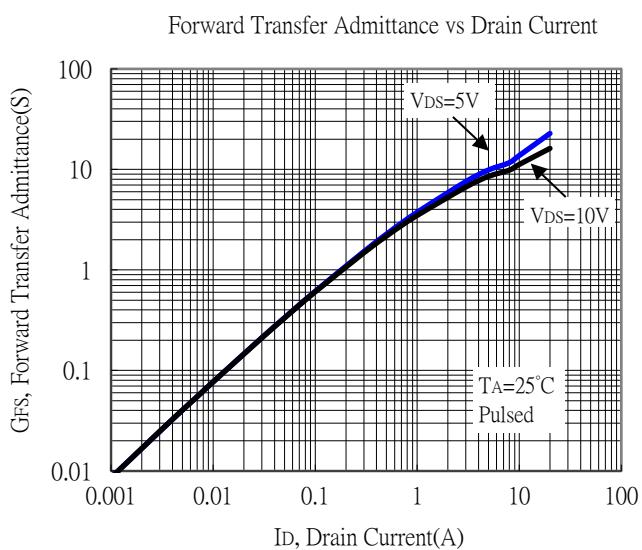
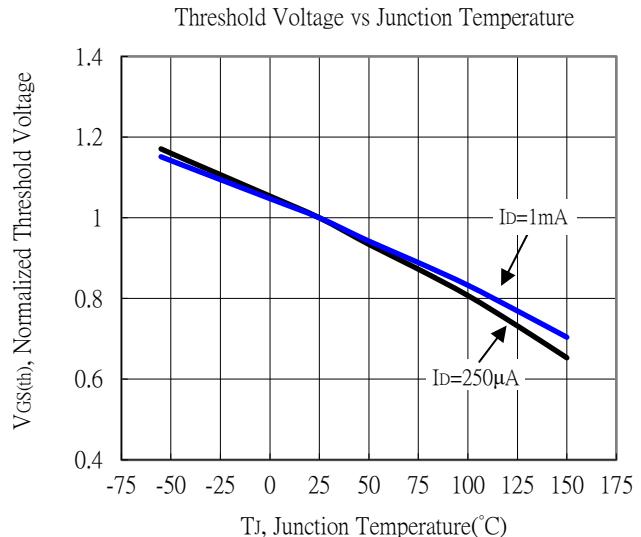
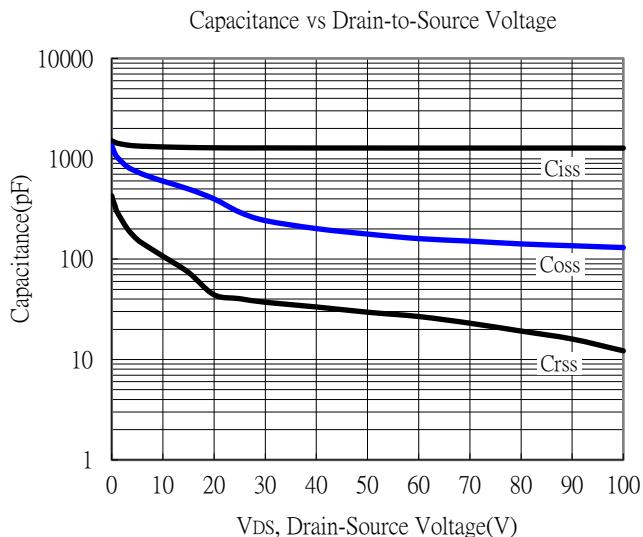
Static Drain-Source On-State Resistance vs Gate-Source Voltage



Drain-Source On-State Resistance vs Junction Temperature

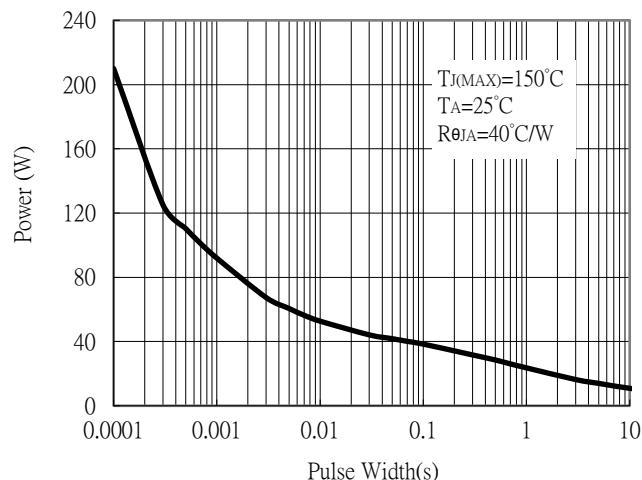


Typical Characteristics (Cont.)

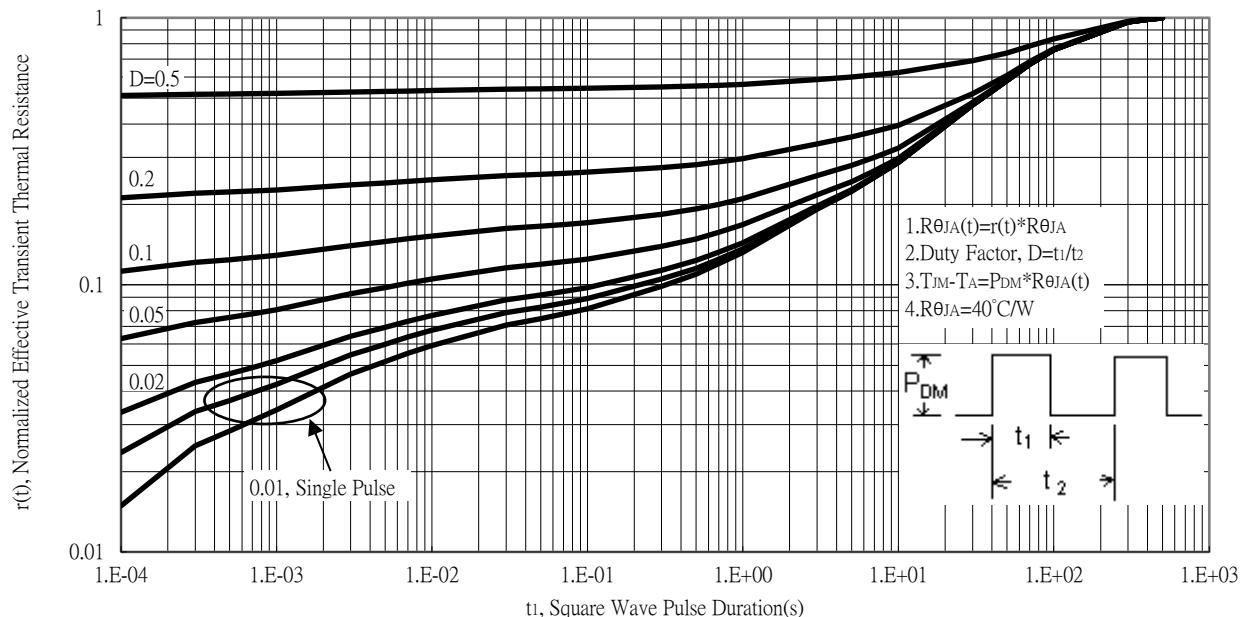


Typical Characteristics (Cont.)

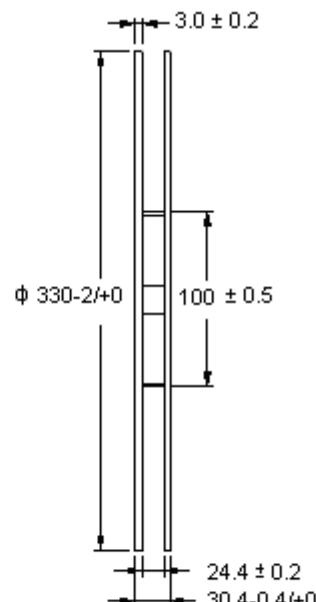
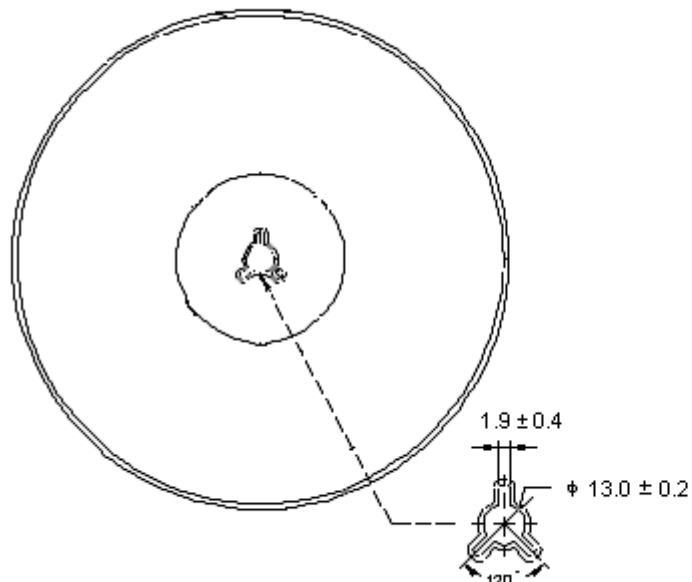
Single Pulse Power Rating, Junction to Ambient



Transient Thermal Response Curves

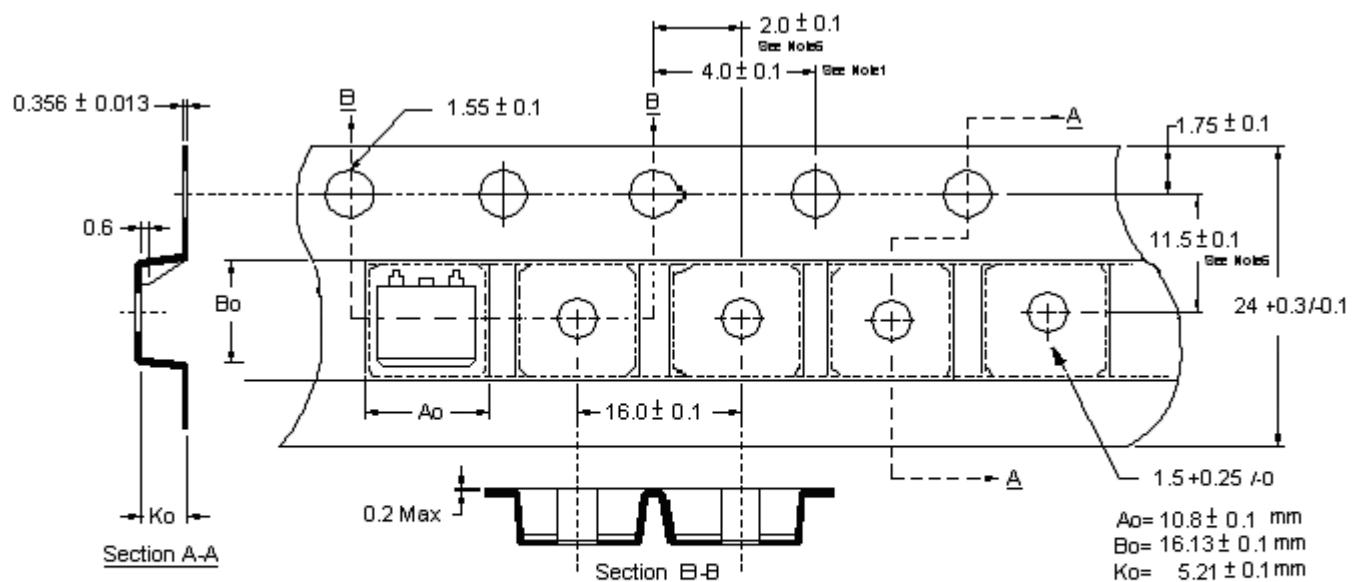


Reel Dimension



Unit: millimeter

Carrier Tape Dimension

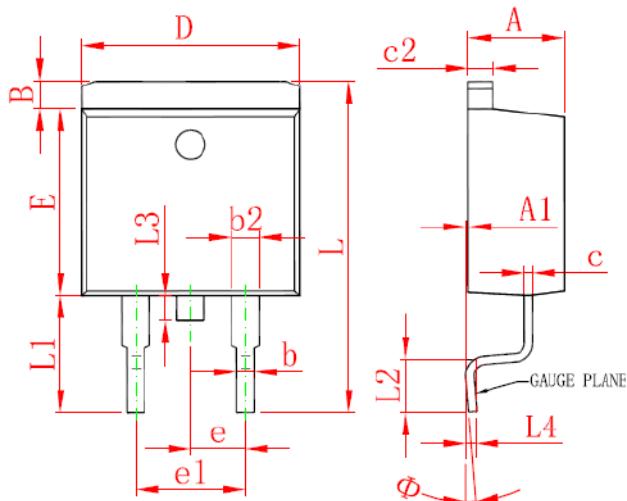


Notes:

1. 10 sprocket hole pitch cumulative tolerance ± 0.2 .
2. Camber not to exceed 1mm in 100mm.
3. Material: Conductive Black Advantek Polystyrene.
4. Ao & Bo measured on a plane 0.3mm above the bottom of the pocket.
5. Ko measured from a plane on the inside bottom of the pocket to the top surface of the carrier.
6. Pocket position relative to sprocket hole measured as a true position of pocket, not pocket hole.

Unit : millimeter

TO-263 Dimension

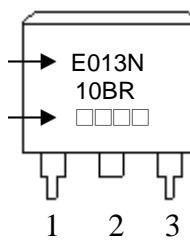


3-Lead Plastic Surface Mounted Package

Marking :

Device Code

Date Code



Style : Pin 1.Gate 2.Drain
 3.Source

Date Code(counting from left to right) :

1st code: year code, the last digit of Christian year

2nd code : month code, Jan→A, Feb→B, Mar→C, Apr→D,
May→E, Jun→F, Jul→G, Aug→H, Sep→J,
Oct→K, Nov→L, Dec→M

3rd and 4th codes : production serial number, 01~99

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	4.470	4.670	0.176	0.184	e	2.540	TYP	0.100	TYP
A1	0.000	0.150	0.000	0.006	e1	4.980	5.180	0.196	0.204
B	1.120	1.420	0.044	0.056	L	14.940	15.500	0.588	0.610
b	0.710	0.910	0.028	0.036	L1	4.950	5.450	0.195	0.215
b2	1.170	1.370	0.046	0.054	L2	2.340	2.740	0.092	0.108
c	0.381	0.530	0.015	0.021	L3	1.300	1.700	0.051	0.067
c2	1.170	1.370	0.046	0.054	L4	0.250	BSC	0.010	BSC
D	10.010	10.310	0.394	0.406	Φ	0°	8°	0°	8°
D1	7.562	8.562	0.245	0.337	V	5.880	6.880	0.231	0.271
E	8.500	8.900	0.335	0.350					