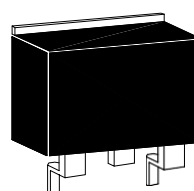


N-Channel Enhancement Mode Power MOSFET

Features:

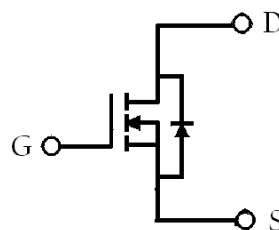
- Simple Drive Requirement
- Fast Switching Characteristic
- RoHS compliant package

TO-263



G D S

BV_{DSS}	60V
$I_D @ V_{GS}=10V, T_C=25^\circ C$	80A
$R_{DS(on)(TYP)} @ V_{GS}=10V, I_D=30A$	3.6m Ω
$R_{DS(on)(TYP)} @ V_{GS}=7V, I_D=20A$	3.8m Ω



G : Gate
D : Drain
S : Source

Ordering Information

Device	Package	Shipping
KUE3D5N06	TO-263 (Pb-free lead plating and RoHS compliant package)	800 pcs / Tape & Reel

Absolute Maximum Ratings (T_C=25°C, unless otherwise noted)

Parameter		Symbol	Limits	Unit
Drain-Source Voltage		V _{DS}	60	V
Gate-Source Voltage		V _{GS}	±30	
Continuous Drain Current @ T _C =25°C(silicon limit)		I _D	143	A
Continuous Drain Current @ T _C =100°C(silicon limit)			101	
Continuous Drain Current @ T _C =25°C(package limit) (Note 1)			80	
Pulsed Drain Current (Note 3)		I _{DM}	480	
Continuous Drain Current @ T _A =25°C (Note 2)		I _{DSM}	15.4	
Continuous Drain Current @ T _A =70°C (Note 2)			12.3	
Avalanche Current (Note 3)		I _{AS}	30	
Avalanche Energy @ L=100μH, I _D =30A, R _G =25Ω (Note 2)		E _{AS}	45	mJ
Power Dissipation	T _C =25°C (Note 1)	P _D	188	W
	T _C =100°C (Note 1)		94	
Power Dissipation	T _A =25°C (Note 2)	P _{DSM}	2	
	T _A =70°C (Note 2)		1.3	
Operating Junction and Storage Temperature		T _j , T _{stg}	-55~+175	°C

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	R _{th,j-c}	0.8	°C/W
Thermal Resistance, Junction-to-ambient, max, (Note 2)	R _{th,j-a}	62.5	

- Note : 1.The power dissipation P_D is based on T_{J(MAX)}=175 °C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
- 2.The value of R_{θJA} is measured with the device mounted on 1 in² FR-4 board with 2 oz. copper, in a still air environment with T_A=25 °C. The power dissipation P_{DSM} is based on R_{θJA} and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design, and the maximum temperature of 175°C may be used if the PCB allows it.
3. Pulse width limited by junction temperature T_{J(MAX)}=175 °C. Ratings are based on low frequency and low duty cycles to keep initial T_J=25 °C.
4. The static characteristics are obtained using <300μs pulses, duty cycle 0.5% maximum.
5. The R_{θJA} is the sum of thermal resistance from junction to case R_{θJC} and case to ambient.

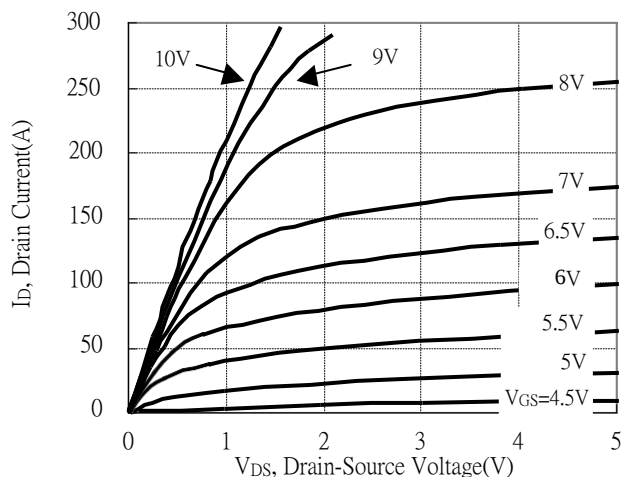
Characteristics (T_c=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	60	-	-	V	V _{GS} =0V, I _D =250μA
V _{GS(th)}	2.0	-	4.0		V _{DS} = V _{GS} , I _D =250μA
G _{FS}	-	46	-	S	V _{DS} =5V, I _D =20A
I _{GSS}	-	-	±100	nA	V _{GS} =±30V
I _{DSS}	-	-	1	μA	V _{DS} =60V, V _{GS} =0V
	-	-	25		V _{DS} =60V, V _{GS} =0V, T _j =125°C
*R _{DS(ON)}	-	3.6	4.8	mΩ	V _{GS} =10V, I _D =30A
	-	3.8	5.0		V _{GS} =7V, I _D =20A
Dynamic					
*Q _g	-	68.3	-	nC	I _D =30A, V _{DS} =30V, V _{GS} =10V
*Q _{gs}	-	11	-		
*Q _{gd}	-	28	-		
*t _{d(ON)}	-	24	-	ns	V _{DS} =30V, I _D =30A, V _{GS} =10V, R _G =1Ω
*tr	-	25.2	-		
*t _{d(OFF)}	-	56.2	-		
*t _f	-	17.8	-		
C _{iss}	-	2919	-	pF	V _{GS} =0V, V _{DS} =25V, f=1MHz
C _{oss}	-	585	-		
C _{rss}	-	270	-		
R _g	-	1.1	-	Ω	f=1MHz
Source-Drain Diode					
*I _S	-	-	80	A	
*I _{SM}	-	-	480		
*V _{SD}	-	0.81	1.2	V	I _S =30A, V _{GS} =0V
*trr	-	28.7	-	ns	I _F =30A, V _{GS} =0V, dI _F /dt=100A/μs
*Q _{rr}	-	26.2	-	nC	

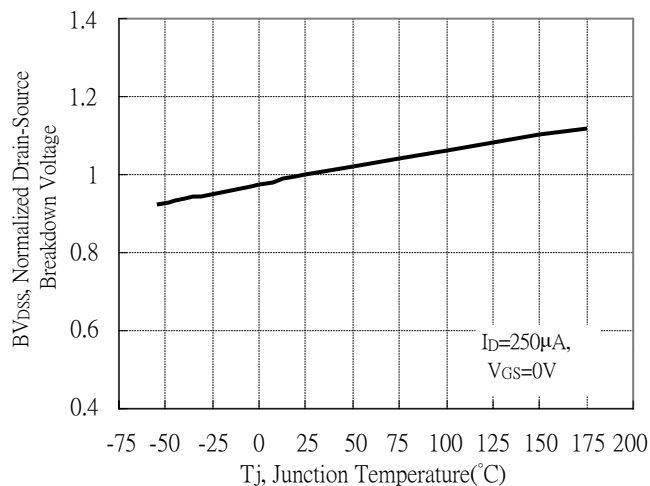
*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

Typical Characteristics

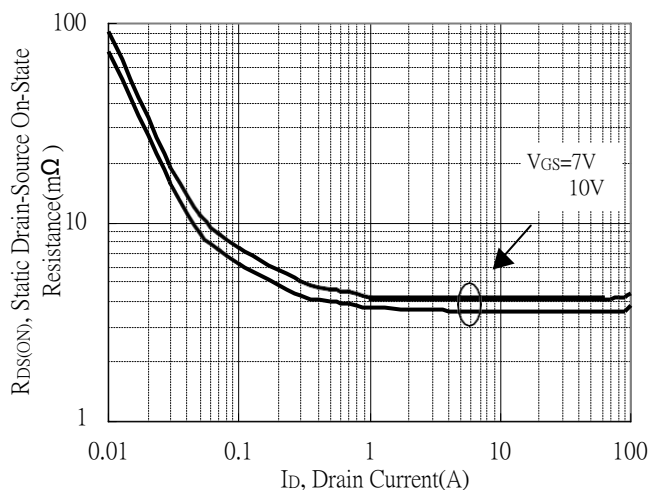
Typical Output Characteristics



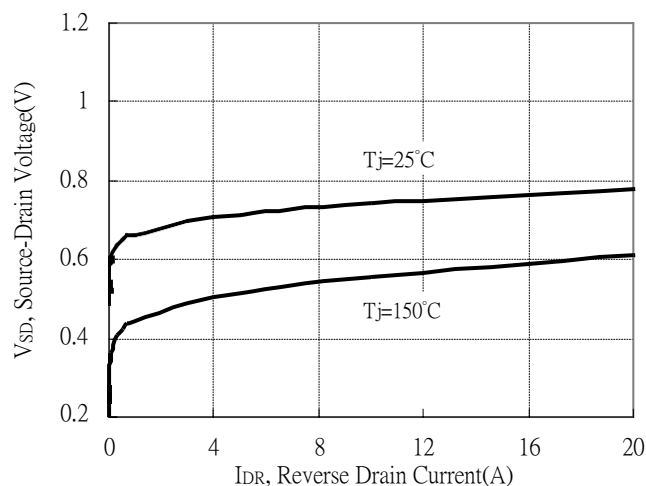
Breakdown Voltage vs Ambient Temperature



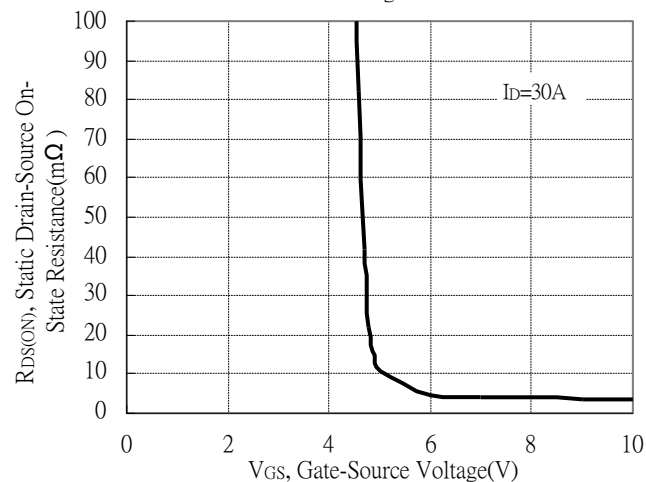
Static Drain-Source On-State resistance vs Drain Current



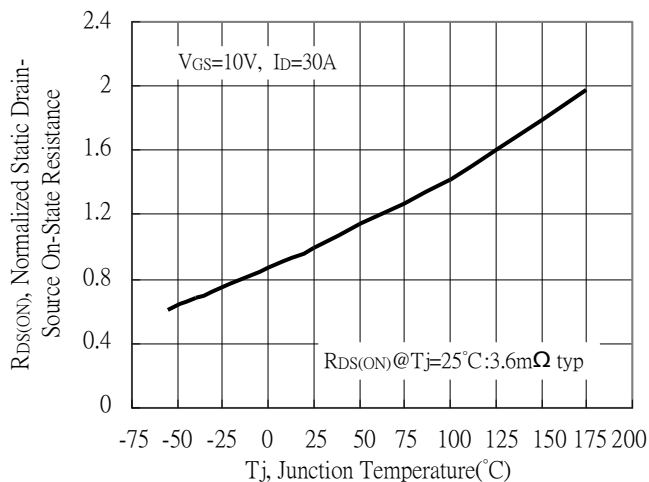
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

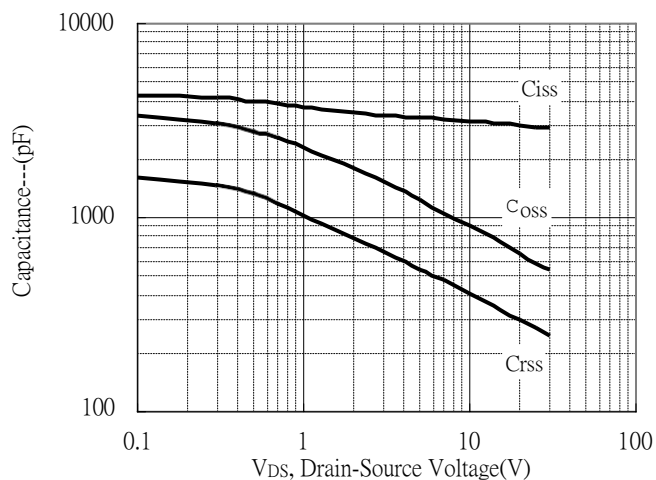


Drain-Source On-State Resistance vs Junction Temperature

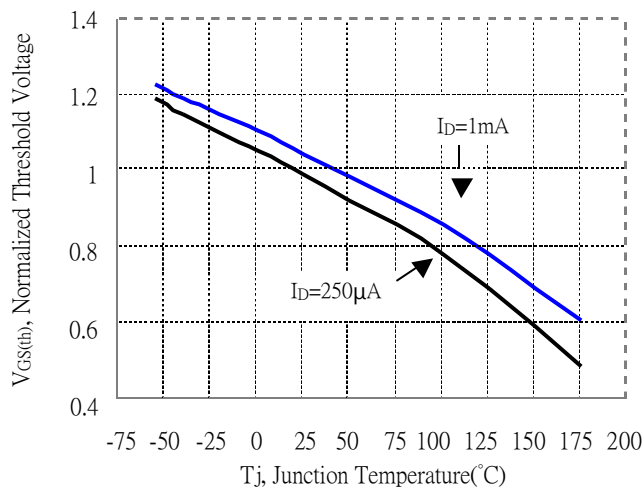


Typical Characteristics(Cont.)

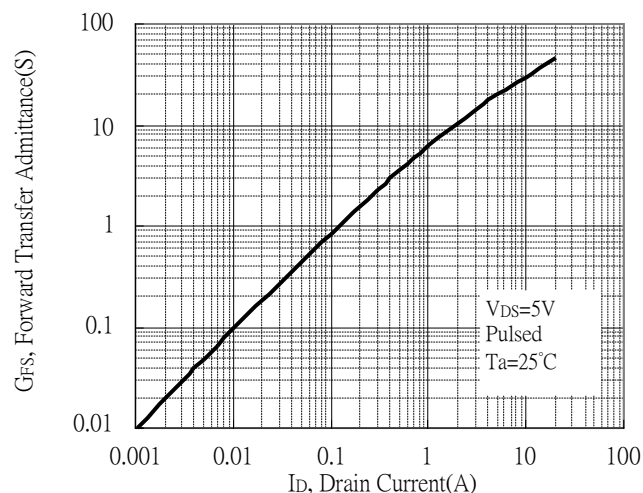
Capacitance vs Drain-to-Source Voltage



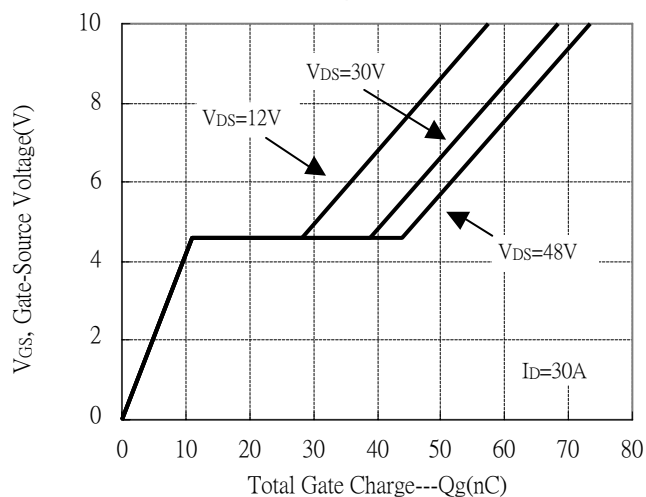
Normalized Threshold Voltage vs Junction Temperature



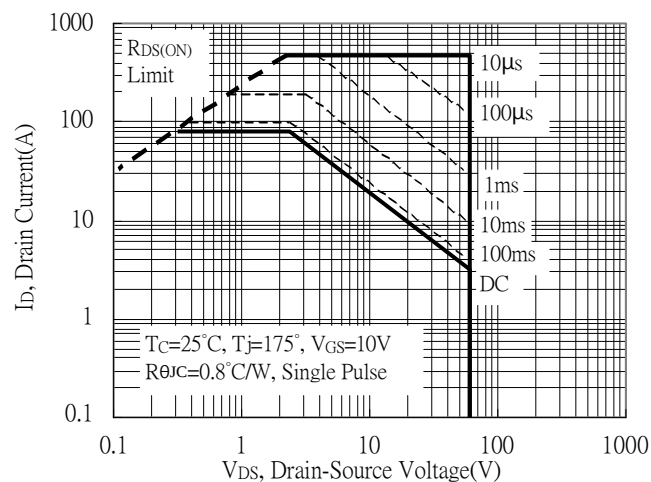
Forward Transfer Admittance vs Drain Current



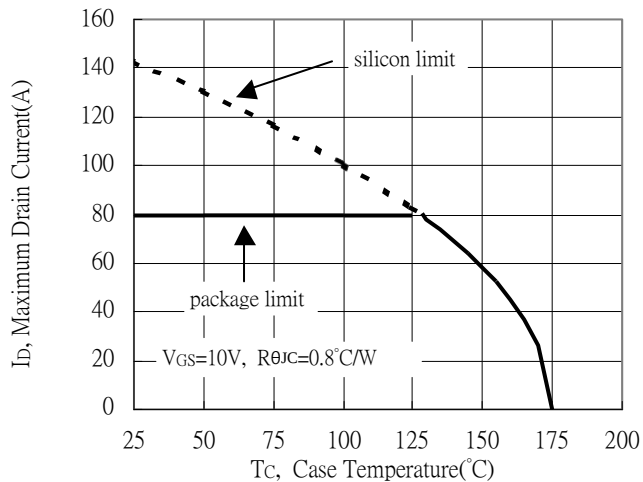
Gate Charge Characteristics



Maximum Safe Operating Area

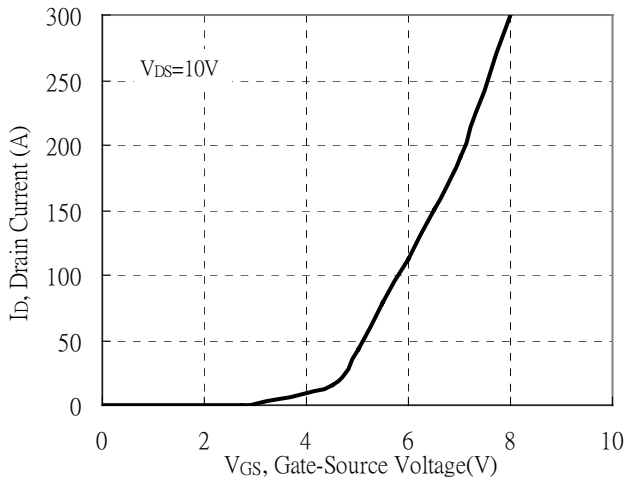


Maximum Drain Current vs Case Temperature

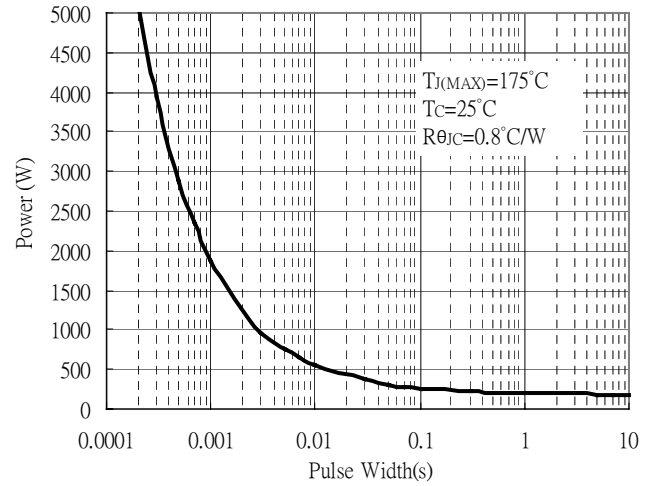


Typical Characteristics(Cont.)

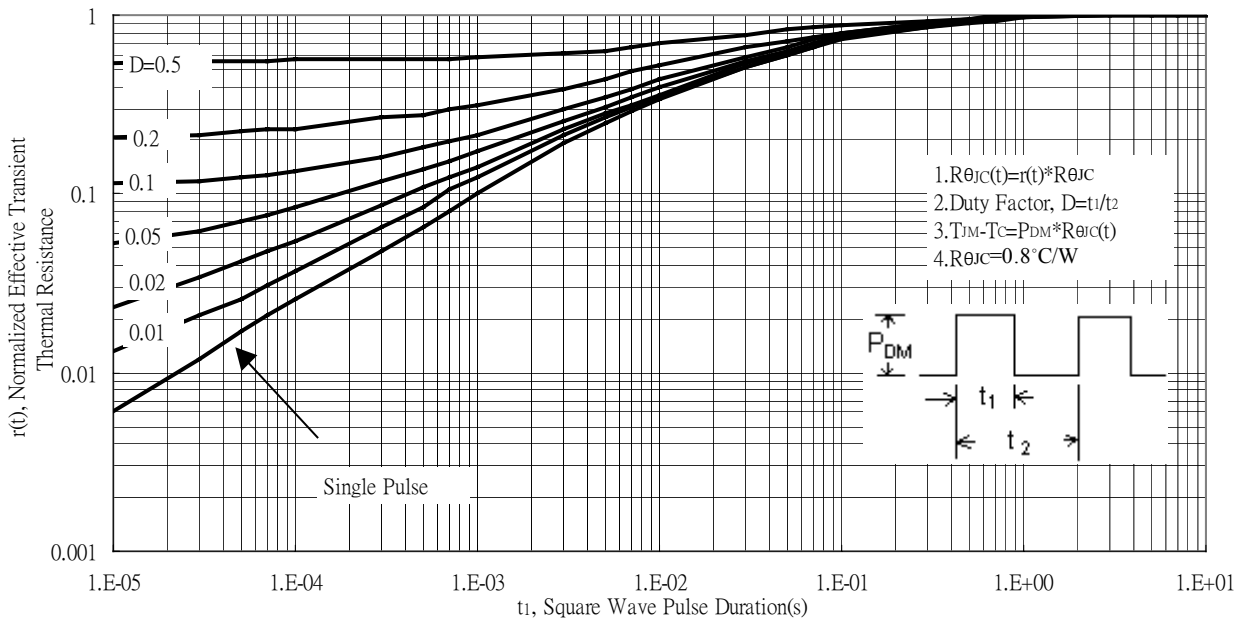
Typical Transfer Characteristics



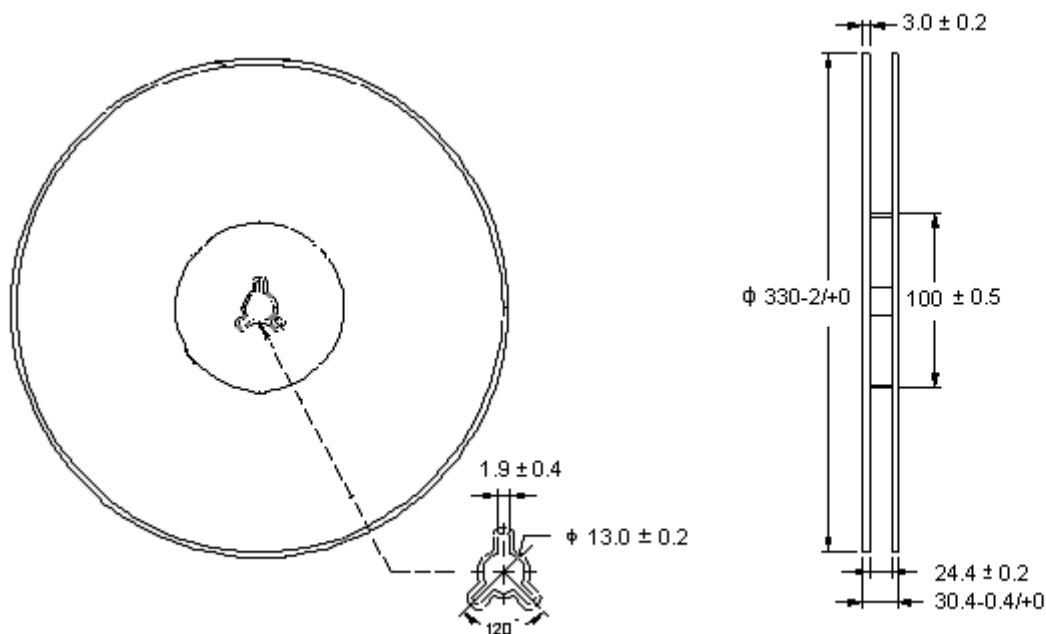
Single Pulse Maximum Power Dissipation



Transient Thermal Response Curves

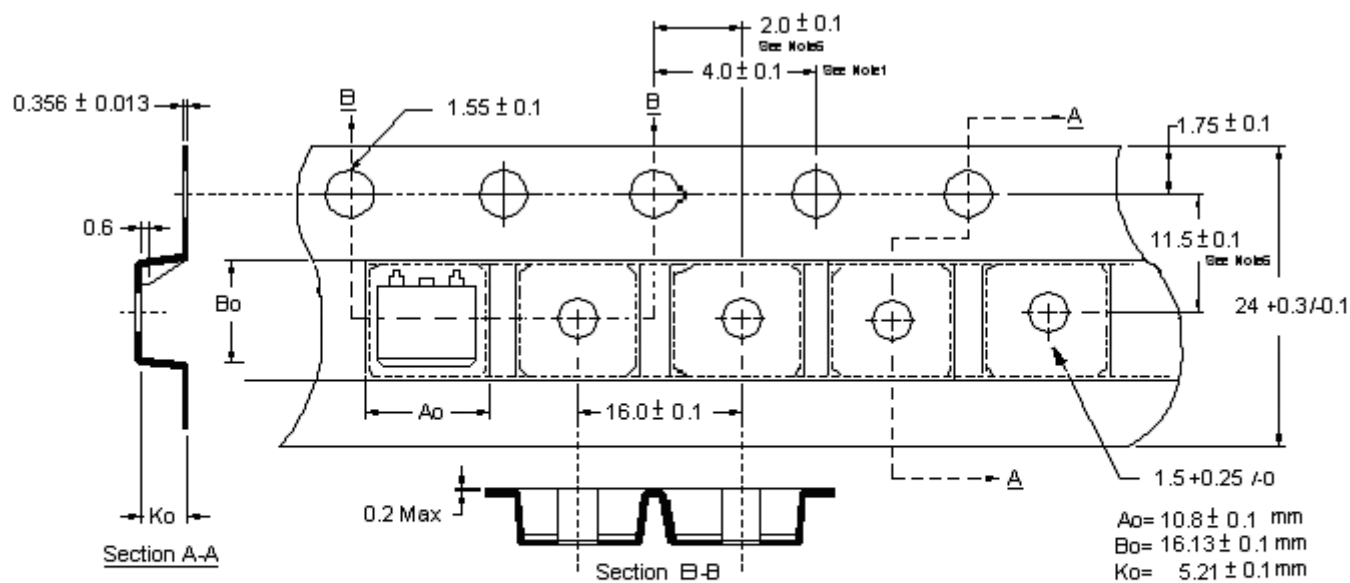


Reel Dimension

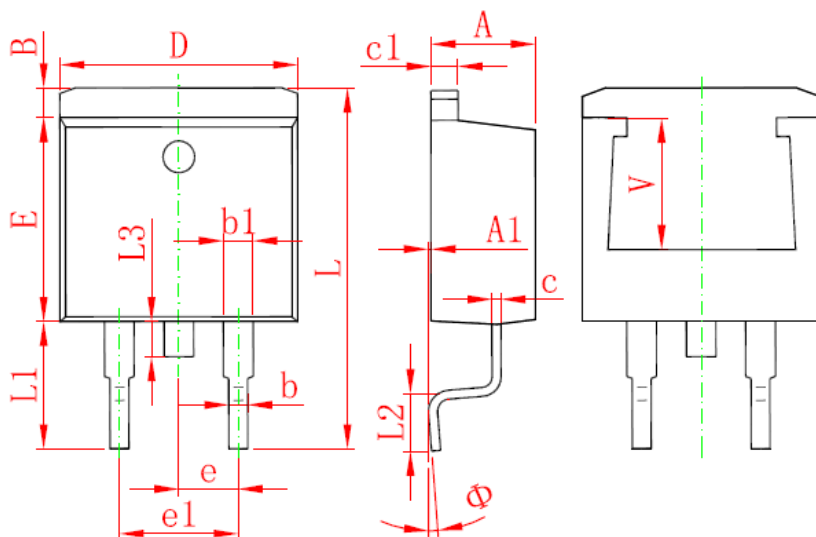


Unit: millimeter

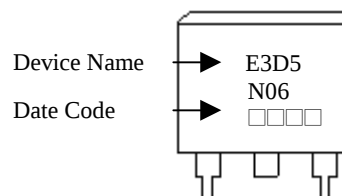
Carrier Tape Dimension



TO-263 Dimension



Marking :



Style : Pin 1.Gate 2.Drain
 3.Source

3-Lead Plastic Surface Mounted Package

Date Code : (From left to right)

First Code : Year code, the last digit of Christinr year. For example, 2014→4, 2015→, 2016→6, ..., etc.

Second Code : Month code, Jan→A, Feb→B, Mar→C, Apr→D, May→E, Jun→F, Jul→G, Aug→H, Sep→J,
 Oct→K, Nov→L, Dec→M

Third and fourth codes : production serial number, 01~99

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	4.470	4.670	0.176	0.184	e	2.540 TYP.		0.100 TYP	
A1	0.000	0.150	0.000	0.010	e1	4.980	5.180	e1	4.980
B	1.120	1.420	0.044	0.056	L	14.940	15.500	L	14.940
b	0.710	0.910	0.028	0.036	L1	4.950	5.450	L1	4.950
b1	1.170	1.370	0.046	0.054	L2	2.340	2.740	L2	2.340
c	0.310	0.530	0.012	0.021	L3	1.300	1.700	L3	1.300
c1	1.170	1.370	0.046	0.054	φ	0°	8°	φ	0°
D	10.010	10.310	0.394	0.406	V	5.600 REF		0.220 REF	
E	8.500	8.900	0.335	0.350					