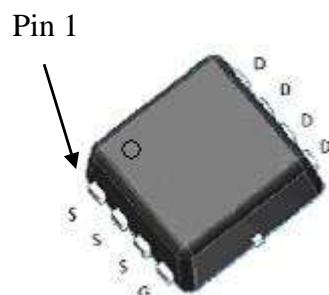


P-Channel Enhancement Mode Power MOSFET

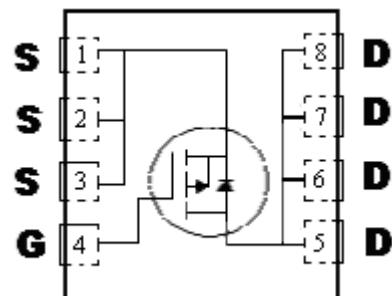
Features:

- Simple drive requirement
- Low on-resistance
- Fast switching speed
- Pb-free lead plating package

DFN3x3



BV_{DSS}	-30V
$I_D @ T_c=25^\circ C, V_{GS}=-10V$	-44.0A
$I_D @ T_a=25^\circ C, V_{GS}=-10V$	-12A
$R_{DS(on)(MAX)} @ V_{GS}=-10V, I_D=-10A$	8.6 m Ω (typ.)
$R_{DS(on)(MAX)} @ V_{GS}=-4.5V, I_D=-8A$	18.6m Ω (typ.)



G : Gate S : Source D : Drain

Ordering Information

Device	Package	Shipping
KSPRD9D0P03	DFN3x3 (Pb-free lead plating and halogen-free package)	3000 pcs / Tape & Reel

Absolute Maximum Ratings ($T_a=25^\circ C$)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 25	
Continuous Drain Current @ $T_c=25^\circ C$, $V_{GS}=-10V$	I_D	-44.0	A
Continuous Drain Current @ $T_c=100^\circ C$, $V_{GS}=-10V$		-27.8	
Continuous Drain Current @ $T_a=25^\circ C$, $V_{GS}=-10V$		-12	
Continuous Drain Current @ $T_a=70^\circ C$, $V_{GS}=-10V$		-9.6	
Pulsed Drain Current	I_{DM}	-176 *1	
Avalanche Current @ $L=0.1mH$	I_{AS}	-40	
Avalanche Energy @ $L=1mH$, $I_D=-24A$, $V_{DD}=-15V$	E_{AS}	288 *4	mJ
Repetitive Avalanche Energy @ $L=0.05mH$	E_{AR}	4 *2	
Total Power Dissipation	$T_c=25^\circ C$	36	W
	$T_c=100^\circ C$	14	
	$T_a=25^\circ C$	2.5 *3	
	$T_a=70^\circ C$	1.6 *3	
Operating Junction and Storage Temperature Range	T_j , T_{stg}	-55~+150	$^\circ C$

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	$R_{\theta JC}$	3.5	$^\circ C/W$
Thermal Resistance, Junction-to-ambient, max	$R_{\theta JA}$	50 *3	

Note : 1. Pulse width limited by maximum junction temperature
 2. Duty cycle $\leq 1\%$
 3. Surface mounted on 1 in² copper pad of FR-4 board, $t \leq 10s$; $125^\circ C/W$ when mounted on minimum copper pad.
 4. 100% tested by conditions of $L=0.1mH$, $I_{AS}=-10A$, $V_{GS}=-10V$, $V_{DD}=-15V$

Electrical Characteristics ($T_j=25^\circ C$, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV_{DSS}	-30	-	-	V	$V_{GS}=0V$, $I_D=-250\mu A$
$V_{GS(th)}$	-1.5	-	-3.0		$V_{DS}=V_{GS}$, $I_D=-250\mu A$
I_{GSS}	-	-	± 100	nA	$V_{GS}=\pm 25V$, $V_{DS}=0V$
ID_{SS}	-	-	-1	μA	$V_{DS}=-24V$, $V_{GS}=0V$
	-	-	-10		$V_{DS}=-24V$, $V_{GS}=0V$, $T_j=125^\circ C$
$R_{DS(ON)} *1$	-	8.6	12	$m\Omega$	$V_{GS}=-10V$, $I_D=-10A$
	-	18.6	25		$V_{GS}=-4.5V$, $I_D=-8A$
$G_{FS} *1$	-	21	-	S	$V_{DS}=-5V$, $I_D=-10A$

Electrical Characteristics (Cont.) ($T_j=25^\circ\text{C}$, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Dynamic *4					
C _{iss}	-	2510	-	pF	$V_{DS}=-15\text{V}, V_{GS}=0\text{V}, f=1\text{MHz}$
C _{oss}	-	353	-		
C _{rss}	-	316	-		
t _{d(ON)} *1, 2	-	21.2	-		
t _r *1, 2	-	25	-		
t _{d(OFF)} *1, 2	-	89.8	-		
t _f *1, 2	-	26.6	-		
Q _g *1, 2	-	48.9	-		
Q _{gs} *1, 2	-	9.2	-		
Q _{gd} *1, 2	-	12.2	-	nC	$V_{DS}=-15\text{V}, I_D=-13\text{A}, V_{GS}=-10\text{V}$
R _g	-	4	-		
Source-Drain Diode					
I _S *1	-	-	-4	A	$I_F=-1\text{A}, V_{GS}=0\text{V}$
I _{SM} *3	-	-	-16		
V _{SD} *1	-	-0.74	-1	V	$I_F=-13\text{A}, dI_F/dt=100\text{A}/\mu\text{s}$
t _{rr}	-	17.8	-	ns	
Q _{rr}	-	9.7	-	nC	

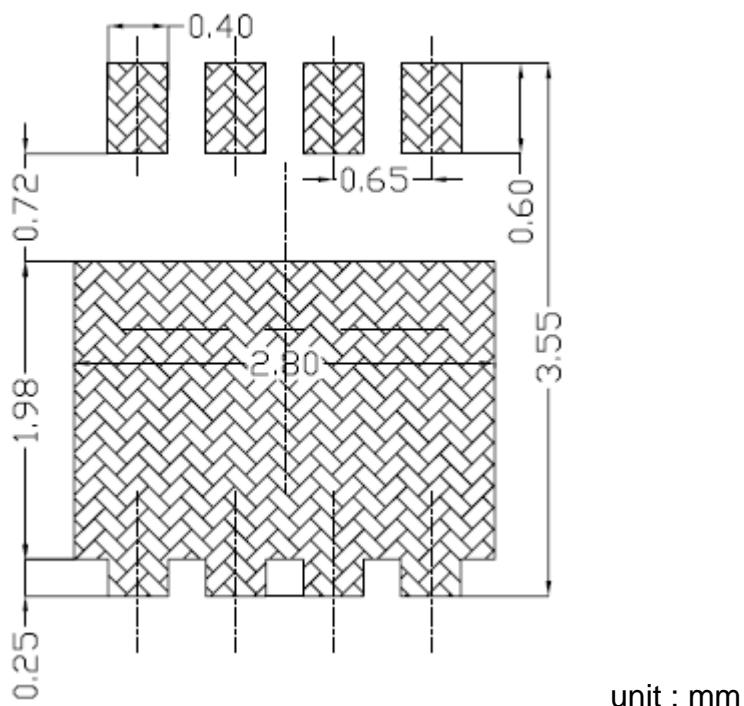
Note : *1.Pulse Test : Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$

*2.Independent of operating temperature

*3.Pulse width limited by maximum junction temperature.

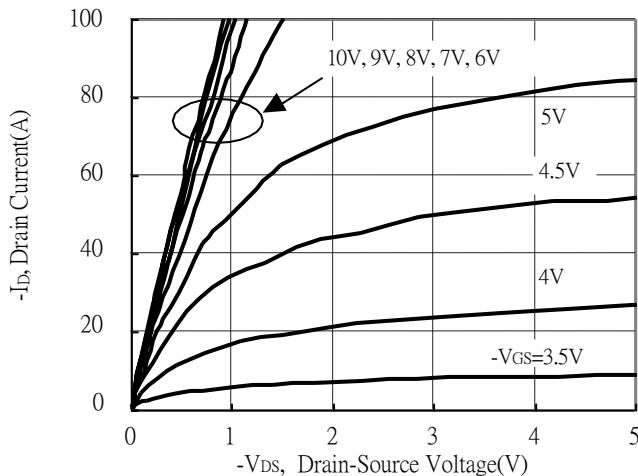
*4.Guaranteed by design, not subject to production testing.

Recommended Soldering Footprint

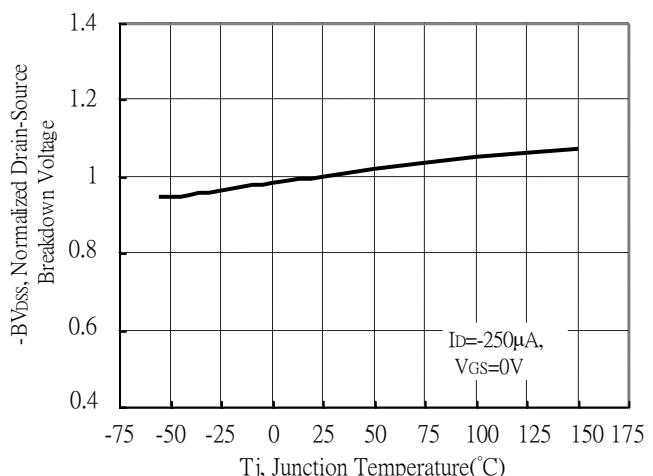


Typical Characteristics

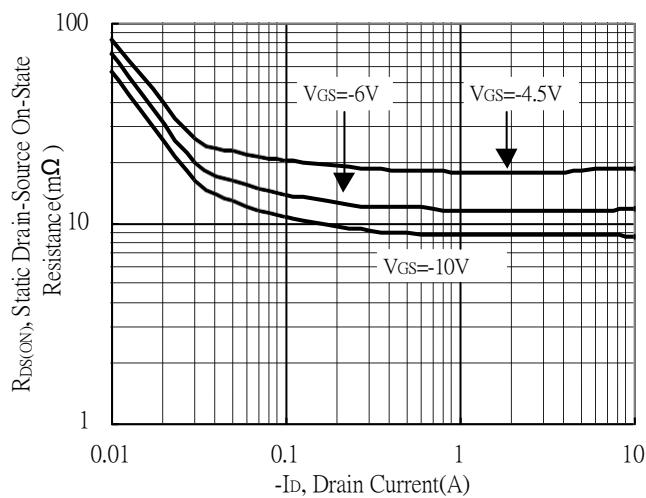
Typical Output Characteristics



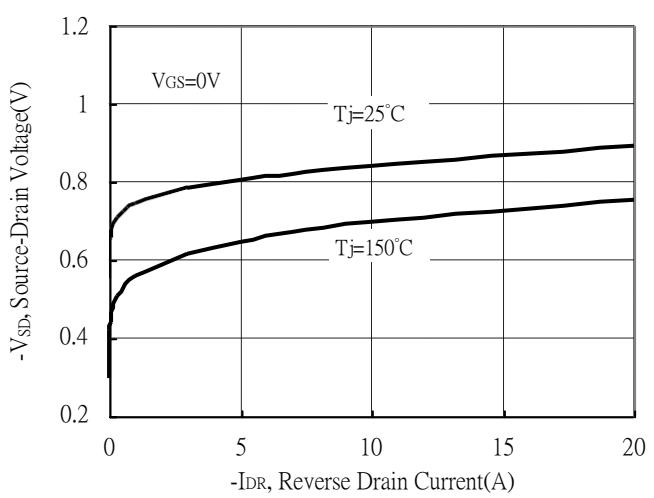
Breakdown Voltage vs Ambient Temperature



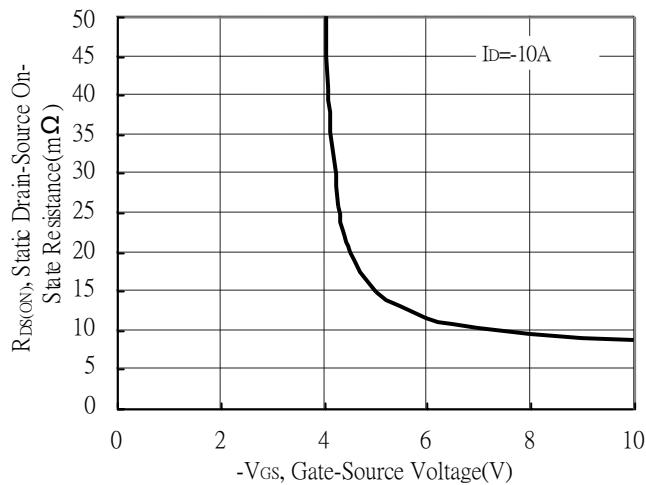
Static Drain-Source On-State resistance vs Drain Current



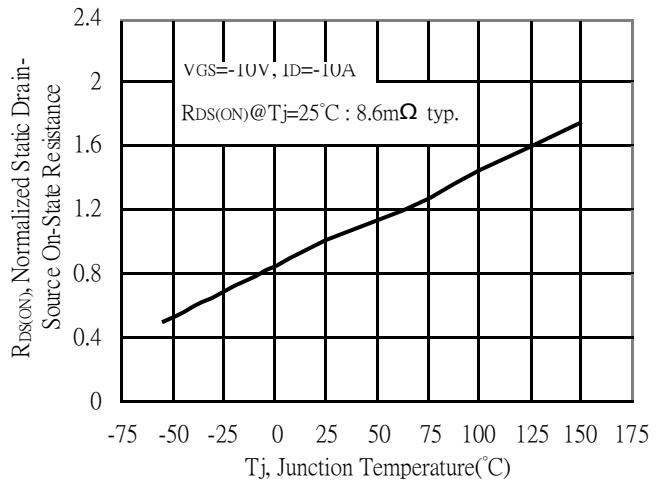
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

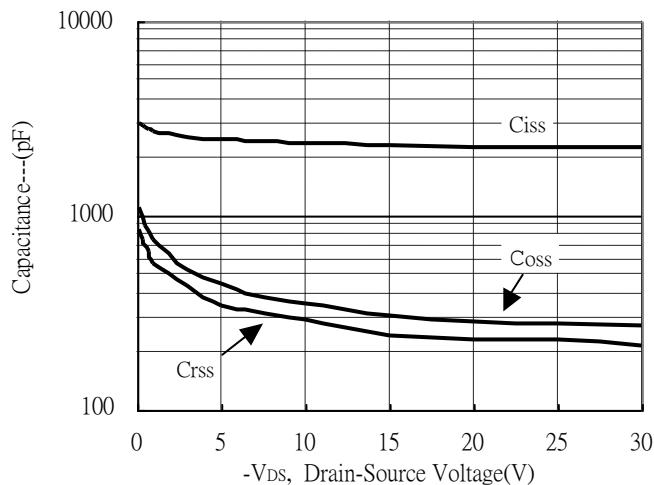


Drain-Source On-State Resistance vs Junction Temperature

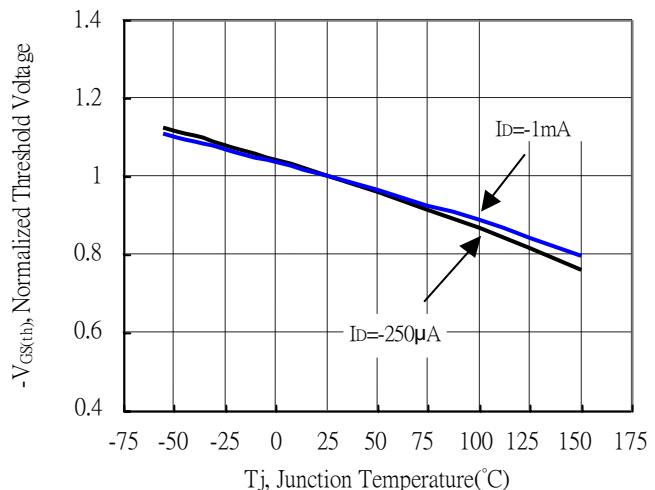


Typical Characteristics(Cont.)

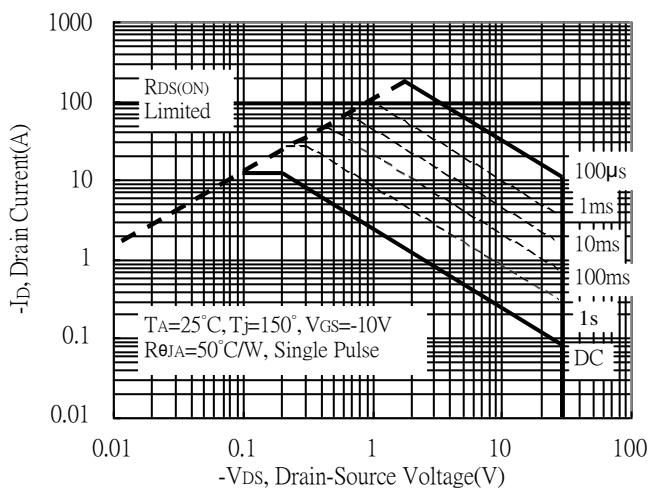
Capacitance vs Drain-to-Source Voltage



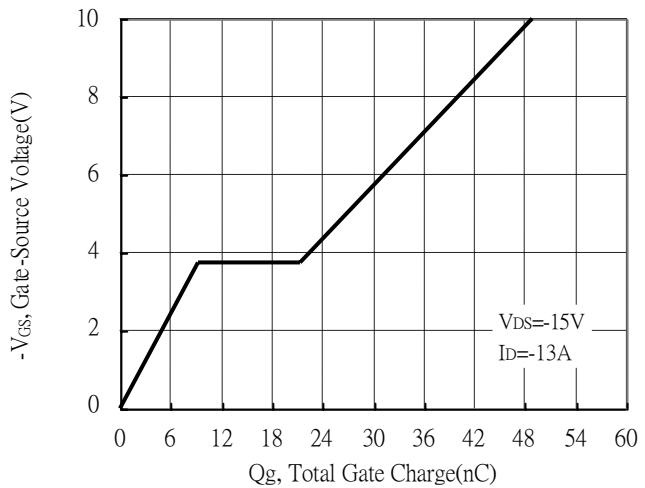
Threshold Voltage vs Junction Temperature



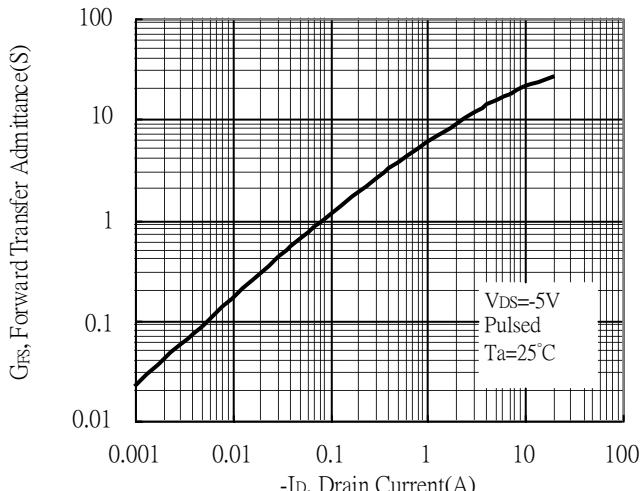
Maximum Safe Operating Area



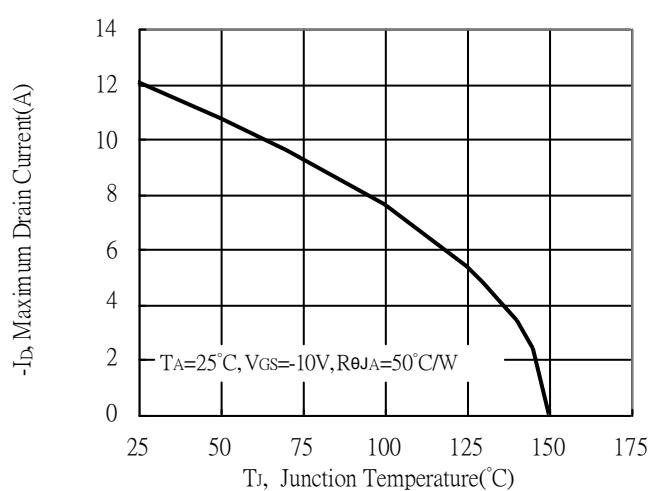
Gate Charge Characteristics



Forward Transfer Admittance vs Drain Current

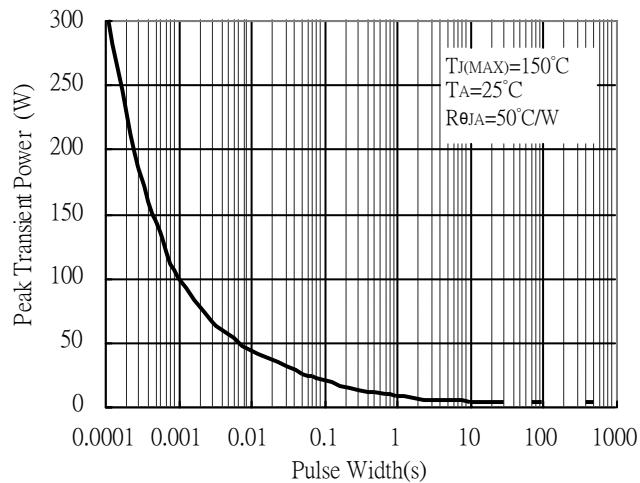


Maximum Drain Current vs Junction Temperature

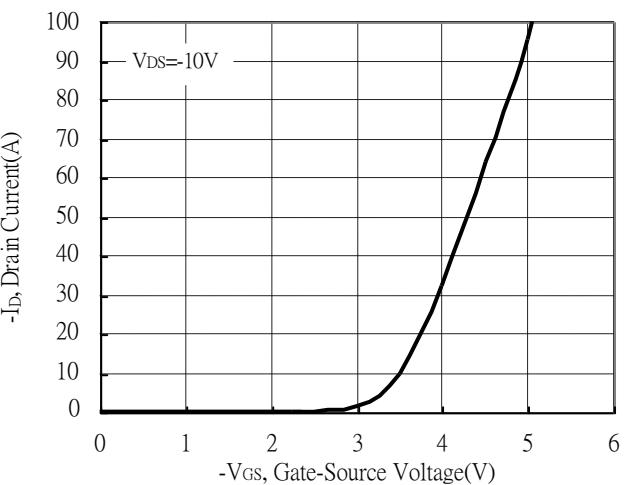


Typical Characteristics(Cont.)

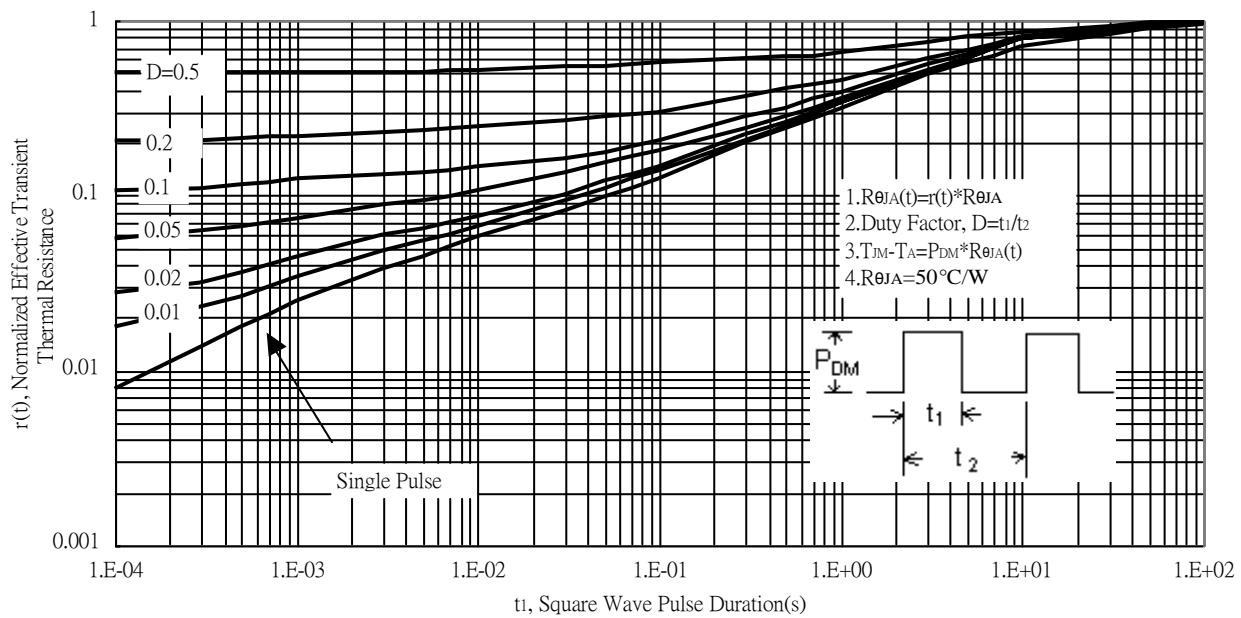
Single Pulse Maximum Power Dissipation



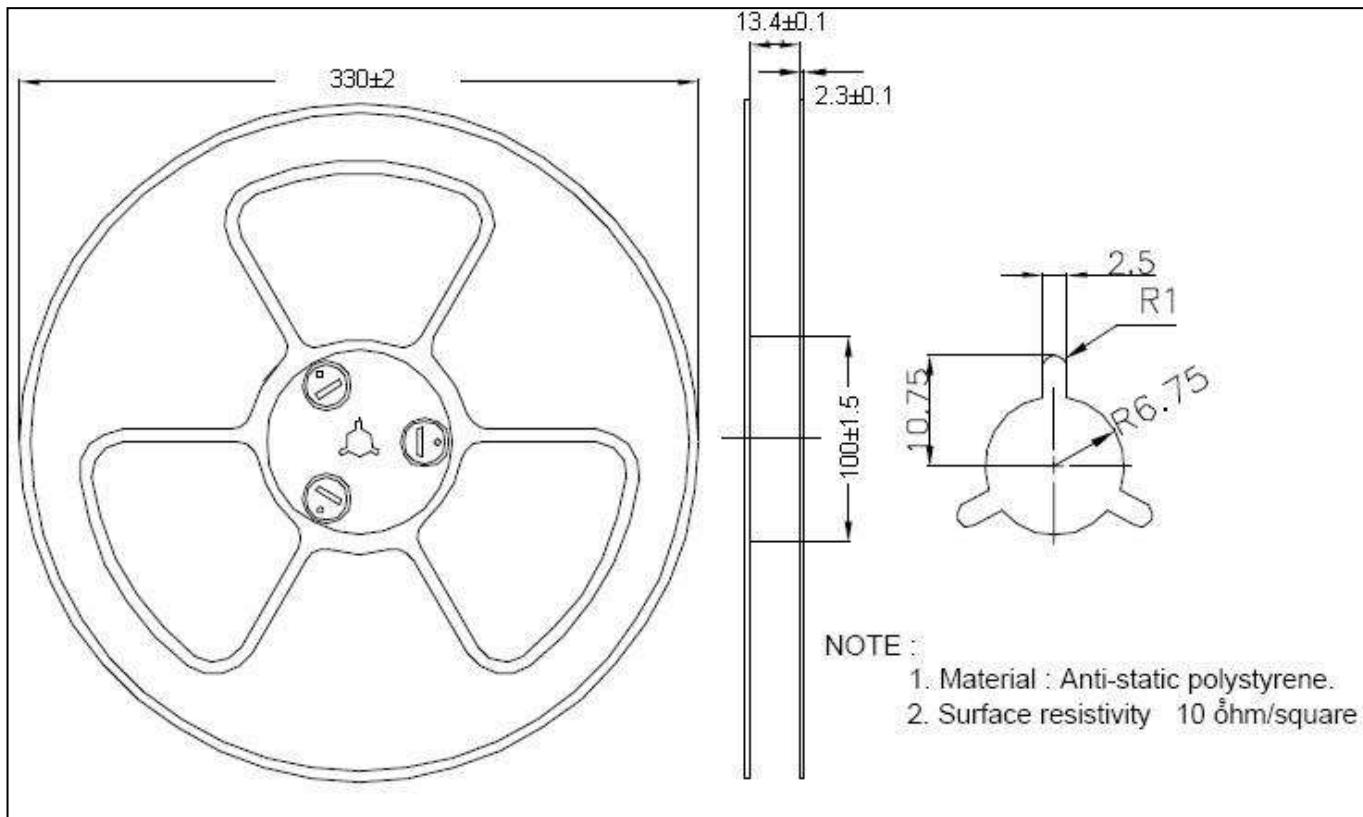
Typical Transfer Characteristics



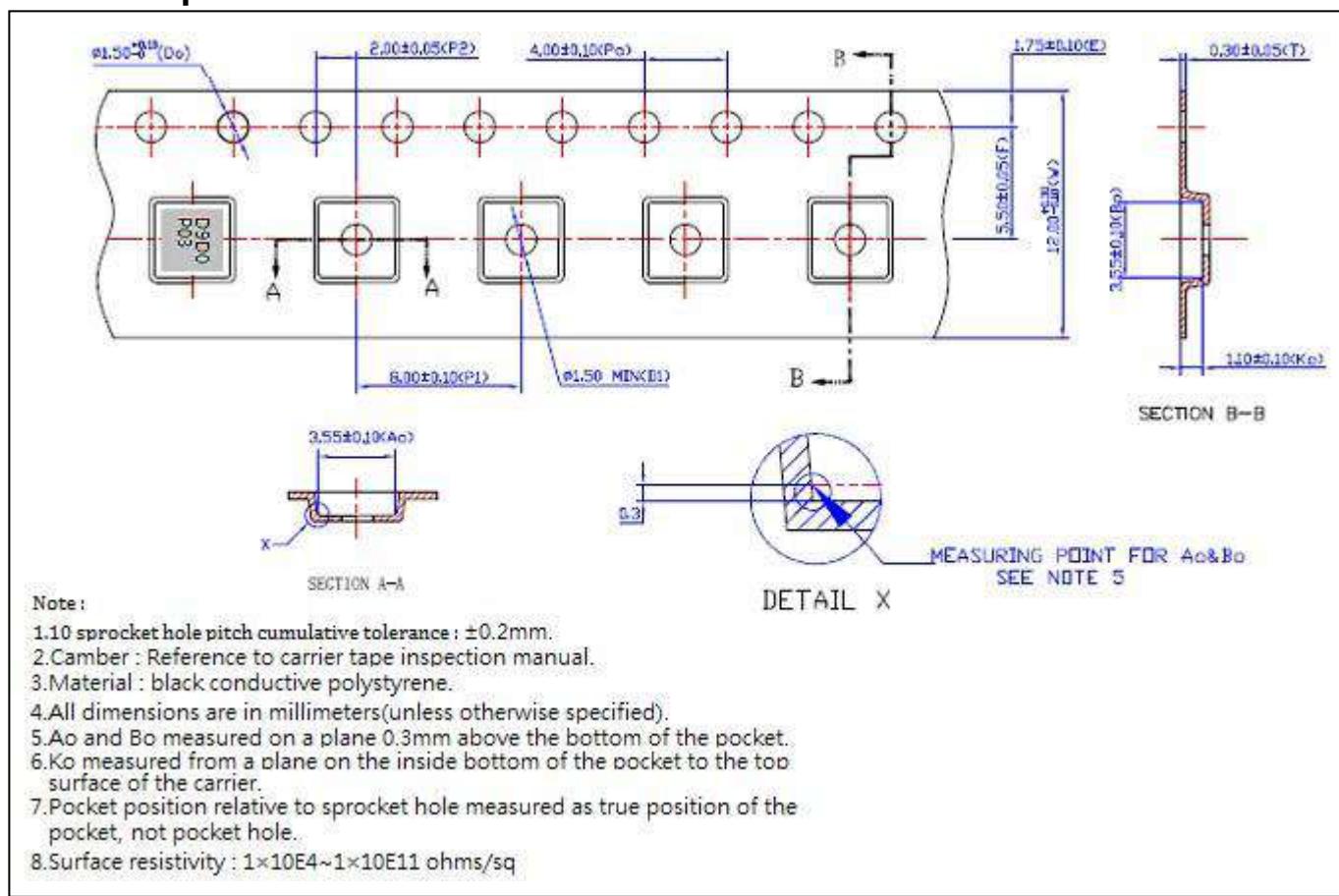
Transient Thermal Response Curves



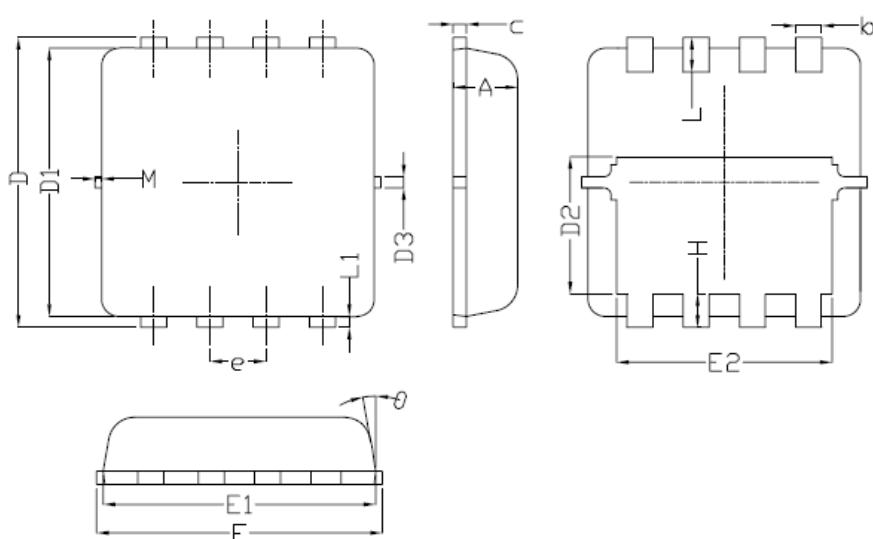
Reel Dimension



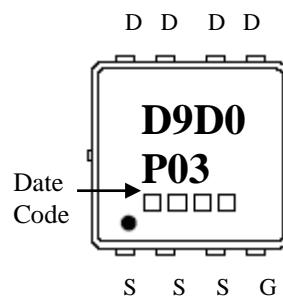
Carrier Tape Dimension



DFN3x3 Dimension



Marking:



8-Lead DFN3x3 Plastic Package

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.70	0.80	0.028	0.031	E1	3.00	3.20	0.118	0.126
b	0.25	0.35	0.010	0.014	E2	2.39	2.59	0.094	0.102
c	0.10	0.25	0.004	0.010	e	0.65	BSC	0.026	BSC
D	3.25	3.45	0.128	0.136	H	0.30	0.50	0.012	0.020
D1	3.00	3.20	0.118	0.126	L	0.30	0.50	0.012	0.020
D2	1.48	1.68	0.058	0.066	L1	0.13	TYP	0.005	TYP
D3	0.13 TYP		0.005	TYP	θ	8°	12°	8°	12°
E	3.20	3.40	0.126	0.134	M	-	0.15	-	0.006