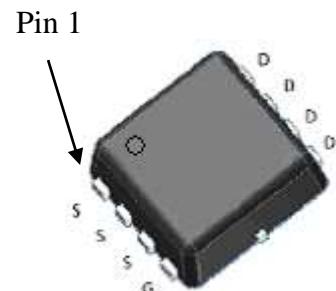


## N-Channel Logic Level Enhancement Mode Power MOSFET

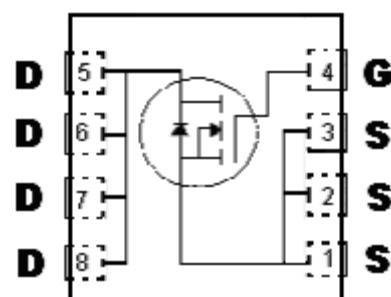
### Features:

- Single Drive Requirement
- Low On-resistance
- Fast Switching Characteristic
- Pb-free lead plating and halogen-free package

DFN3x3



BV <sub>DSS</sub>	14V	
I <sub>D</sub> @ T <sub>C</sub> =25°C, V <sub>GS</sub> =4.5V	32.7A	
I <sub>D</sub> @ T <sub>A</sub> =25°C, V <sub>GS</sub> =4.5V	14.6A	
R <sub>DSON</sub> (TYP)	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	3.3mΩ
	V <sub>GS</sub> =4.5V, I <sub>D</sub> =20A	4.0mΩ
	V <sub>GS</sub> =3.5V, I <sub>D</sub> =5A	4.2mΩ



G : Gate   D : Drain   S : Source

### Ordering Information

Device	Package	Shipping
KSPRA3D0N01	DFN3x3 (Pb-free lead plating and halogen-free package)	3000 pcs / Tape & Reel



### Absolute Maximum Ratings (Ta=25°C, unless otherwise specified)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V <sub>DS</sub>	14	V
Gate-Source Voltage	V <sub>GS</sub>	±10	
Continuous Drain Current @ V <sub>GS</sub> =4.5V, Tc=25°C	I <sub>D</sub>	32.7	A
Continuous Drain Current @ V <sub>GS</sub> =4.5, Tc=100°C		20.7	
Continuous Drain Current @ V <sub>GS</sub> =10V, Ta=25°C		14.6	
Continuous Drain Current @ V <sub>GS</sub> =10V, Ta=70°C		11.7	
Pulsed Drain Current	I <sub>DM</sub>	130 *1	A
Avalanche Current	I <sub>AS</sub>	32	
Avalanche Energy @ L=0.1mH, Id=32A, V <sub>DD</sub> =10V	E <sub>AS</sub>	51 *3	
Total Power Dissipation	Tc=25°C	PD	
	Tc=100°C	5	
	TA=25°C	P <sub>DSM</sub>	2.5 *2
	TA=70°C		1.6 *2
Operating Junction and Storage Temperature Range	T <sub>j</sub> , T <sub>stg</sub>	-55~+150	°C

### Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	R <sub>th,j-c</sub>	10	°C/W
Thermal Resistance, Junction-to-ambient, max	R <sub>th,j-a</sub>	50 *2	

- Note : 1. Pulse width limited by maximum junction temperature.  
 2. Surface mounted on a 1 in<sup>2</sup> pad of 2oz copper, t≤10s. In practice R<sub>th,j-a</sub> will be determined by customer's PCB characteristics. 125°C/W when mounted on a minimum pad of 2 oz. copper.  
 3.100% tested by condition of L=0.1mH, I<sub>AS</sub>=20A, V<sub>GS</sub>=10V, V<sub>DD</sub>=10V.

### Characteristics (Tc=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
BV <sub>DSS</sub>	14	-	-	V	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA
V <sub>GS(th)</sub>	0.3	-	1.3		V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> =250μA
G <sub>FS</sub> *1	-	38	-	S	V <sub>DS</sub> =5V, I <sub>D</sub> =15A
I <sub>GSS</sub>	-	-	±100	nA	V <sub>GS</sub> =±10V
I <sub>DSS</sub>	-	-	1	μA	V <sub>DS</sub> =12V, V <sub>GS</sub> =0V
	-	-	5		V <sub>DS</sub> =12V, V <sub>GS</sub> =0V, T <sub>j</sub> =55°C
R <sub>DSS(ON)</sub> *1	-	3.3	4.3	mΩ	V <sub>GS</sub> =10V, I <sub>D</sub> =20A
	-	4.0	5.3		V <sub>GS</sub> =4.5V, I <sub>D</sub> =20A
	-	4.2	5.6		V <sub>GS</sub> =3.5V, I <sub>D</sub> =5A
<b>Dynamic</b>					
C <sub>iss</sub>	-	1036	-	pF	V <sub>DS</sub> =10V, V <sub>GS</sub> =0V, f=1MHz
C <sub>oss</sub>	-	380	-		
C <sub>rss</sub>	-	219	-		
Q <sub>g</sub> *1, 2	10	14.3	18.6	nC	V <sub>DS</sub> =10V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =15A
Q <sub>gs</sub> *1, 2	-	1.6	-		
Q <sub>gd</sub> *1, 2	-	5.6	-		

### Characteristics (T<sub>c</sub>=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
t <sub>d(ON)</sub> *1, 2	-	12	18	ns	V <sub>DS</sub> =10V, I <sub>D</sub> =15A, V <sub>GS</sub> =4.5V, R <sub>GS</sub> =2.7Ω
t <sub>r</sub> *1, 2	-	23	34.5		
t <sub>d(OFF)</sub> *1, 2	-	41.4	62.1		
t <sub>f</sub> *1, 2	-	24.6	36.9		
R <sub>g</sub>	1.3	2.7	4	Ω	f=1MHz

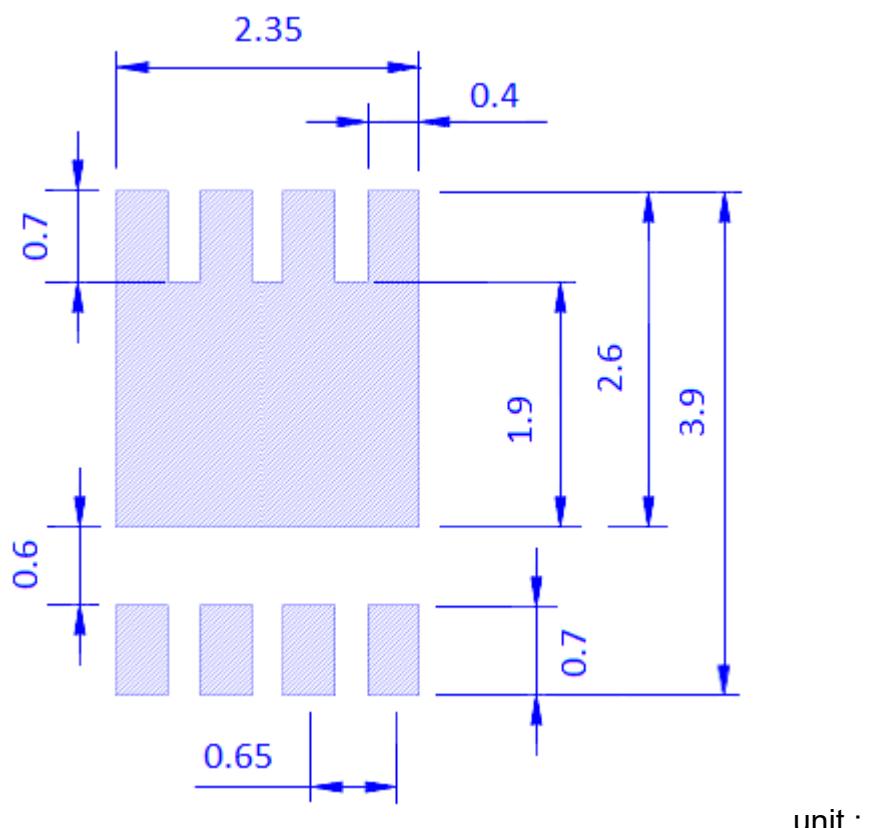
### Source-Drain Diode

I <sub>S</sub> *1	-	-	15	A	
V <sub>SD</sub> *1	-	0.82	1.2	V	I <sub>S</sub> =15A, V <sub>GS</sub> =0V
trr	-	16	-	ns	I <sub>F</sub> =15A, dI <sub>F</sub> /dt=100A/μs
Qrr	-	6	-		

Note : \*1.Pulse Test : Pulse Width <300μs, Duty Cycle≤2%

\*2.Independent of operating temperature

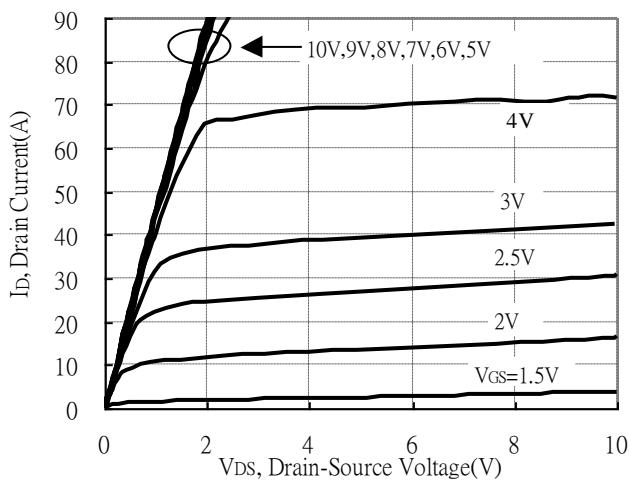
### Recommended Soldering Footprint



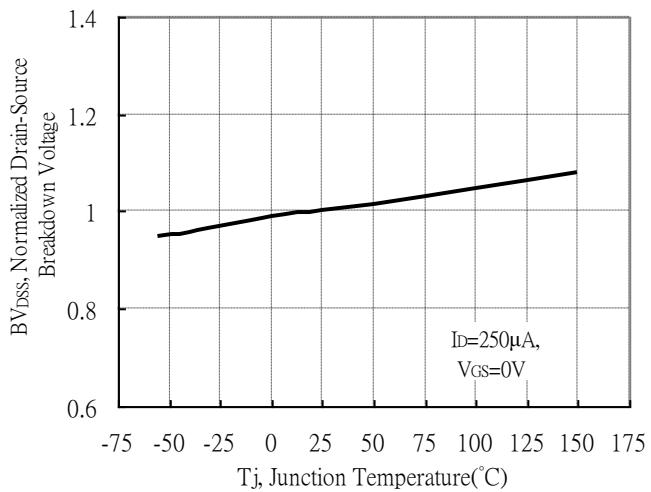
unit : mm

## Typical Characteristics

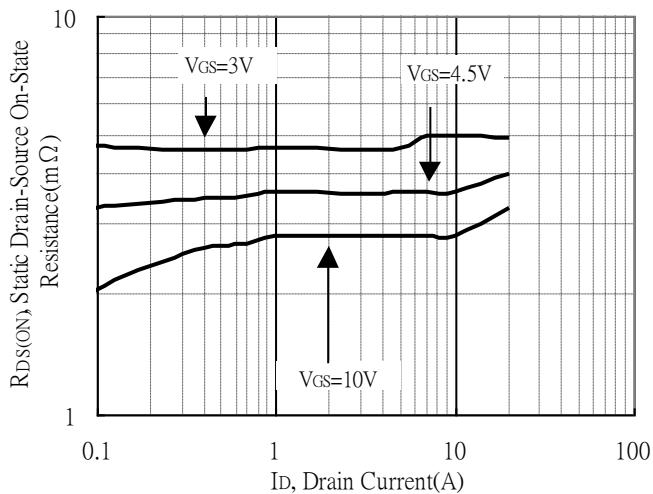
Typical Output Characteristics



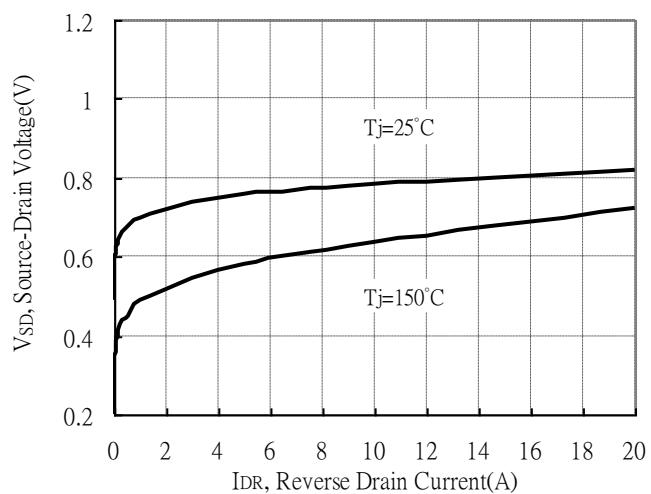
Breakdown Voltage vs Ambient Temperature



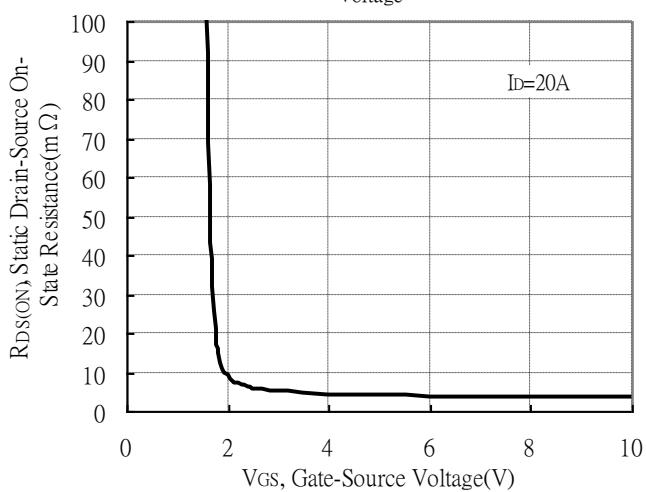
Static Drain-Source On-State resistance vs Drain Current



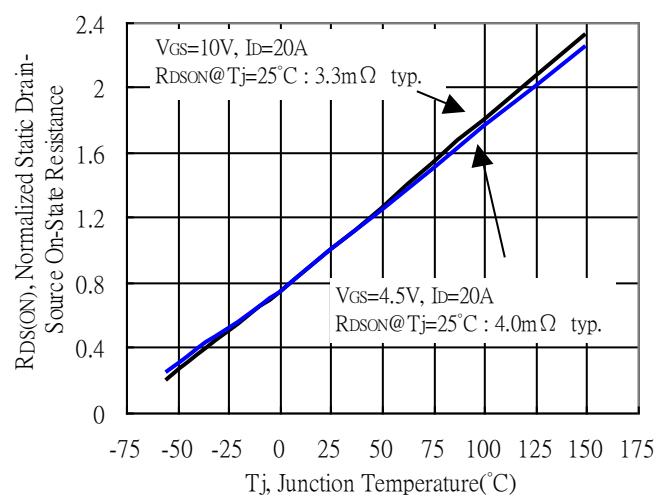
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

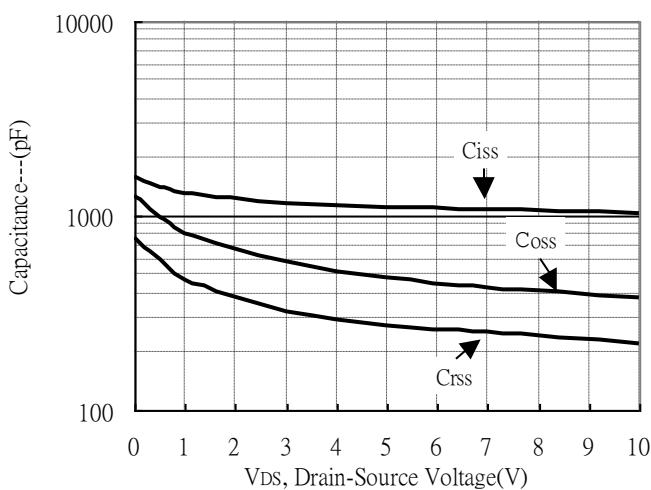


Drain-Source On-State Resistance vs Junction Temperature

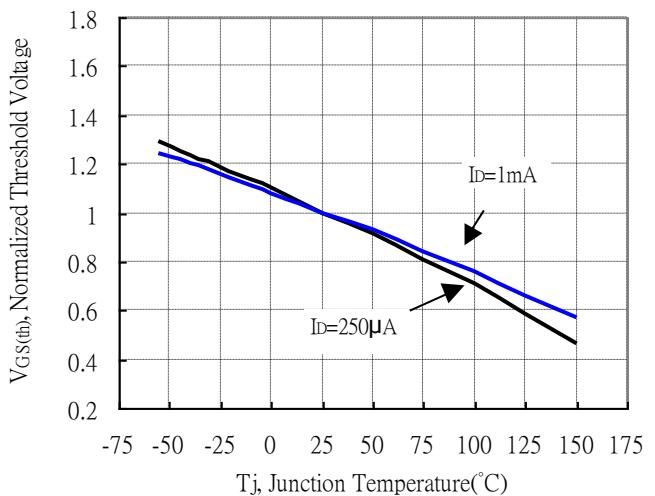


## Typical Characteristics(Cont.)

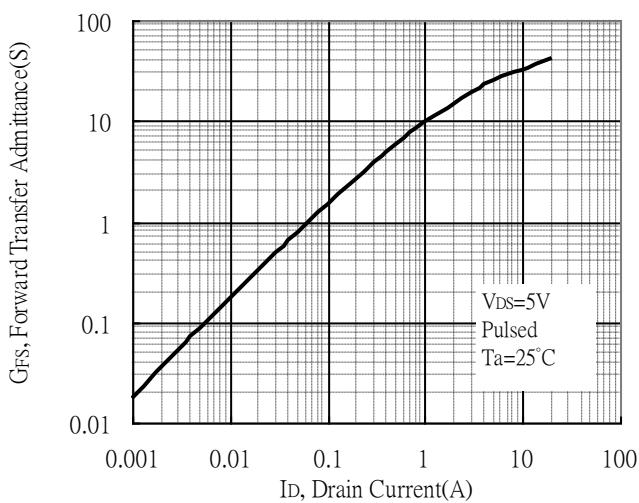
Capacitance vs Drain-to-Source Voltage



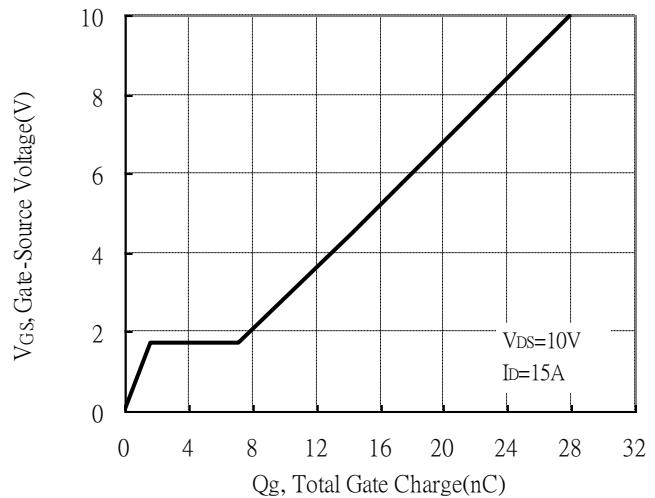
Threshold Voltage vs Junction Temperature



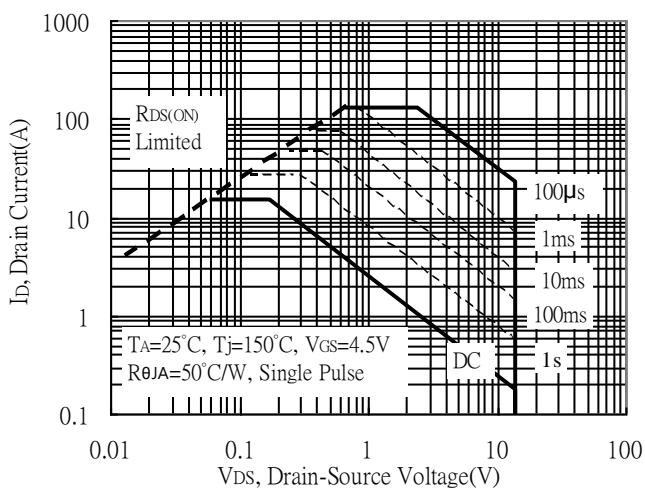
Forward Transfer Admittance vs Drain Current



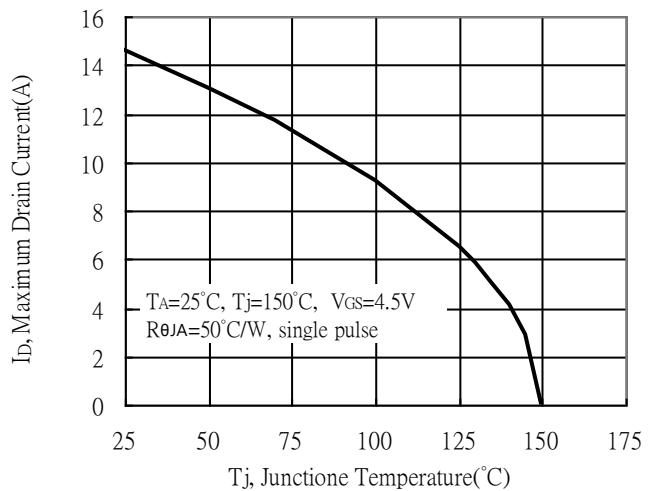
Gate Charge Characteristics



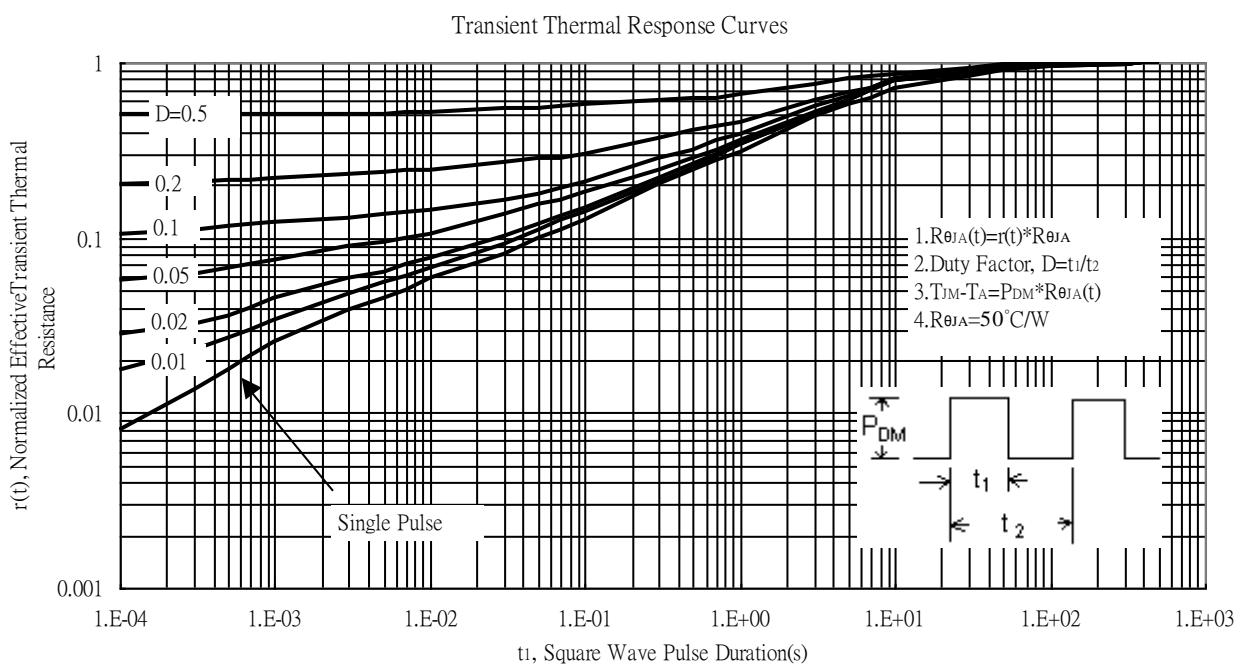
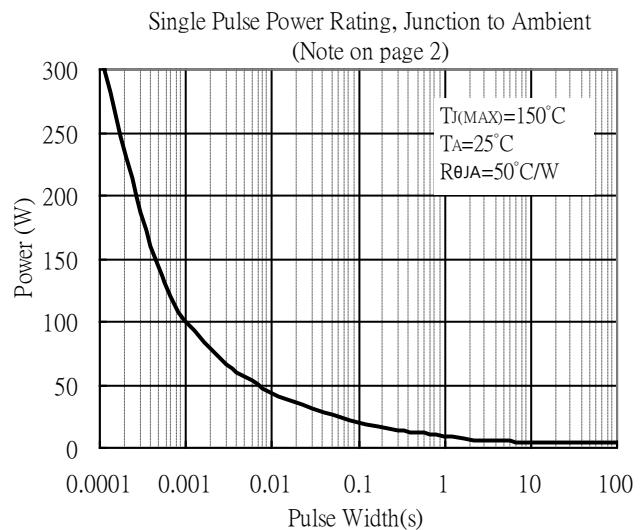
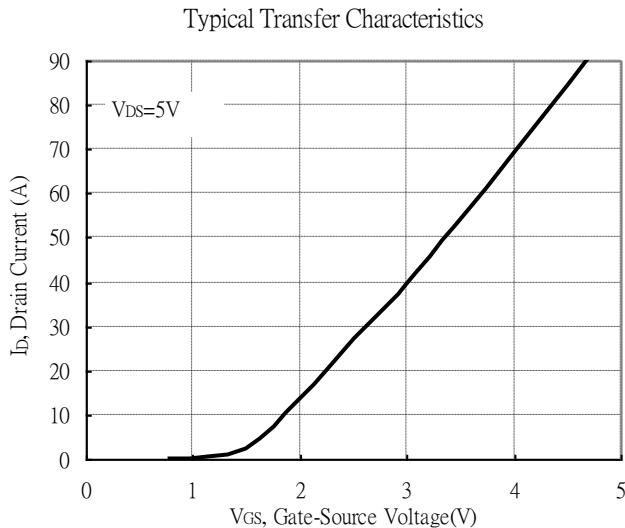
Maximum Safe Operating Area



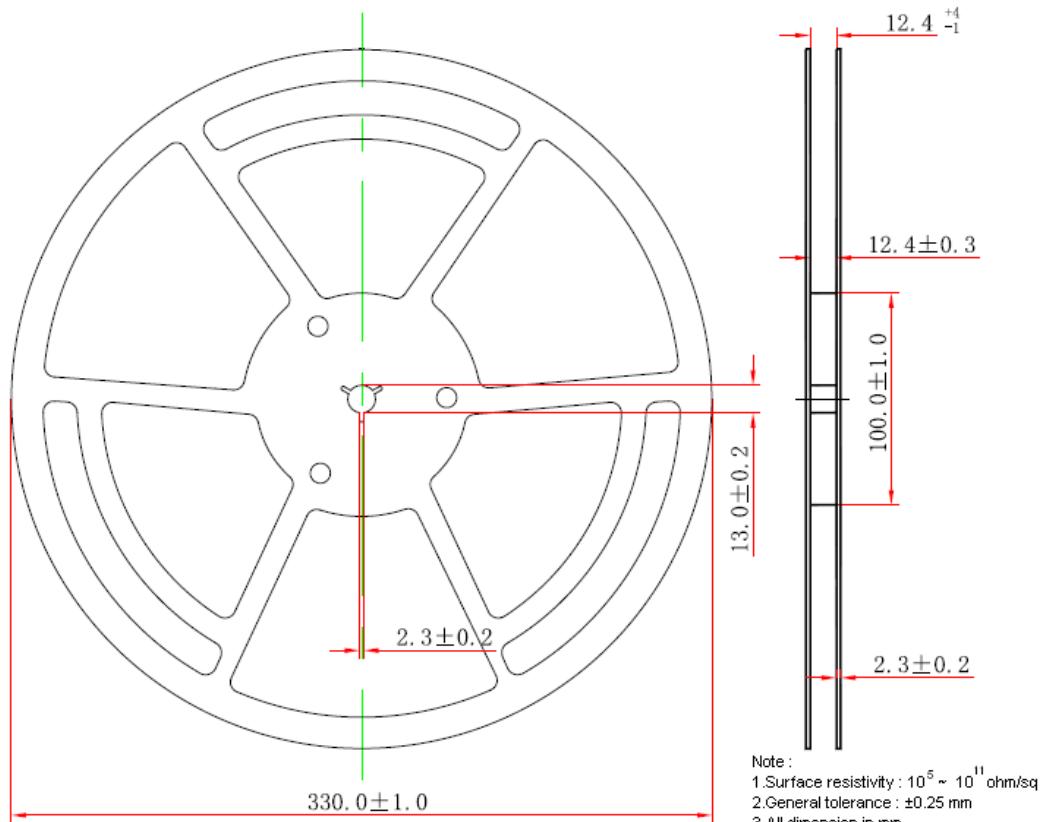
Maximum Drain Current vs Junction Temperature



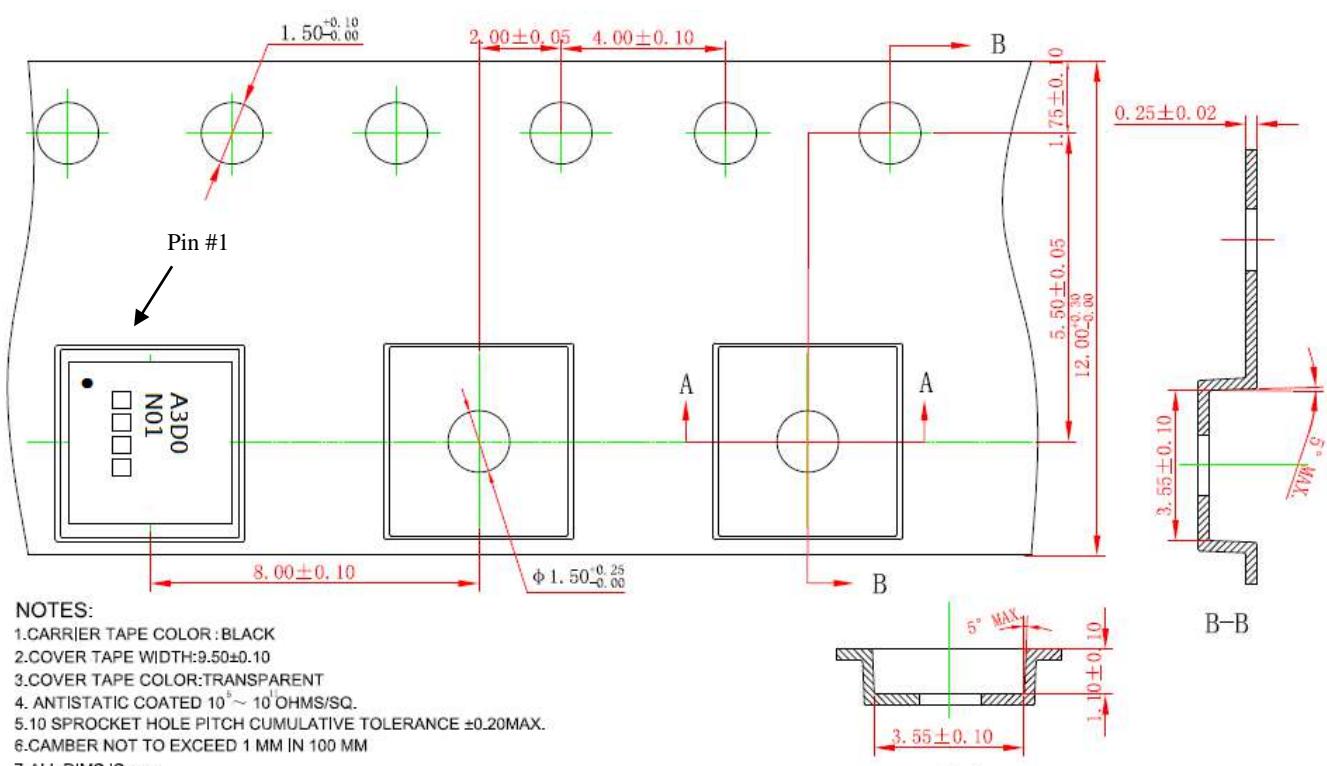
## Typical Characteristics(Cont.)



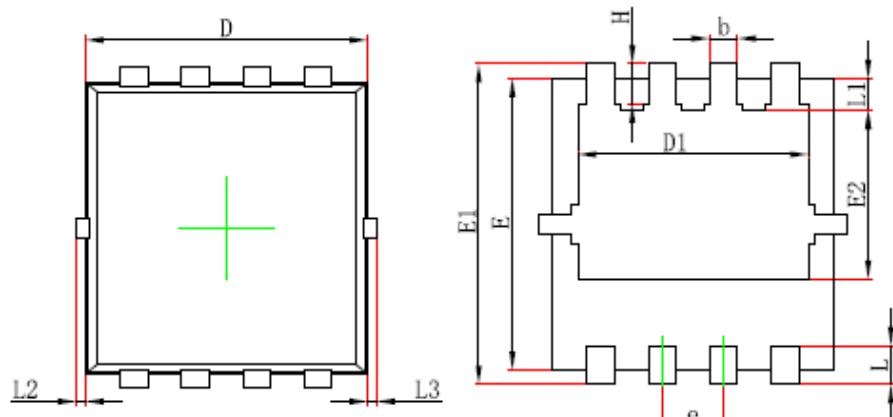
## Reel Dimension



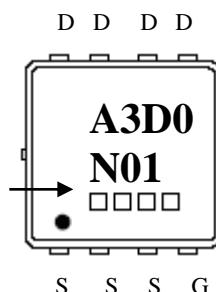
## Carrier Tape Dimension



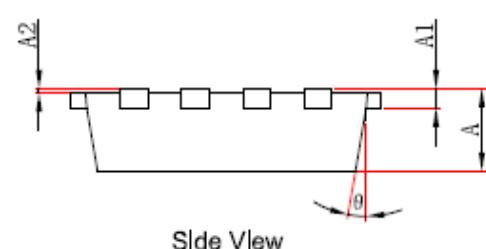
## DFN3x3 Dimension



Marking:



Date Code



8-Lead DFN3x3 Plastic Package  
 Package Code: V8

\*: Typical

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.605	0.850	0.026	0.033	b	0.200	0.400	0.008	0.016
A1	0.152	REF	0.006	REF	e	0.550	0.750	0.022	0.030
A2	0.000	0.050	0.000	0.002	L	0.300	0.500	0.012	0.020
D	2.900	3.100	0.114	0.122	L1	0.180	0.480	0.007	0.019
D1	2.300	2.600	0.091	0.102	L2	0.000	0.100	0.000	0.004
E	2.900	3.100	0.114	0.122	L3	0.000	0.100	0.000	0.004
E1	3.150	3.450	0.124	0.136	H	0.315	0.515	0.012	0.020
E2	1.535	1.935	0.060	0.076	θ	9°	13°	9°	13°