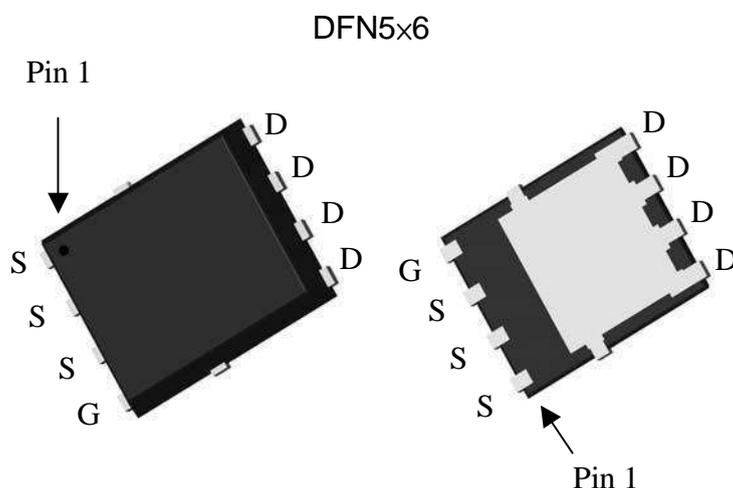


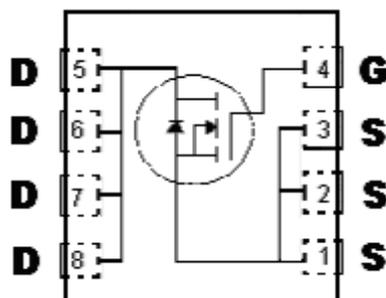
## N-Channel Enhancement Mode Power MOSFET

### Features:

- Simple Drive Requirement
- Low On-resistance
- Fast Switching Characteristic
- Pb-free lead plating and Halogen-free package



$BV_{DSS}$	100V
$I_D @ V_{GS}=10V, T_C=25^\circ C$	81A
$I_D @ V_{GS}=10V, T_A=25^\circ C$	21A
$R_{DSON(TYP)}$   $V_{GS}=10V, I_D=30A$	6m $\Omega$



G : Gate D : Drain S : Source

### Ordering Information

Device	Package	Shipping
KPRE5D0N10R	DFN 5 x6 (Pb-free lead plating and halogen-free package)	3000 pcs / tape & reel

### Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	10s	Steady State	Unit	
Drain-Source Voltage	V <sub>DS</sub>	100		V	
Gate-Source Voltage	V <sub>GS</sub>	±20			
Continuous Drain Current @ T <sub>C</sub> =25°C, V <sub>GS</sub> =10V (Note 1)	I <sub>D</sub>	81		A	
Continuous Drain Current @ T <sub>C</sub> =100°C, V <sub>GS</sub> =10V (Note 1)		57.3			
Continuous Drain Current @ T <sub>A</sub> =25°C, V <sub>GS</sub> =10V (Note 2)	I <sub>DSM</sub>	21	14		
Continuous Drain Current @ T <sub>A</sub> =70°C, V <sub>GS</sub> =10V (Note 2)		17.6	11.7		
Continuous Drain Current @ T <sub>A</sub> =85°C, V <sub>GS</sub> =10V (Note 2)		16.3	10.8		
Pulsed Drain Current (Note 3)	I <sub>DM</sub>	324 *1			
Avalanche Current @ L=0.1mH (Note 3)	I <sub>AS</sub>	90			
Avalanche Energy @ L=1mH, I <sub>D</sub> =36A, V <sub>DD</sub> =50V (Note 4)	E <sub>AS</sub>	648			mJ
Repetitive Avalanche Energy @ L=0.05mH (Note 3)	E <sub>AR</sub>	5 *2			
Total Power Dissipation	P <sub>D</sub>	T <sub>C</sub> =25°C (Note 1)	100		W
		T <sub>C</sub> =100°C (Note 1)	50		
	P <sub>DSM</sub>	T <sub>A</sub> =25°C (Note 2)	6.8	3	
		T <sub>A</sub> =70°C (Note 2)	4.8	2.1	
		T <sub>A</sub> =85°C (Note 2)	4.1	1.8	
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55~+175		°C	

### Thermal Data

Parameter	Symbol	Typical	Maximum	Unit	
Thermal Resistance, Junction-to-ambient (Note 2)	R <sub>θJA</sub>	t≤10s	18	22	°C/W
		Steady State	42	50	
Thermal Resistance, Junction-to-case	R <sub>θJC</sub>	1.3	1.5		

Note : 1.The power dissipation P<sub>D</sub> is based on T<sub>J(MAX)</sub>=175 °C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.

2.The value of R<sub>θJA</sub> is measured with the device mounted on 1 in<sup>2</sup>FR-4 board with 2 oz. copper, in a still air environment with T<sub>A</sub>=25 °C. The power dissipation P<sub>DSM</sub> is based on R<sub>θJA</sub> and the maximum allowed junction temperature of 175°C. The value in any given application depends on the user's specific board design.

3.Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=175 °C. Ratings are based on low frequency and low duty cycles to keep initial T<sub>J</sub>=25°C.

4.100% tested by conditions of L=0.1mH, I<sub>AS</sub>=30A, V<sub>GS</sub>=10V, V<sub>DD</sub>=50V

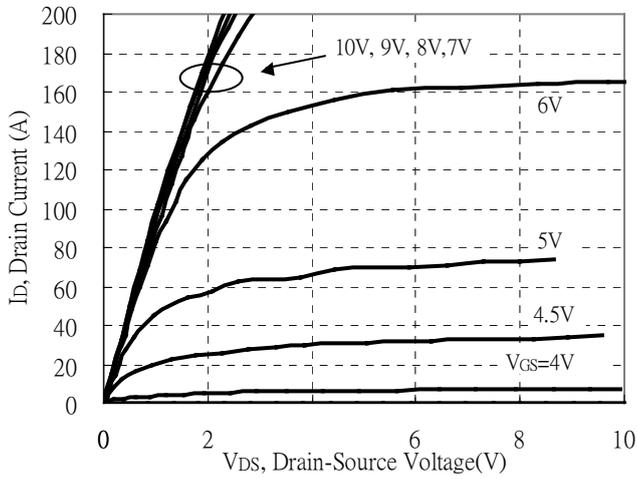
**Characteristics (Tc=25°C, unless otherwise specified)**

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
BV <sub>DSS</sub>	100	-	-	V	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA
V <sub>GS(th)</sub>	2	-	4		V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> =250μA
G <sub>FS</sub> *1	-	30.9	-	S	V <sub>DS</sub> =10V, I <sub>D</sub> =20A
I <sub>GSS</sub>	-	-	±100	nA	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V
I <sub>DSS</sub>	-	-	1	μA	V <sub>DS</sub> =80V, V <sub>GS</sub> =0V
	-	-	25		V <sub>DS</sub> =80V, V <sub>GS</sub> =0V, T <sub>j</sub> =125°C
R <sub>DS(ON)</sub> *1	-	6	8.2	mΩ	V <sub>GS</sub> =10V, I <sub>D</sub> =30A
<b>Dynamic</b>					
C <sub>iss</sub>	-	4829	6278	pF	V <sub>GS</sub> =0V, V <sub>DS</sub> =50V, f=1MHz
C <sub>oss</sub>	-	504	655		
C <sub>rss</sub>	-	22	33		
Q <sub>g</sub> *1, 2	-	66.7	87	nC	V <sub>DS</sub> =80V, V <sub>GS</sub> =10V, I <sub>D</sub> =30A
Q <sub>gs</sub> *1, 2	-	21	-		
Q <sub>gd</sub> *1, 2	-	13	-		
t <sub>d(ON)</sub> *1, 2	-	33.6	-	ns	V <sub>DD</sub> =50V, I <sub>D</sub> =30A, V <sub>GS</sub> =10V, R <sub>G</sub> =1Ω
t <sub>r</sub> *1, 2	-	20.4	-		
t <sub>d(OFF)</sub> *1, 2	-	63.2	-		
t <sub>f</sub> *1, 2	-	13	-		
R <sub>g</sub>	-	2.1	-	Ω	f=1MHz
<b>Source-Drain Diode</b>					
I <sub>S</sub> *1	-	-	81	A	
I <sub>SM</sub> *3	-	-	324		
V <sub>SD</sub> *1	-	0.81	1.2	V	I <sub>S</sub> =20A, V <sub>GS</sub> =0V
t <sub>rr</sub>	-	45.9	60	ns	I <sub>F</sub> =20A, dI <sub>F</sub> /dt=100A/μs
Q <sub>rr</sub>	-	87.8	-	nC	
I <sub>RRM</sub>	-	3.5	-	A	

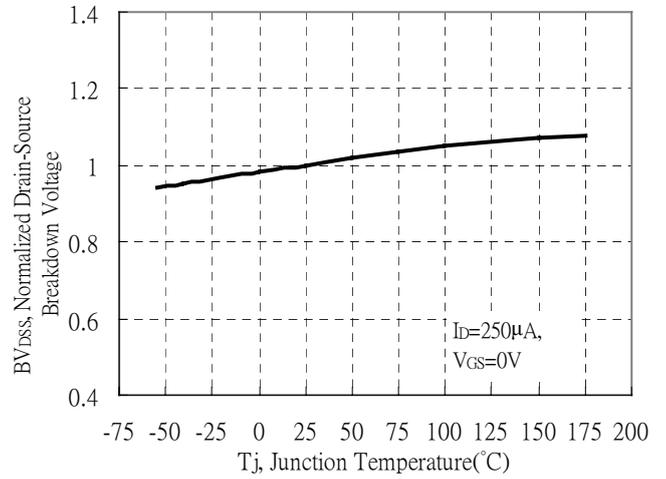
Note : \*1.Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%  
 \*2.Independent of operating temperature  
 \*3.Pulse width limited by maximum junction temperature.

## Typical Characteristics

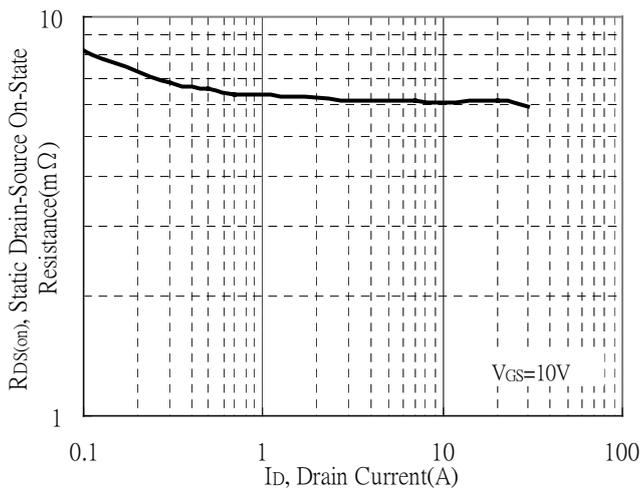
Typical Output Characteristics



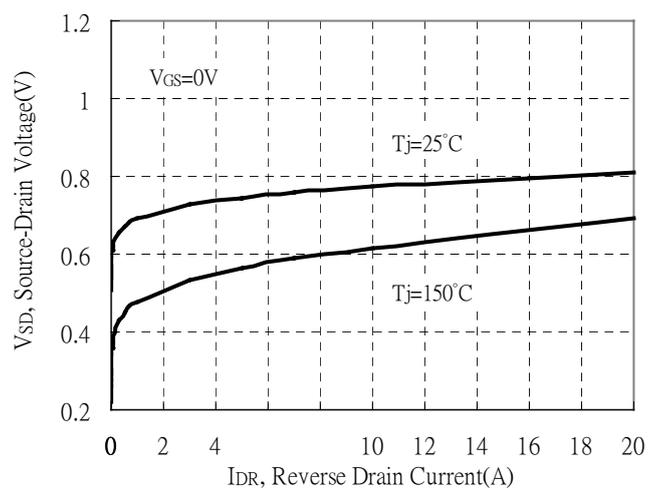
Brekdown Voltage vs Ambient Temperature



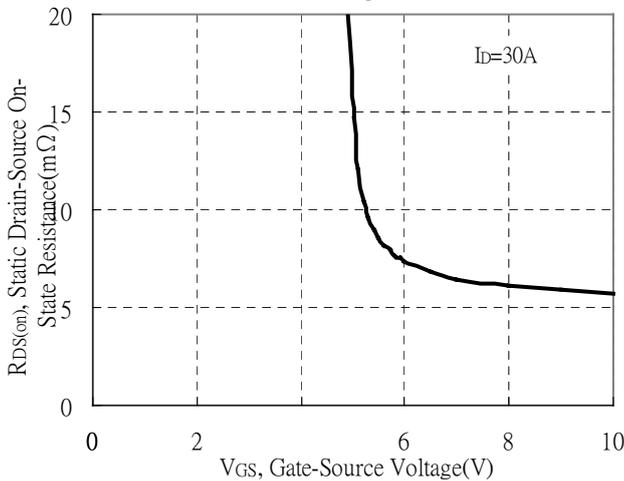
Static Drain-Source On-State resistance vs Drain Current



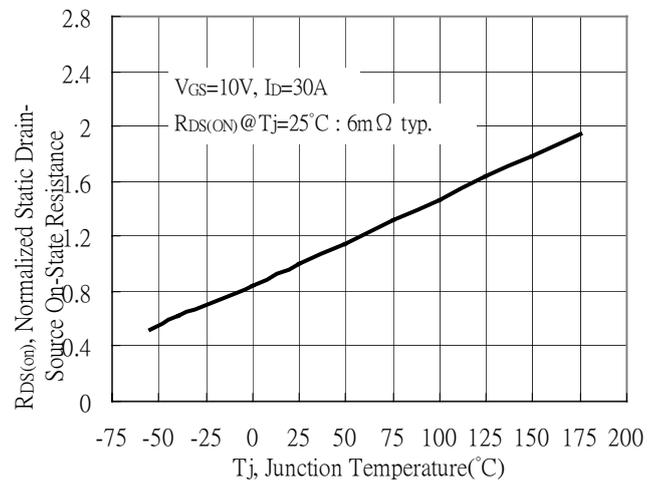
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

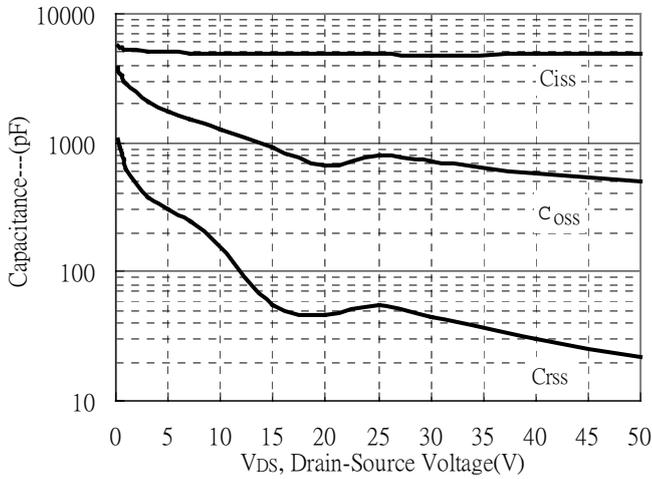


Drain-Source On-State Resistance vs Junction Temperature

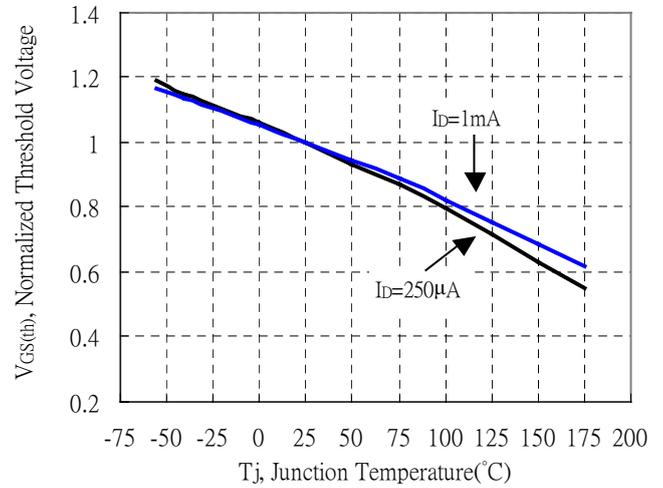


**Typical Characteristics(Cont.)**

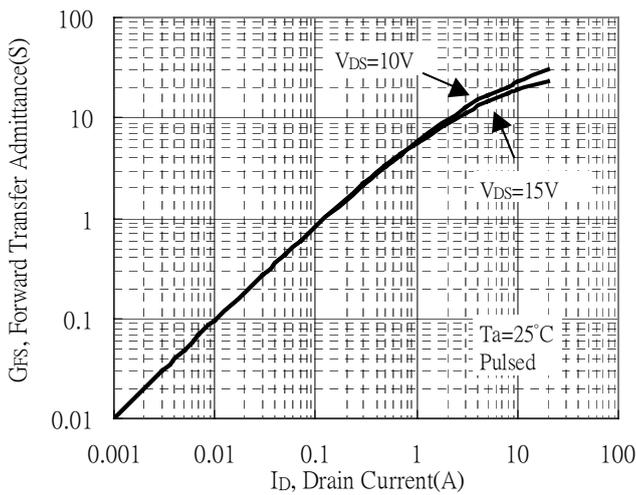
Capacitance vs Drain-to-Source Voltage



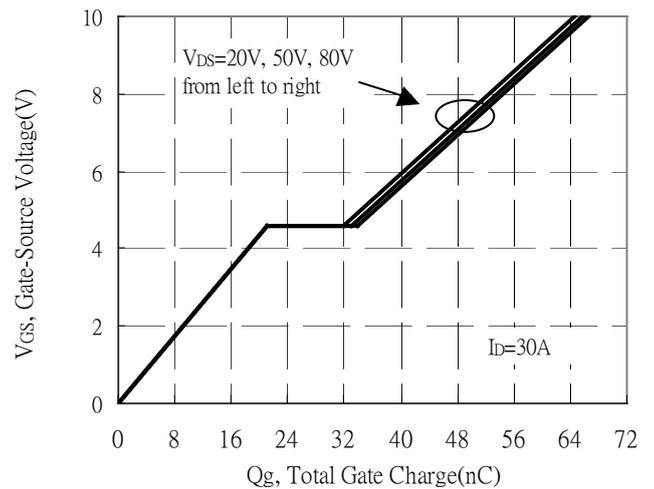
Threshold Voltage vs Junction Temperature



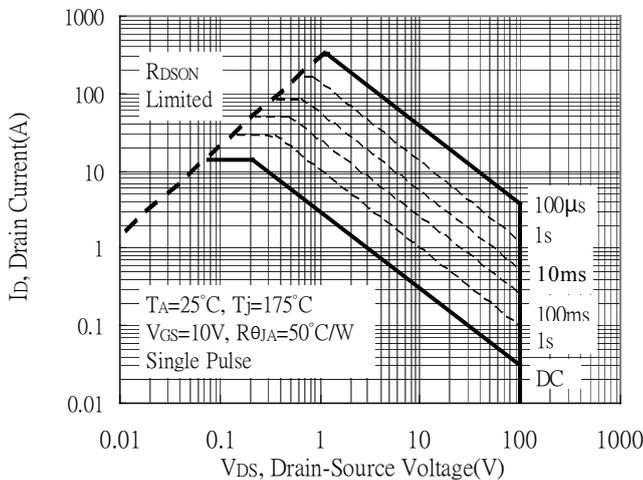
Forward Transfer Admittance vs Drain Current



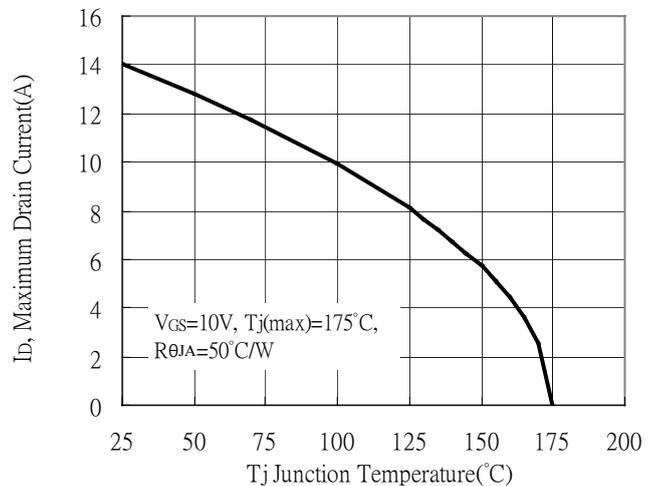
Gate Charge Characteristics



Maximum Safe Operating Area

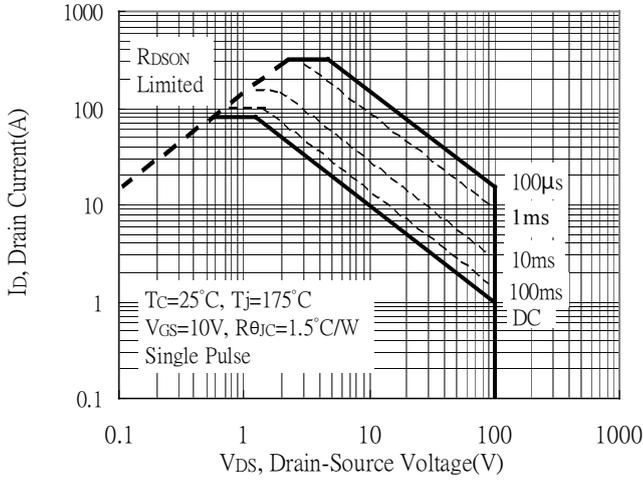


Maximum Drain Current vs Junction Temperature

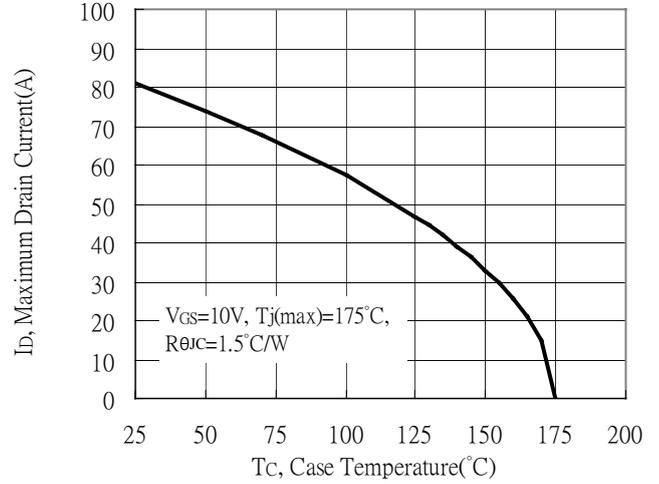


**Typical Characteristics(Cont.)**

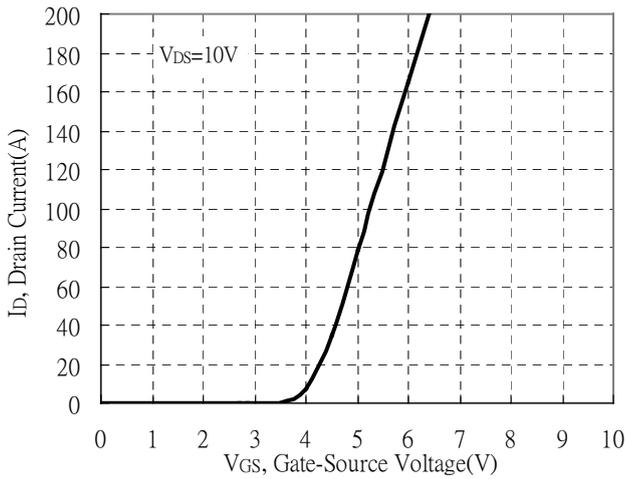
Maximum Safe Operating Area



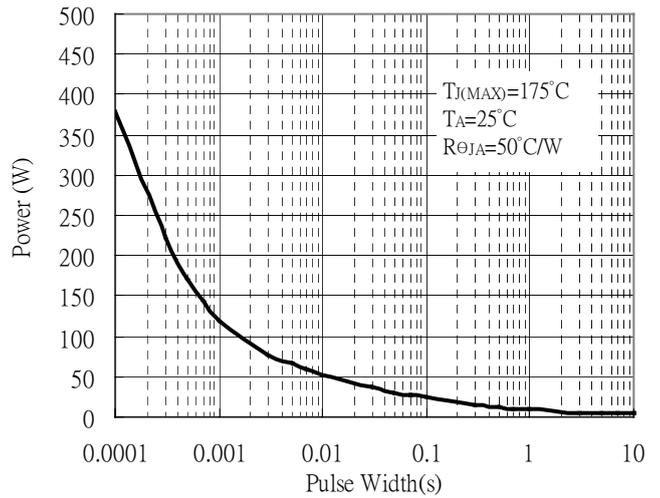
Maximum Drain Current vs Case Temperature



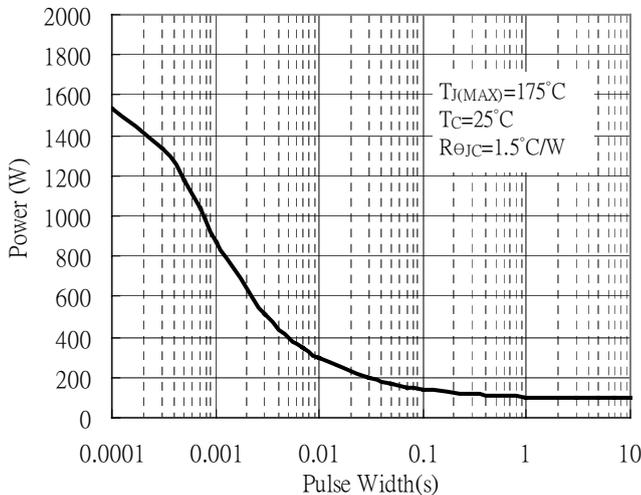
Typical Transfer Characteristics



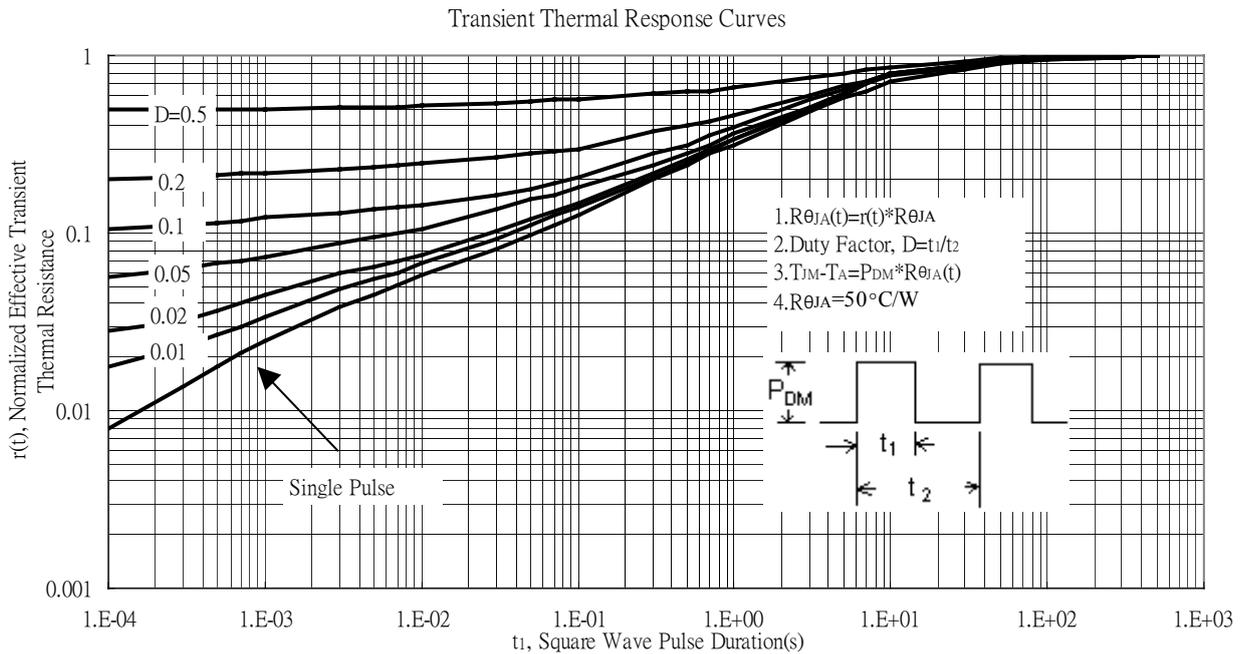
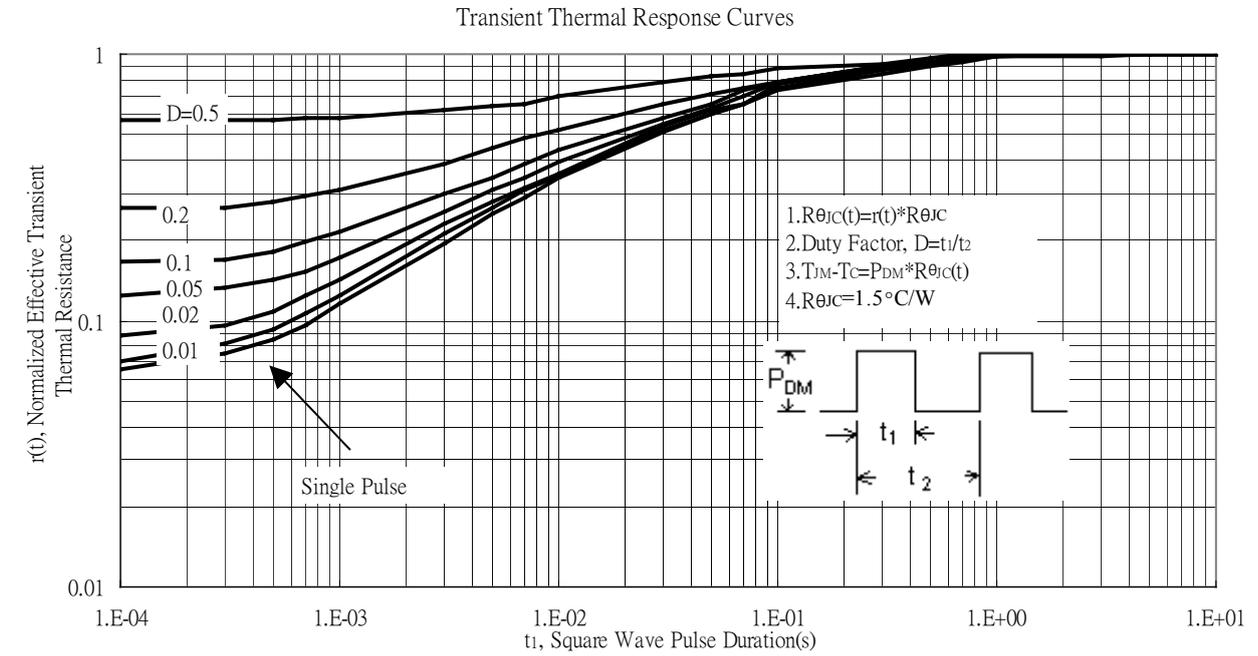
Single Pulse Power Rating, Junction to Case



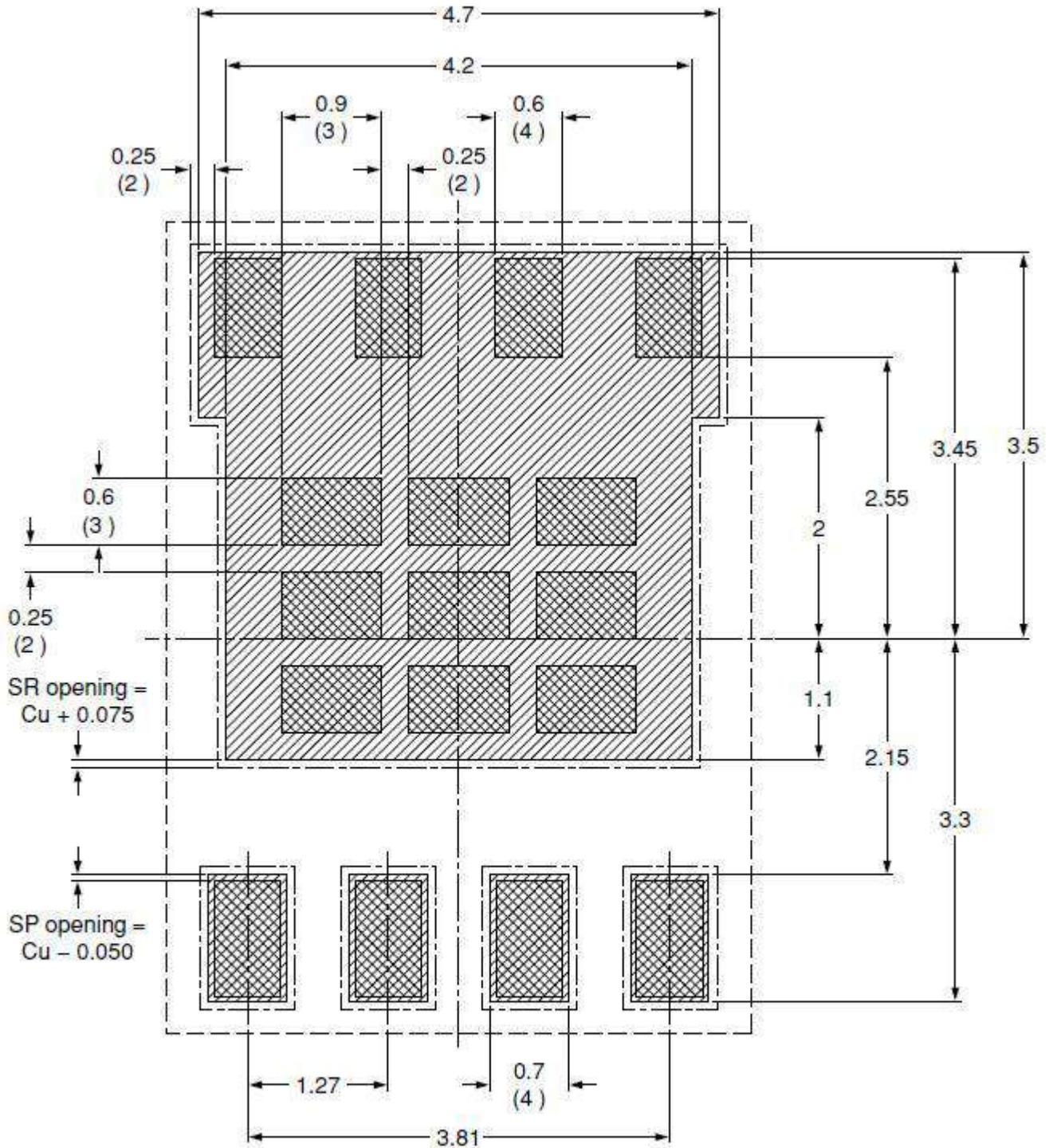
Single Pulse Power Rating, Junction to Case

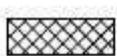
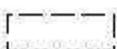


**Typical Characteristics(Cont.)**



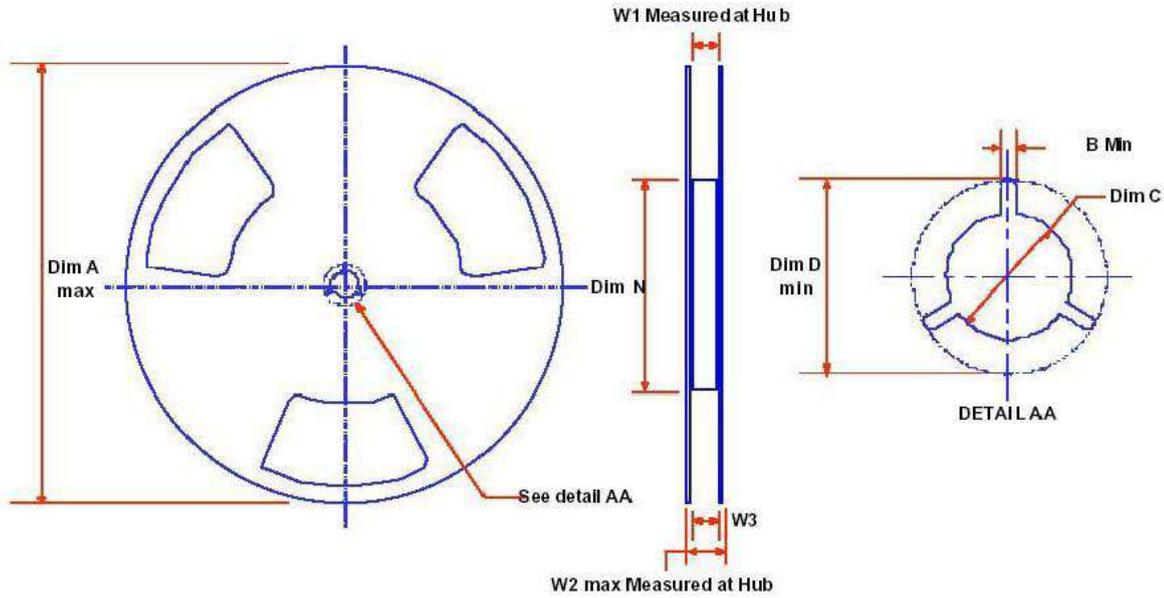
### Recommended Soldering Footprint & Stencil Design



- |   |               |   |                                     |
|---|---------------|---|-------------------------------------|
|  | solder lands  |  | solder paste<br>125 $\mu$ m stencil |
|  | solder resist |  | occupied area                       |

unit : mm

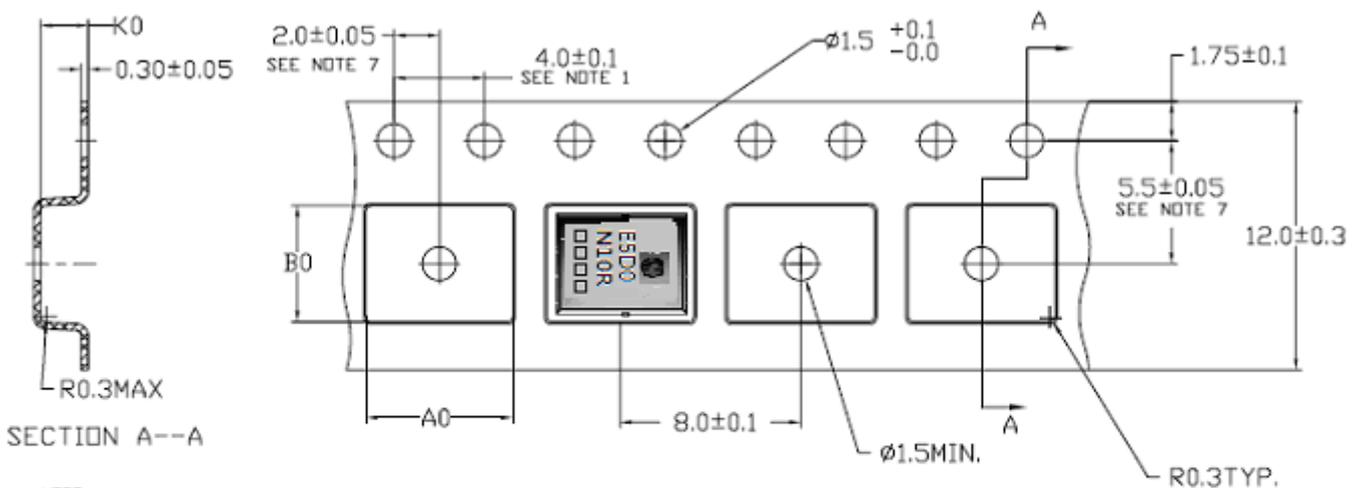
### Reel Dimension



Dimensions are in inches and millimeters

Tape Size	Reel Option	Dim A	Dim B	Dim C	Dim D	Dim N	Dim W1	Dim W2	Dim W3 (LSL-USL)
12mm	13" Dia (STD/199Z)	13.00 330+/-1	0.069 1.5 Min.	0.512 13.0 Min.	0.796 20.2(ref.)	7.00 178+0/-2	0.488 +0.078/-0.000 12.4 +2/0	0.724 18.4(ref.)	0.469 - 0.606 11.9 - 15.4

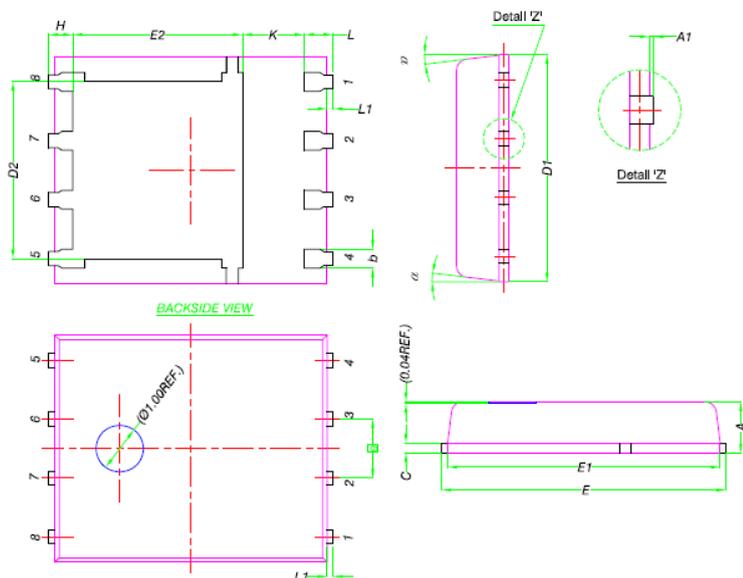
### Carrier Tape Dimension



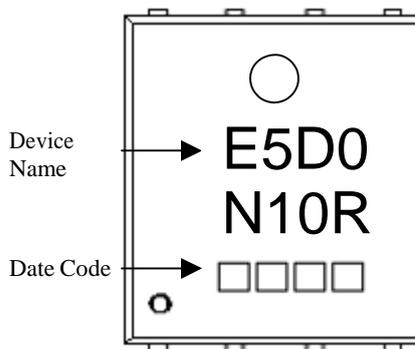
- NOTE:
- 10 SPROCKET HOLE PITCH CUMULATIVE TOLERANCE ±0.2
  - CAMBER NOT TO EXCEED 1mm IN 100mm, NONCUMULATIVE OVER 250mm.
  - MATERIAL: BLACK STATIC DISSIPATIVE PS.(POLYSTYRENE)
  - ALL DIMENSIONS ARE IN MILLIMETERS (UNLESS OTHERWISE SPECIFIED)
  - A0 AND B0 MEASURED ON A PLANE 0.3mm ABOVE THE BOTTOM OF THE POCKET
  - K0 MEASURED FROM A PLANE ON THE INSIDE BOTTOM OF THE POCKET TO THE TOP SURFACE OF THE CARRIER
  - POCKET POSITION RELATIVE TO SPROCKET HOLE MEASURED AS TRUE POSITION OF POCKET, NOT POCKET HOLE
  - SURFACE RESISTIVITY  
 $1 \times 10^4 - 1 \times 10^6 \Omega \text{ SQR}$

A0=6.5±0.1  
 B0=5.3±0.1  
 K0=1.4±0.1

**DFN5x6 Dimension**



Marking :



8-Lead DFN5x6 Plastic Package

Date Code(counting from left to right) :

1<sup>st</sup> code: year code, the last digit of Christian year

2<sup>nd</sup> code : month code, Jan→A, Feb→B, Mar→C, Apr→D, May→E, Jun→F, Jul→G, Aug→H, Sep→J, Oct→K, Nov→L, Dec→M

3<sup>rd</sup> and 4<sup>th</sup> codes : production serial number, 01~99

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.90	1.10	0.035	0.043	E2	3.38	3.78	0.133	0.149
A1	0.00	0.05	0.000	0.002	e	1.27	BSC	0.050	BSC
b	0.33	0.51	0.013	0.020	H	0.41	0.61	0.016	0.024
C	0.20	0.30	0.008	0.012	K	1.10	-	0.043	-
D1	4.80	5.00	0.189	0.197	L	0.51	0.71	0.020	0.028
D2	3.61	3.96	0.142	0.156	L1	0.06	0.20	0.002	0.008
E	5.90	6.10	0.232	0.240	$\theta$	8°	12°	8°	12°
E1	5.70	5.80	0.224	0.228					