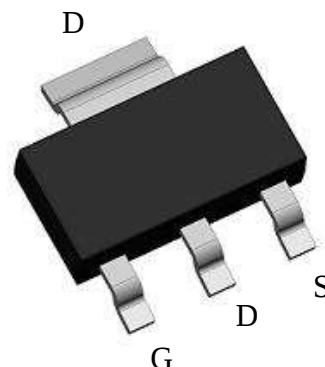


N-Channel Enhancement Mode Power MOSFET

Description:

The KLB280N15 is a N-channel enhancement-mode MOSFET, providing the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost effectiveness. The SOT-223 package is universally preferred for all commercial-industrial surface mount applications.

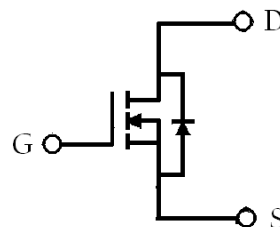
SOT-223



Features:

- Simple Drive Requirement
- Fast Switching Characteristic
- Pb-free lead plating and halogen-free package

BVDSS	150V
ID@VGS=10V, TA=25°C	2A
RDSON@VGS=10V, ID=1.5A	297mΩ(typ)
RDSON@VGS=4.5V, ID=1A	304mΩ(typ)



G : Gate D : Drain S : Source

Ordering Information

Device	Package	Shipping
KLB280N15	SOT-223 (Pb-free lead plating and halogen-free package)	2500 pcs / tape & reel

Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V _{DS}	150	V
Gate-Source Voltage	V _{GS}	±20	
Continuous Drain Current @V _{GS} =10V, T _C =25°C	I _D	3	A
Continuous Drain Current @V _{GS} =10V, T _C =100°C		1.9	
Continuous Drain Current @V _{GS} =10V, T _A =25°C		2	
Continuous Drain Current @V _{GS} =10V, T _A =70°C		1.6	
Pulsed Drain Current	I _{DM}	8 *1	W
Total Power Dissipation	P _D	T _C =25°C	
		T _C =100°C	
		T _A =25°C	
		T _A =100°C	
Operating Junction and Storage Temperature Range	T _j , T _{stg}	-55~+150	°C

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	R _{th,j-c}	20	°C/W
Thermal Resistance, Junction-to-ambient, max	R _{th,j-a}	45 *3	

- Note : 1. Pulse width limited by maximum junction temperature
2. Duty cycle ≤ 1%
3. Surface mounted on 1 in² copper pad of FR-4 board, 120°C/W when mounted on minimum copper pad

Characteristics (T_C=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	150	-	-	V	V _{GS} =0V, I _D =250μA
V _{GS(th)}	1	-	2.5		V _{DS} = V _{GS} , I _D =250μA
G _{FS} *1	-	5.9	-	S	V _{DS} =10V, I _D =3A
I _{GSS}	-	-	±100	nA	V _{GS} =±20V, V _{DS} =0V
I _{DSS}	-	-	1	μA	V _{DS} =120V, V _{GS} =0V
	-	-	25		V _{DS} =120V, V _{GS} =0V, T _j =125°C
R _{DS(ON)} *1	-	297	360	mΩ	V _{GS} =10V, I _D =1.5A
	-	304	400		V _{GS} =4.5V, I _D =1A
Dynamic					
C _{iss}	-	288	374	pF	V _{GS} =0V, V _{DS} =75V, f=1MHz
C _{oss}	-	25	50		
C _{rss}	-	11	22		

Characteristics (Tc=25°C, unless otherwise specified)

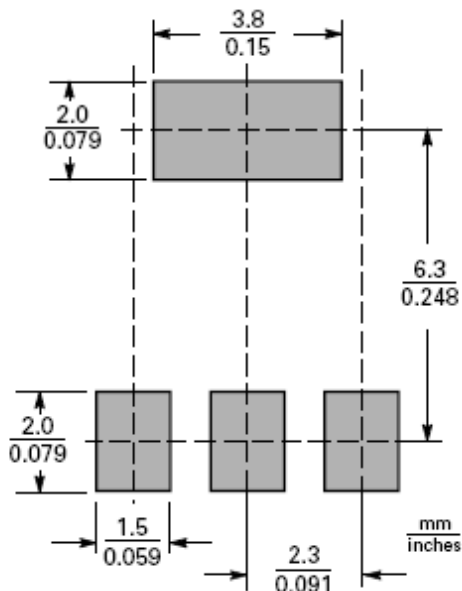
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Qg *1, 2	-	8.1	12	nC	V _{DS} =75V, V _{GS} =10V, I _D =1.7A
Qgs *1, 2	-	1	-		
Qgd *1, 2	-	1.9	-		
t _{d(ON)} *1, 2	-	5.2	12	ns	V _{DS} =75V, I _D =1A, V _{GS} =10V, R _{GS} =6Ω
t _r *1, 2	-	16.2	33		
t _{d(OFF)} *1, 2	-	20.8	42		
t _f *1, 2	-	15.6	32		
R _g	-	4.6	-	Ω	f=1MHz
Source-Drain Diode					
I _S *1	-	-	2	A	
I _{SM} *3	-	-	8		
V _{SD} *1	-	0.79	1.2	V	I _S =1.5A, V _{GS} =0V
t _{rr}	-	39	60	ns	I _F =1.7A, dI _F /dt=100A/μs
Q _{rr}	-	43	65	nC	

Note : *1.Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

*2.Independent of operating temperature

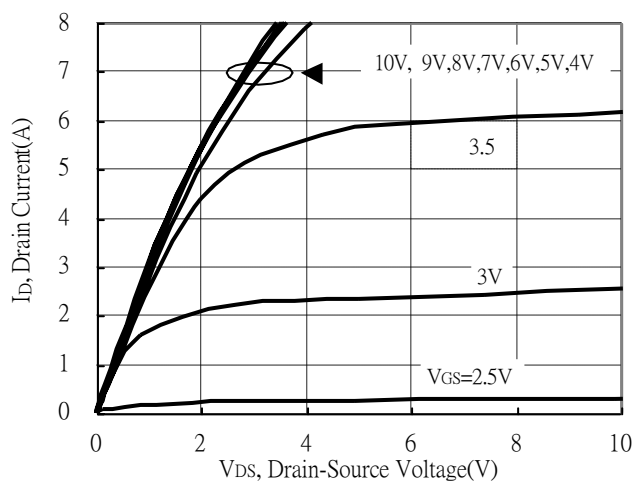
*3.Pulse width limited by maximum junction temperature.

Recommended soldering footprint

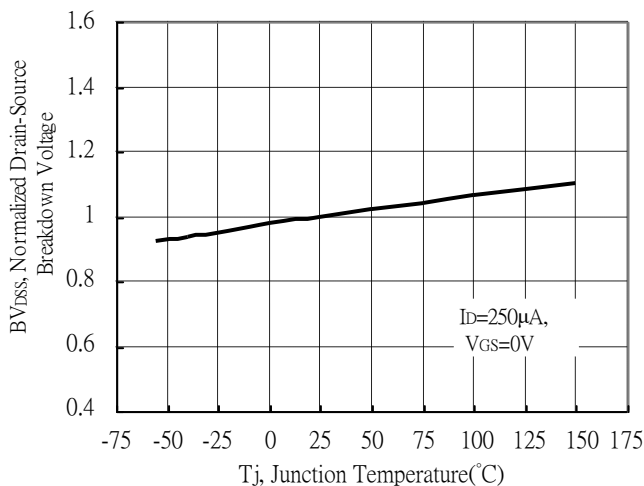


Typical Characteristics

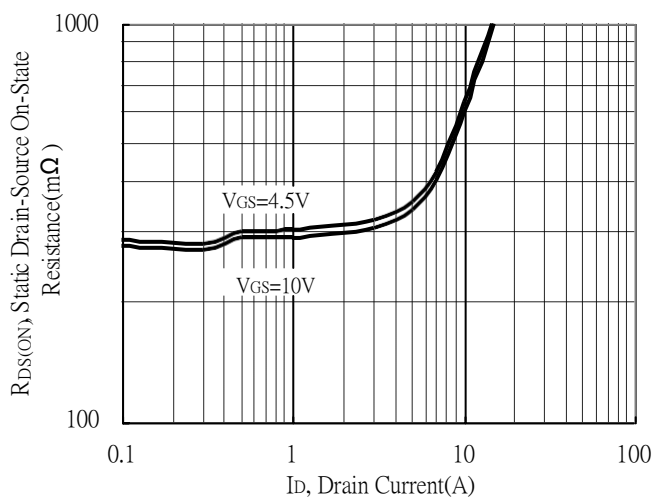
Typical Output Characteristics



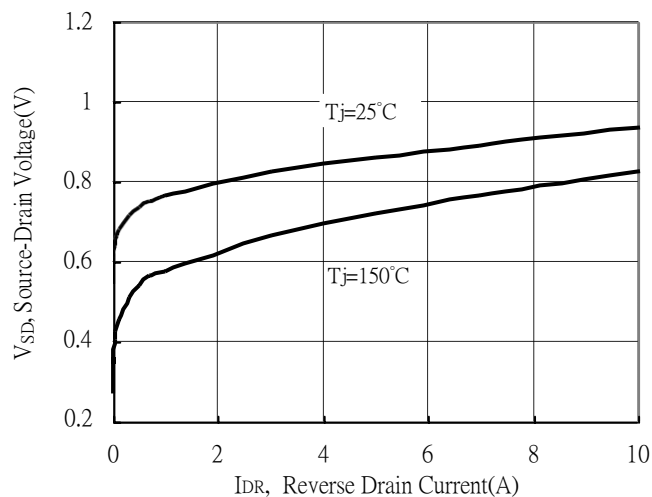
Breakdown Voltage vs Ambient Temperature



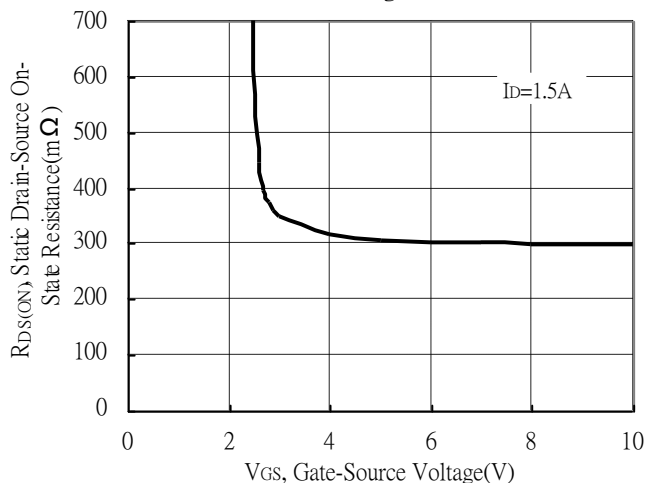
Static Drain-Source On-State resistance vs Drain Current



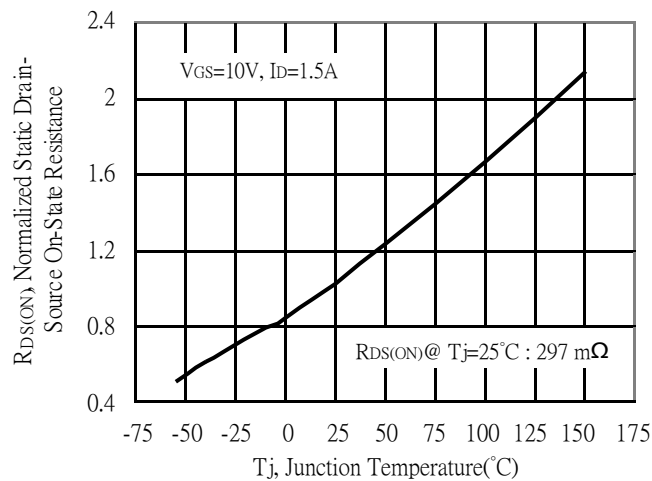
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

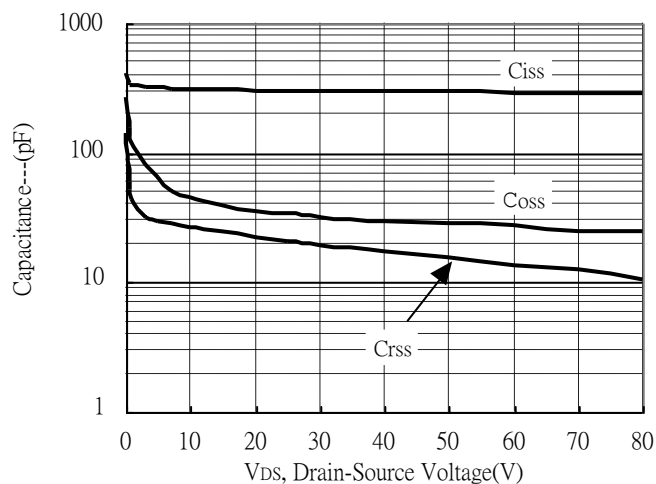


Drain-Source On-State Resistance vs Junction Temperature

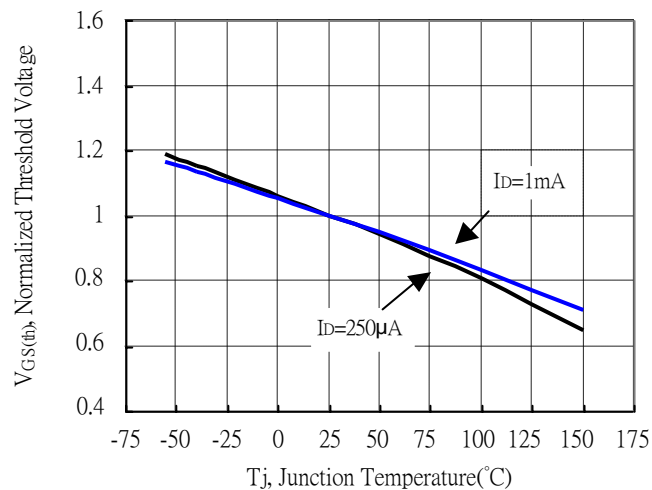


Typical Characteristics(Cont.)

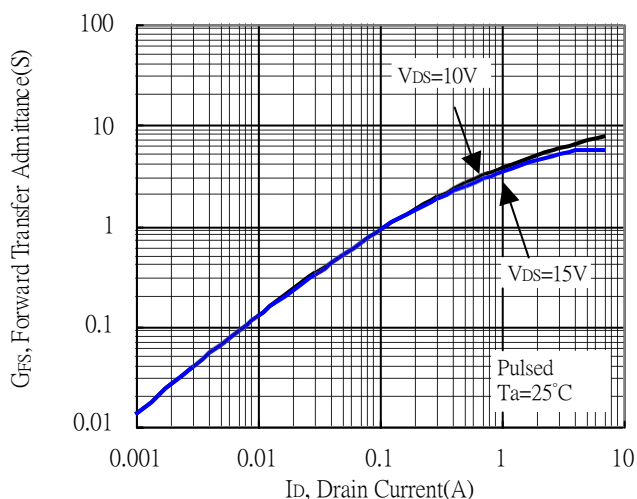
Capacitance vs Drain-to-Source Voltage



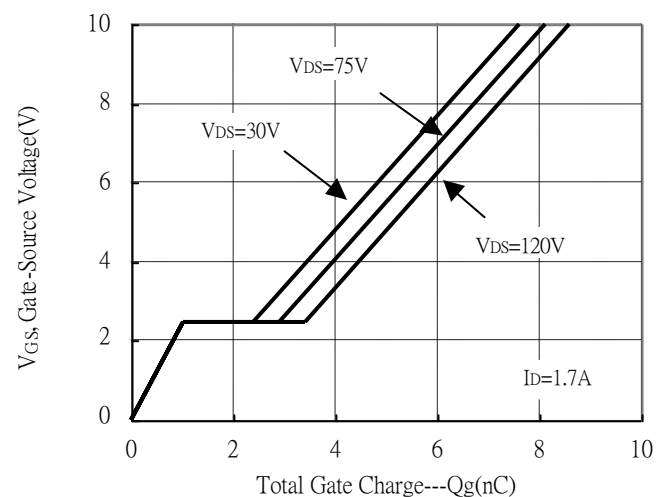
Threshold Voltage vs Junction Temperature



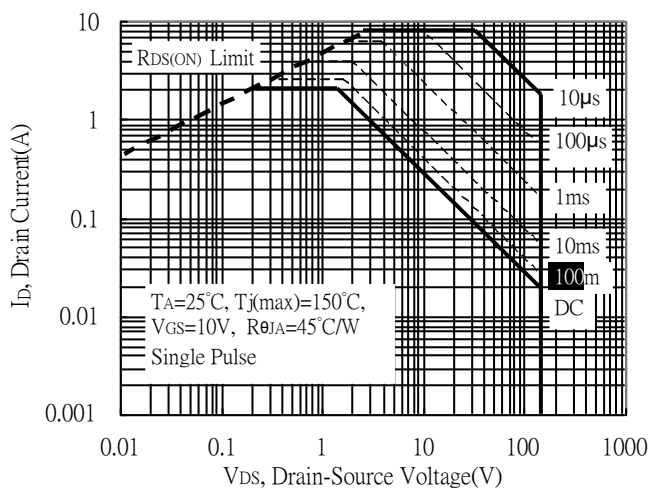
Forward Transfer Admittance vs Drain Current



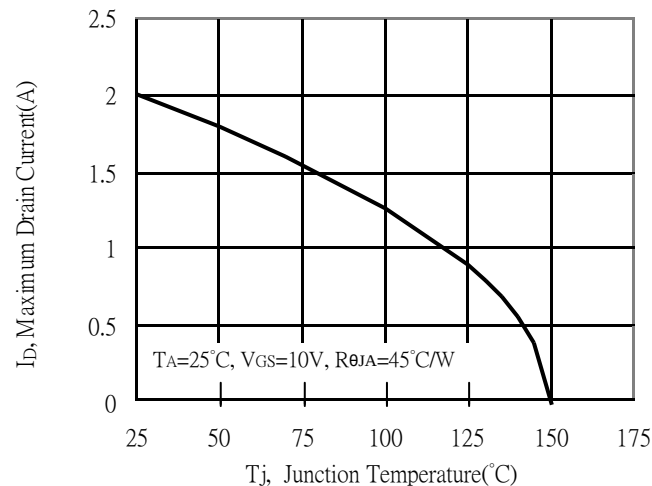
Gate Charge Characteristics



Maximum Safe Operating Area

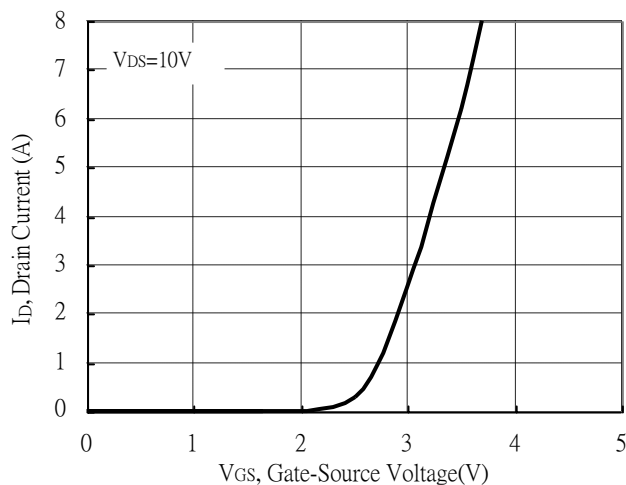


Maximum Drain Current vs Junction Temperature

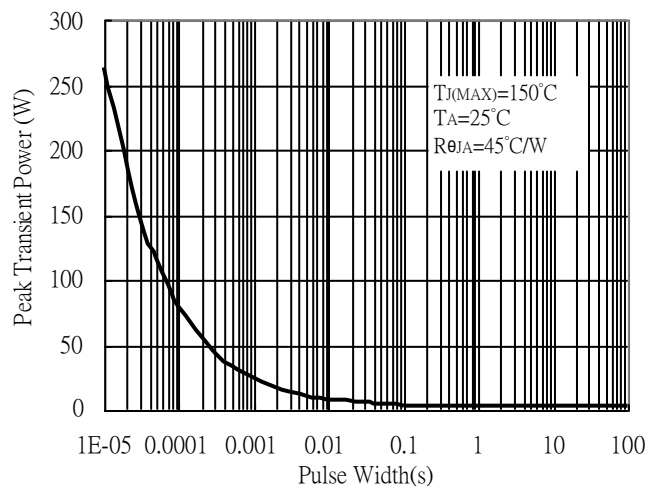


Typical Characteristics(Cont.)

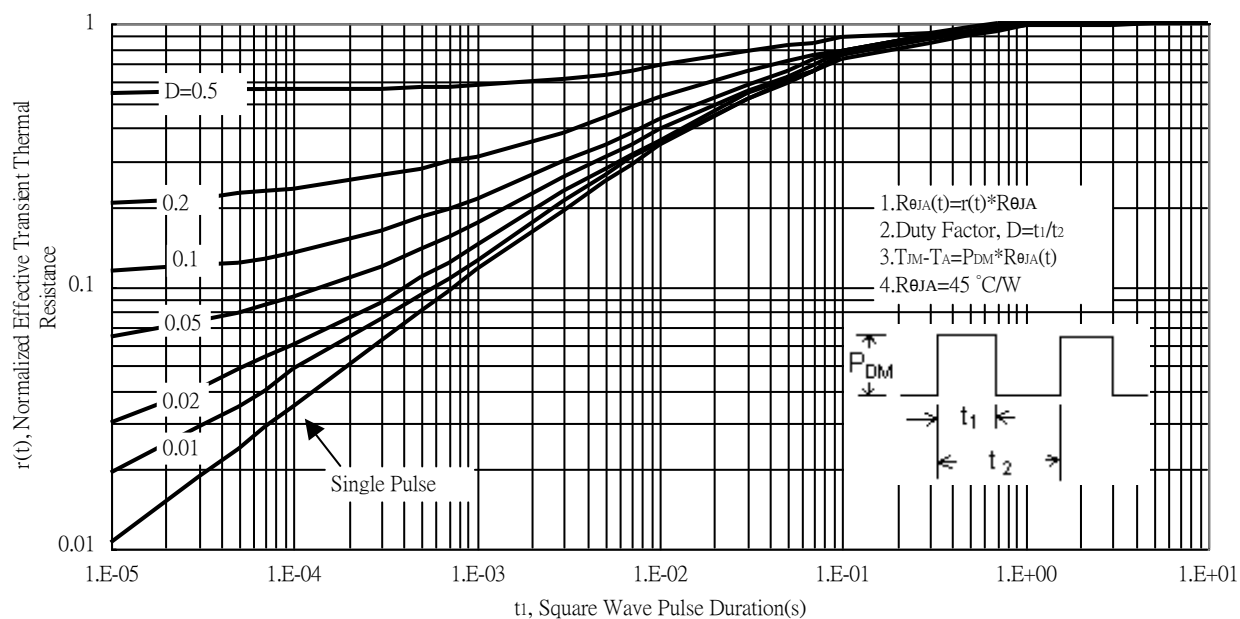
Typical Transfer Characteristics



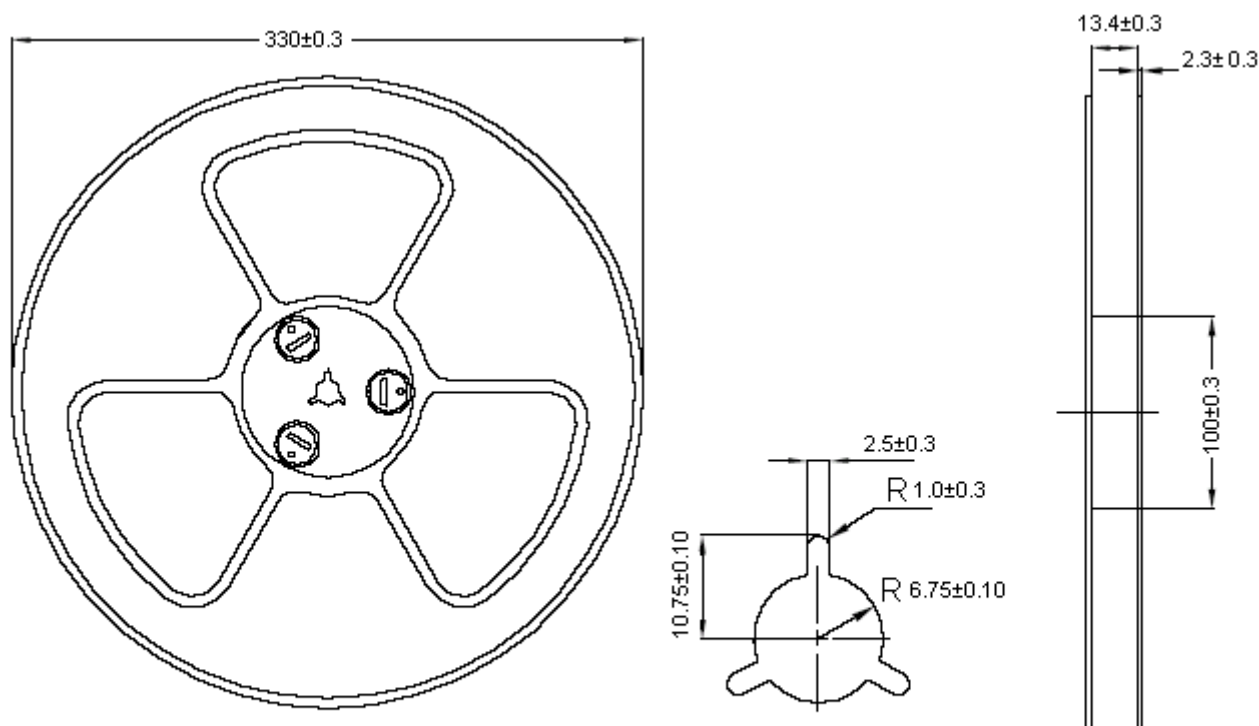
Single Pulse Maximum Power Dissipation



Transient Thermal Response Curves



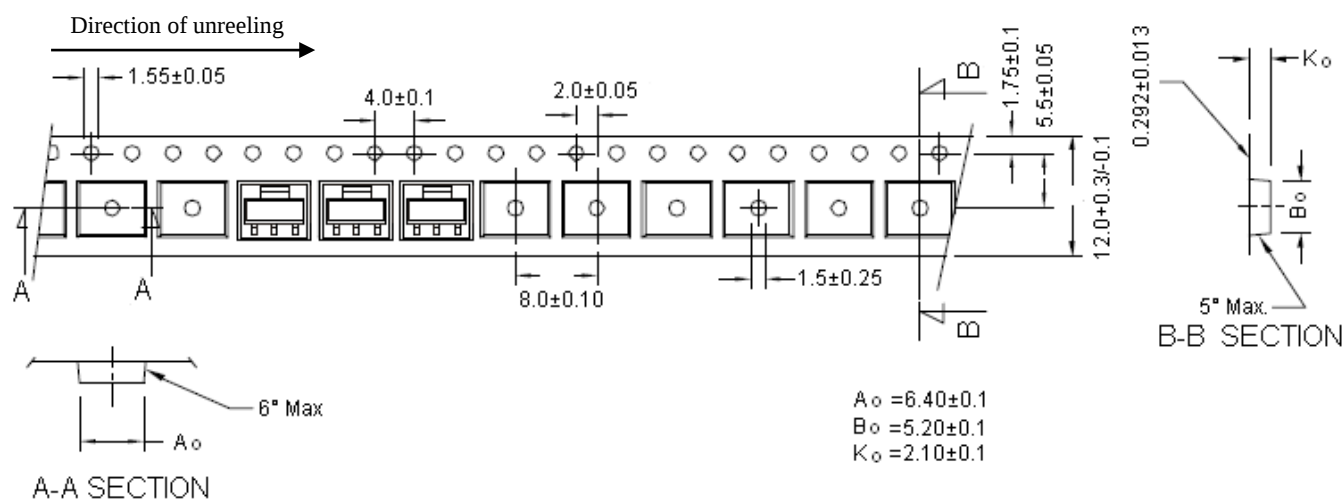
Reel Dimension



UNIT : mm

NOTE : 1. Material : Anti-static polystyrene
2. Surface resistivity $10^9 \Omega/\text{sq}$

Carrier Tape Dimension

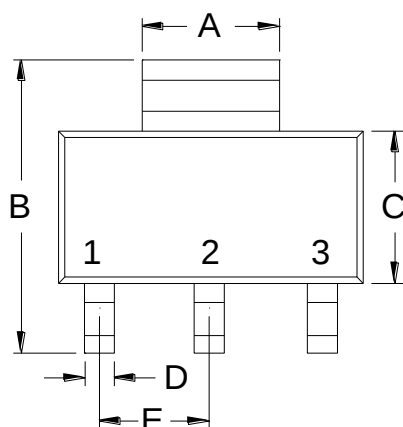


Notes:

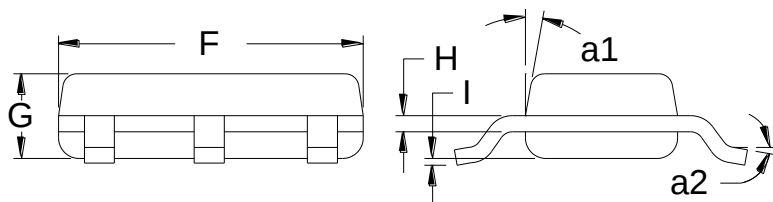
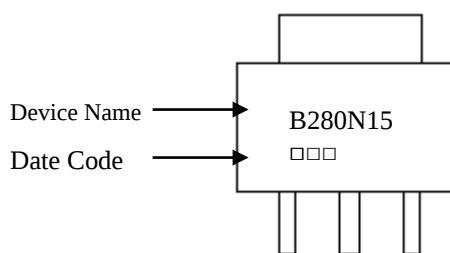
1. 10 sprocket hole pitch cumulative tolerance ± 0.2 .
2. Camber not to exceed 1mm in 100mm.
3. Material: conductive black polystyrene
4. A_o & B_o measured on a plane 0.3mm above the bottom of the pocket.
5. K_o measured from a plane on the inside bottom of the pocket to the top surface of the carrier.
6. Pocket position relative to sprocket hole measured as true position of pocket, not pocket hole.

Uni : millimeter

SOT-223 Dimension



Marking:



Style: Pin 1.Gate 2.Drain 3.Source

3-Lead SOT-223 Plastic
Surface Mounted Package

Date Code (counting from left to right) :

1st code: year code, the last digit of Christian year

2nd code : month code, Jan→A, Feb→B, Mar→C, Apr→D, May→E, Jun→F, Jul→G, Aug→H, Sep→J, Oct→K, Nov→L,
Dec→M

3rd and 4th codes : production serial number, 01~99

*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.1142	0.1220	2.90	3.10	G	0.0551	0.0709	1.40	1.80
B	0.2638	0.2874	6.70	7.30	H	0.0098	0.0138	0.23	0.35
C	0.1299	0.1457	3.30	3.70	I	0.0008	0.0039	0.02	0.10
D	0.0236	0.0315	0.60	0.80	a1	*13°	-	*13°	-
E	*0.0906	-	*2.30	-	a2	0°	10°	0°	10°
F	0.2480	0.2638	6.30	6.70					