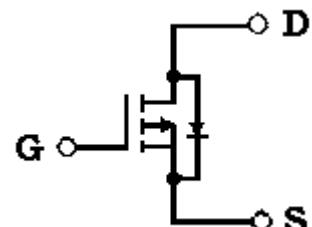
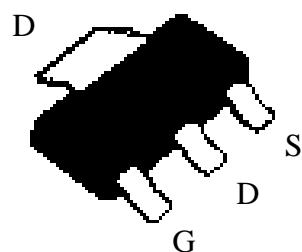


## P-Channel Enhancement Mode Power MOSFET

### Features:

- Low Gate Charge
- Simple Drive Requirement
- Pb-free lead plating & Halogen-free package

SOT-223



G : Gate D : Drain  
S : Source

BV <sub>DSS</sub>	-100V
I <sub>D</sub> @ T <sub>A</sub> =25°C, V <sub>GS</sub> =-10V	-2.2A
R <sub>DSON</sub> @V <sub>GS</sub> =-10V, I <sub>D</sub> =-2A	190mΩ (typ)
R <sub>DSON</sub> @V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-1A	217mΩ (typ)

### Ordering Information

Device	Package	Shipping
KLD190P10	SOT-223 (Pb-free lead plating & Halogen-free package)	2500 pcs / Tape & Reel

### Absolute Maximum Ratings ( $T_c=25^\circ\text{C}$ , unless otherwise noted)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	$V_{DS}$	-100	$\text{V}$
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current @ $V_{GS}=-10\text{V}$ , $T_A=25^\circ\text{C}$	$I_D$	-2.2	$\text{A}$
Continuous Drain Current @ $V_{GS}=-10\text{V}$ , $T_A=70^\circ\text{C}$		-1.8	
Pulsed Drain Current *1	$I_{DM}$	-10	$\text{A}$
Single Pulse Avalanche Current	$I_{AS}$	-2	
Avalanche Energy @ $L=6\text{mH}$ , $I_D=-2\text{A}$ , $R_G=25\Omega$	$E_{AS}$	12	$\text{mJ}$
Repetitive Avalanche Energy @ $L=0.05\text{mH}$ *2	$E_{AR}$	0.5	
Total Power Dissipation @ $T_A=25^\circ\text{C}$	$P_D$	2.5	$\text{W}$
Total Power Dissipation @ $T_A=70^\circ\text{C}$		1.6	
Operating Junction and Storage Temperature Range	$T_J$ , $T_{STG}$	-55~+150	$^\circ\text{C}$

Note : \*1. Pulse width limited by maximum junction temperature

\*2. Duty cycle  $\leq 1\%$

### Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	$R_{\Theta JC}$	15	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction-to-ambient, max	$R_{\Theta JA}$	50 (Note)	

Note : Surface mounted on a 1 in<sup>2</sup> pad of 2 oz. copper,  $t \leq 10\text{s}$ ;  $120^\circ\text{C}/\text{W}$  when mounted on minimum copper pad.

### Characteristics ( $T_c=25^\circ\text{C}$ , unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
$BV_{DSS}$	-100	-	-	$\text{V}$	$V_{GS}=0\text{V}$ , $I_D=-250\mu\text{A}$
$V_{GS(\text{th})}$	-1	-	-2.5		$V_{DS}=V_{GS}$ , $I_D=-250\mu\text{A}$
$I_{GSS}$	-	-	$\pm 100$	$\text{nA}$	$V_{GS}=\pm 20\text{V}$ , $V_{DS}=0\text{V}$
$ID_{SS}$	-	-	-1		$V_{DS}=-80\text{V}$ , $V_{GS}=0\text{V}$
	-	-	-25	$\mu\text{A}$	$V_{DS}=-70\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=125^\circ\text{C}$
$R_{DS(\text{ON})}$ *1	-	190	240	$\text{m}\Omega$	$V_{GS}=-10\text{V}$ , $I_D=-2\text{A}$
	-	217	285		$V_{GS}=-4.5\text{V}$ , $I_D=-1\text{A}$
$G_{FS}$ *1	-	5	-	$\text{S}$	$V_{DS}=-15\text{V}$ , $I_D=-2\text{A}$
<b>Dynamic</b>					
$Q_g$ *1, 2	-	16.5	-	$\text{nC}$	$I_D=-2\text{A}$ , $V_{DS}=-80\text{V}$ , $V_{GS}=-10\text{V}$
$Q_{gs}$ *1, 2	-	2.6	-		
$Q_{gd}$ *1, 2	-	4.3	-		
$t_{d(\text{ON})}$ *1, 2	-	9	-	$\text{ns}$	$V_{DS}=-50\text{V}$ , $I_D=-1\text{A}$ , $V_{GS}=-10\text{V}$ , $R_G=6\Omega$
$t_r$ *1, 2	-	16.8	-		
$t_{d(\text{OFF})}$ *1, 2	-	40.4	-		
$t_f$ *1, 2	-	27.2	-		
$C_{iss}$	-	729	-	$\text{pF}$	$V_{GS}=0\text{V}$ , $V_{DS}=-25\text{V}$ , $f=1\text{MHz}$
$C_{oss}$	-	45	-		
$C_{rss}$	-	35	-		

**Source-Drain Diode**

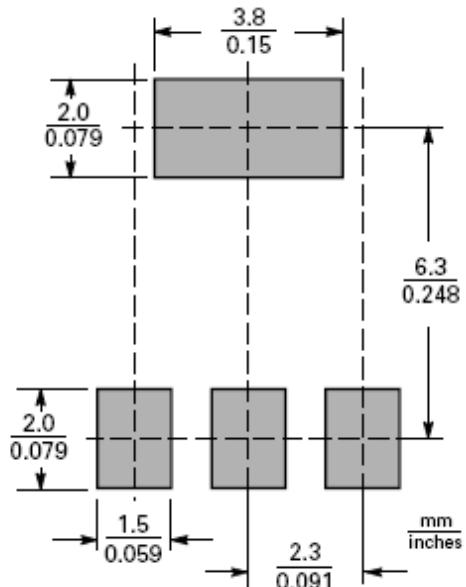
I <sub>S</sub> *1	-	-	-2.2	A	
I <sub>SM</sub> *3	-	-	-10		
V <sub>SD</sub> *1	-	-0.82	-1.2	V	I <sub>S</sub> =-2A, V <sub>GS</sub> =0V
t <sub>rr</sub>	-	21	-	ns	
Q <sub>rr</sub>	-	20	-	nC	I <sub>F</sub> =-2A, dI <sub>F</sub> /dt=100A/μs

Note : \*1.Pulse Test : Pulse Width  $\leq$ 300μs, Duty Cycle $\leq$ 2%

\*2.Independent of operating temperature

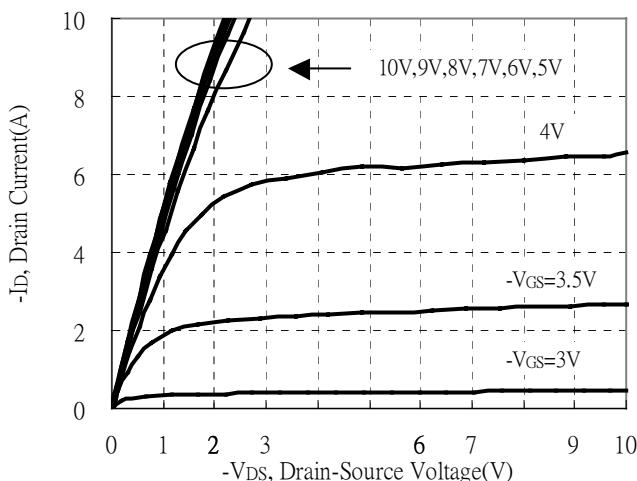
\*3.Pulse width limited by maximum junction temperature.

**Recommended soldering footprint**

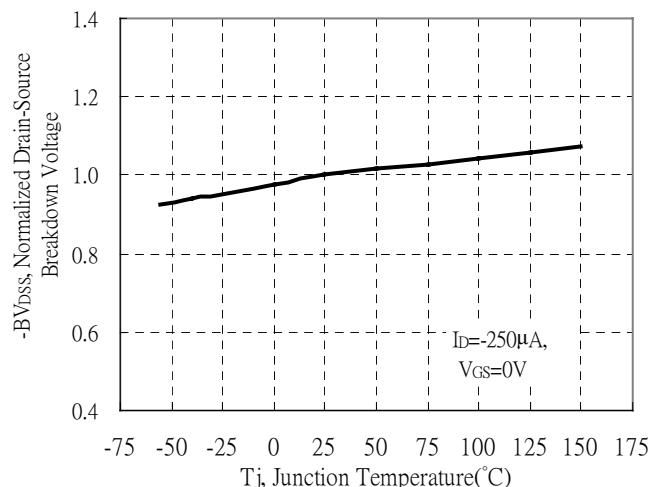


## Typical Characteristics

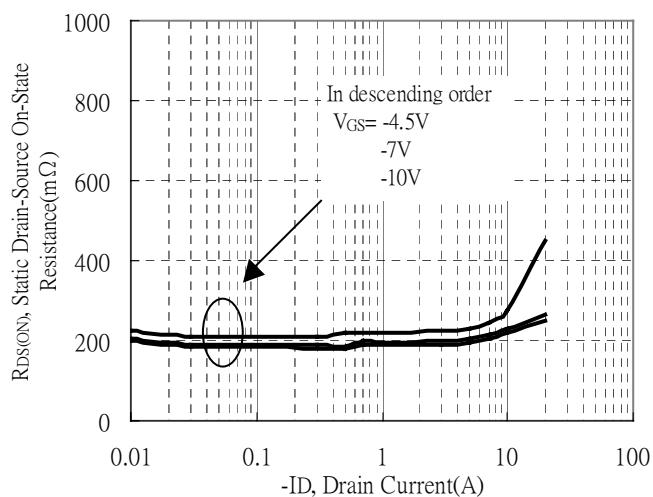
Typical Output Characteristics



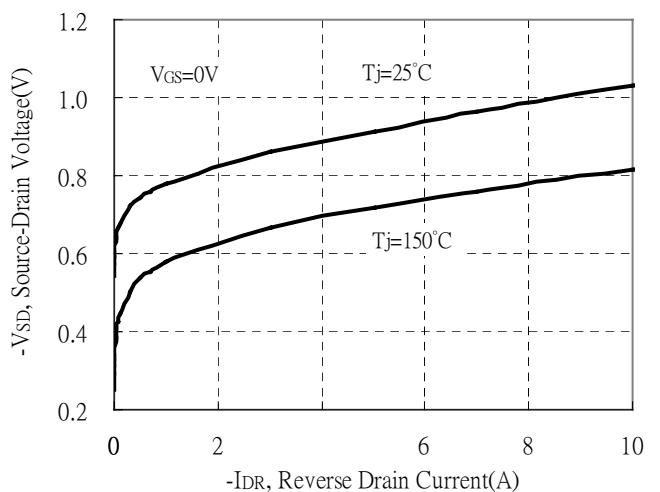
Breakdown Voltage vs Ambient Temperature



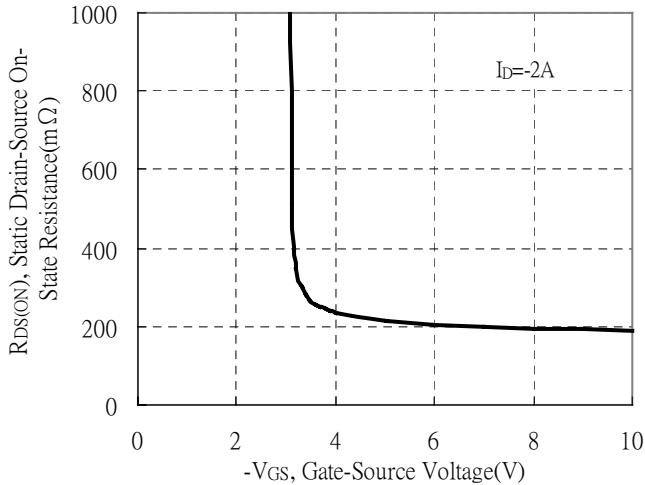
Static Drain-Source On-State resistance vs Drain Current



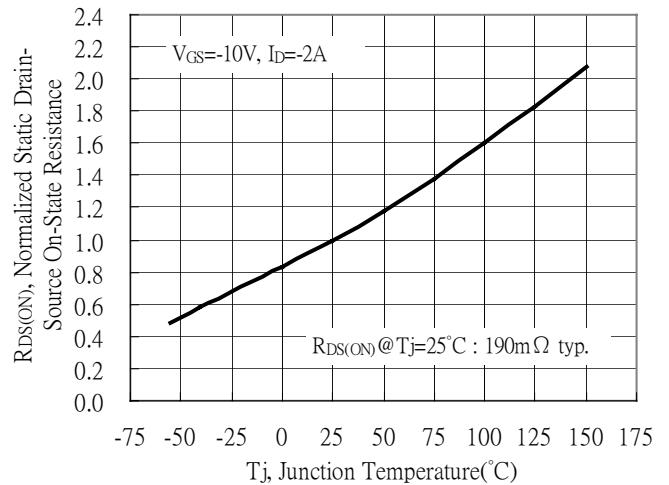
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

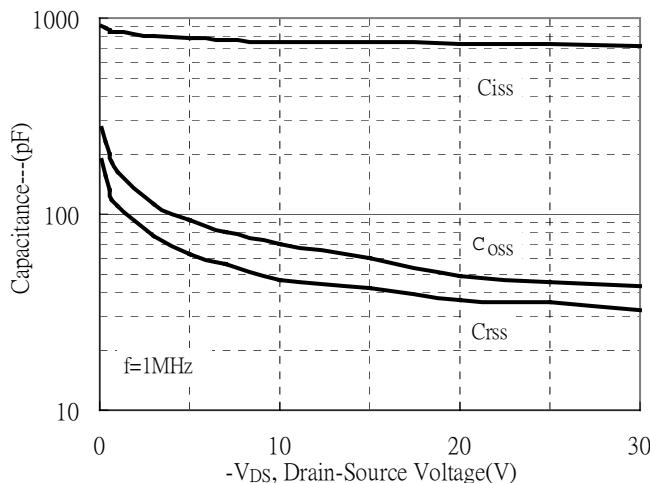


Drain-Source On-State Resistance vs Junction Temperature

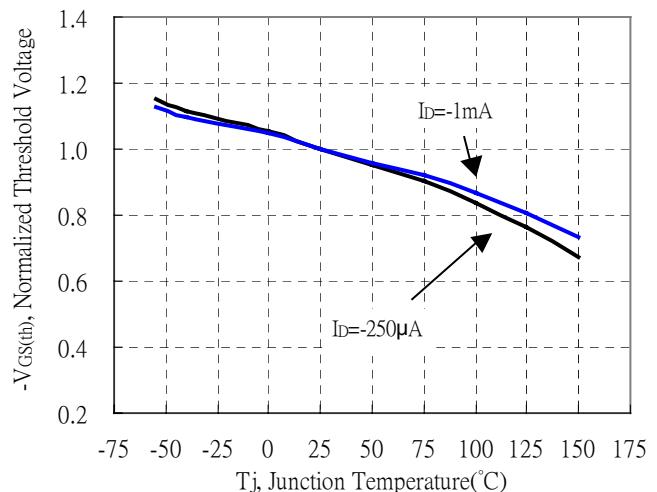


## Typical Characteristics(Cont.)

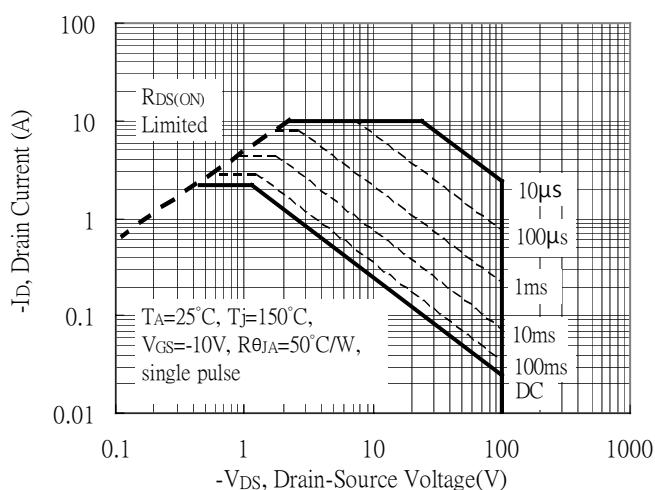
Capacitance vs Drain-to-Source Voltage



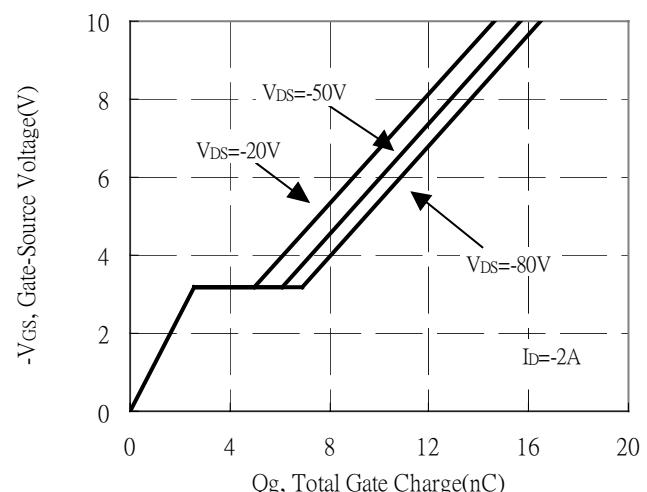
Threshold Voltage vs Junction Temperature



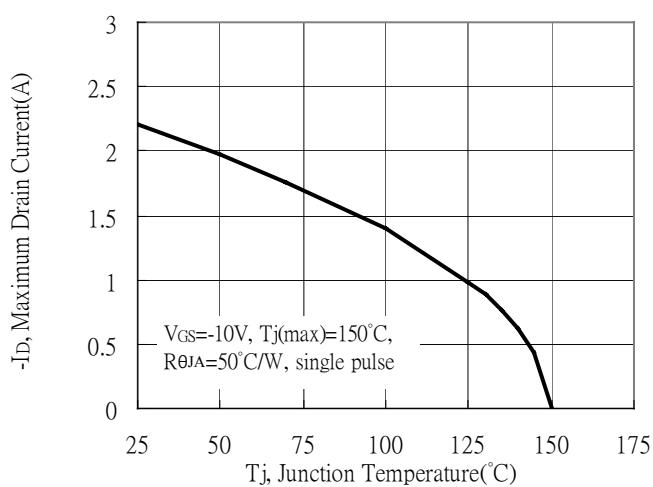
Maximum Safe Operating Area



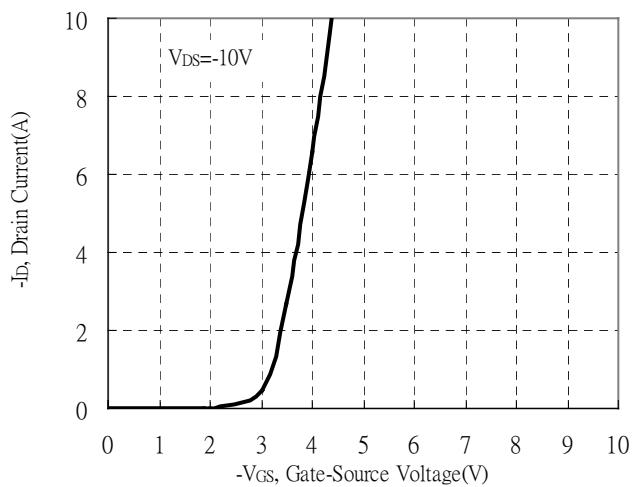
Gate Charge Characteristics



Maximum Drain Current vs Junction Temperature

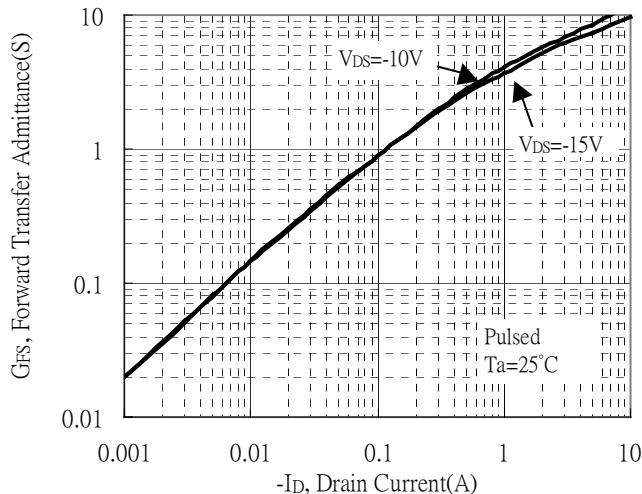


Typical Transfer Characteristics

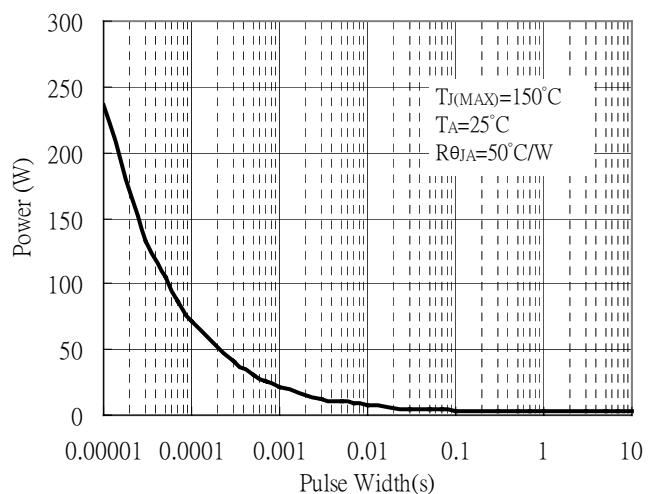


## Typical Characteristics(Cont.)

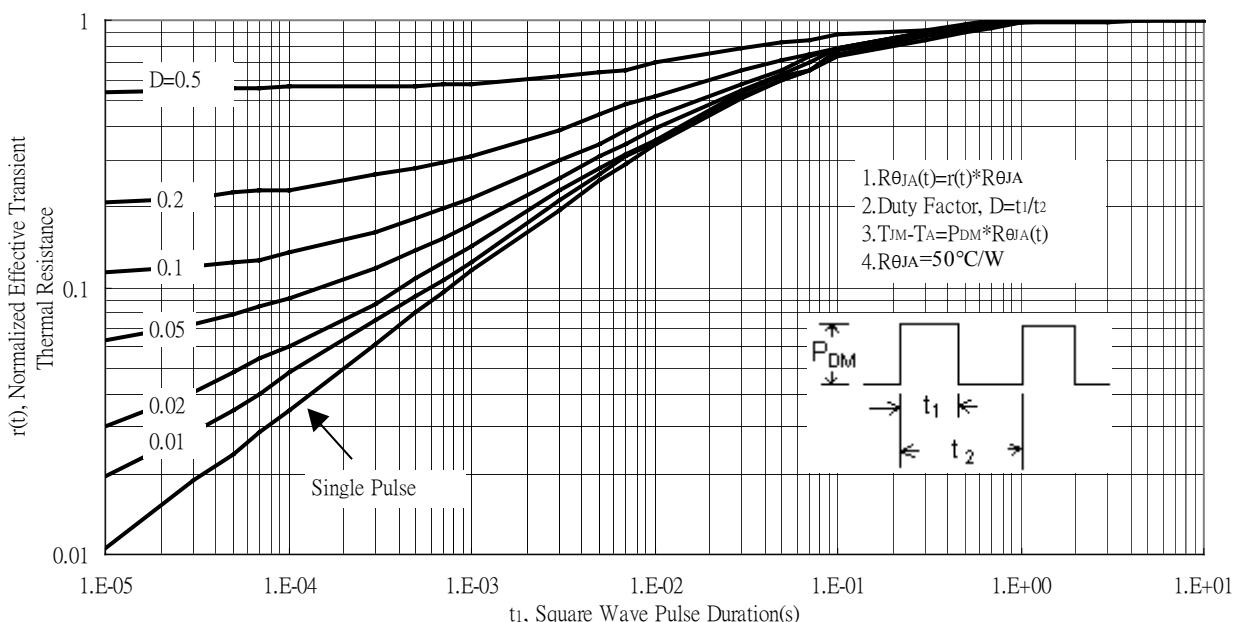
Forward Transfer Admittance vs Drain Current



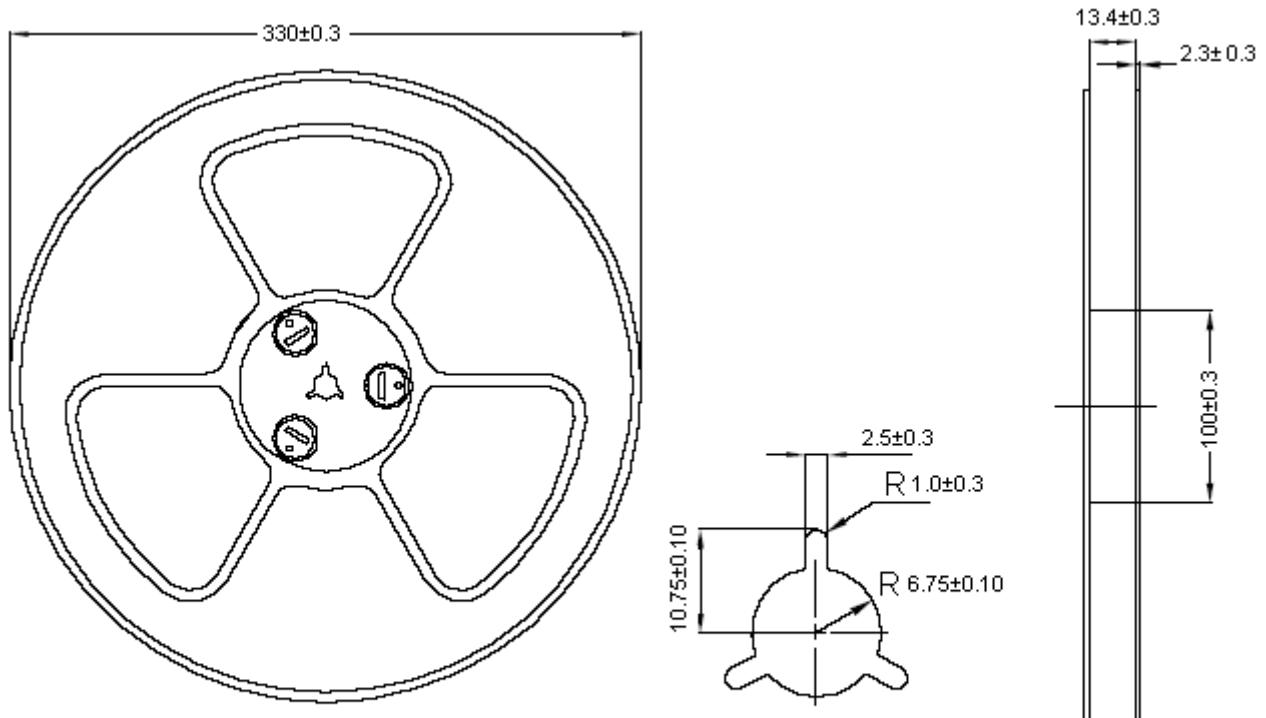
Single Pulse Power Rating, Junction to Case



Transient Thermal Response Curves



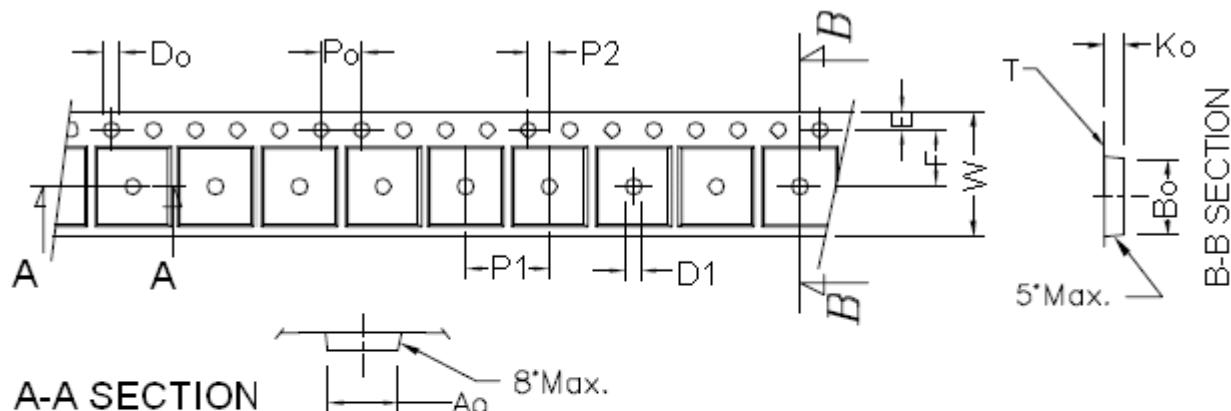
## Reel Dimension



UNIT : mm

NOTE : 1. Material : Anti-static polystyrene  
 2. Surface resistivity  $10^9 \Omega/\text{sq}$

## Carrier Tape Dimension



A-A SECTION

$8 \pm 0.5$

$Ao$

Unit : mm

Symbol Ao Bo Ko Po P1 P2 T

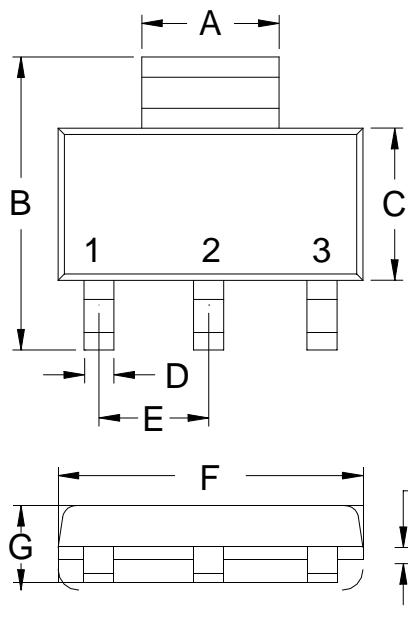
Spec  $6.83 \pm 0.1$   $7.42 \pm 0.1$   $1.88 \pm 0.1$   $4.0 \pm 0.1$   $8.0 \pm 0.10$   $2.0 \pm 0.05$   $0.292 \pm 0.02$

Symbol E F Do D1 W 10Po

Spec  $1.75 \pm 0.1$   $5.5 \pm 0.05$   $1.60 \pm 0.1$   $1.5 \pm 0.25$   $12^{+0.3}_{-0.1}$   $40.0 \pm 0.2$

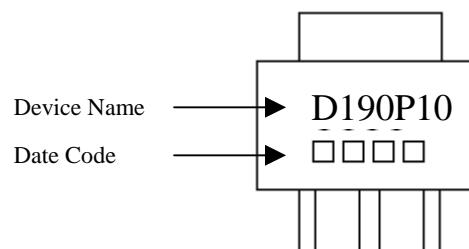
Unit : mm

## SOT-223 Dimension



Style: Pin 1.Gate 2.Drain 3.Source

Marking:



Date Code(counting from left to right) :  
 1<sup>st</sup> code: year code, the last digit of Christian year  
 2<sup>nd</sup> code : month code, Jan→A, Feb→B, Mar→C,  
 Apr→D, May→E, Jun→F, Jul→G, Aug→  
 H, Sep→J, Oct→K, Nov→L, Dec→M  
 3<sup>rd</sup> and 4<sup>th</sup> codes : production serial number, 01~99

3-Lead SOT-223 Plastic  
 Surface Mounted Package

\*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.1142	0.1220	2.90	3.10	G	0.0551	0.0709	1.40	1.80
B	0.2638	0.2874	6.70	7.30	H	0.0098	0.0138	0.25	0.35
C	0.1299	0.1457	3.30	3.70	I	0.0008	0.0039	0.02	0.10
D	0.0236	0.0315	0.60	0.80	a1	*13°	-	*13°	-
E	*0.0906	-	*2.30	-	a2	0 °	10 °	0 °	10 °
F	0.2480	0.2638	6.30	6.70					