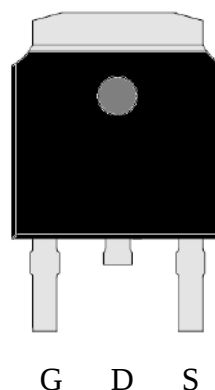


N-Channel Enhancement Mode Power MOSFET

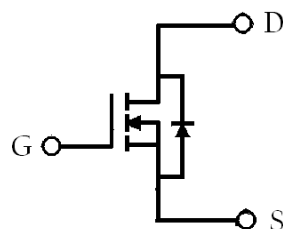
Features:

- Low On Resistance
- Simple Drive Requirement
- Low Gate Charge
- Fast Switching Characteristic
- Pb-free lead plating and halogen-free package

TO-252(DPAK)



BV_{DSS}	60V
I_D@V_{GS}=10V, T_C=25°C	50A
R_{DS(ON)}@V_{GS}=10V, I_D=20A	7 mΩ(typ)



G : Gate D : Drain S : Source

Ordering Information

Device	Package	Shipping
KJE09N06	TO-252 (Pb-free lead plating and halogen-free package)	2500 pcs / tape& reel

Absolute Maximum Ratings (T_C=25°C)

Parameter		Symbol	Limits	Unit
Drain-Source Voltage (Note 1)		V _{DS}	60	V
Gate-Source Voltage		V _{GS}	±20	
Continuous Drain Current @T _C =25°C, V _{GS} =10V(silicon limit) (Note 1)		I _D	70	A
Continuous Drain Current @T _C =100°C, V _{GS} =10V(silicon limit) (Note 1)			50	
Continuous Drain Current @T _C =25°C, V _{GS} =10V(package limit) (Note 1)			50	
Continuous Drain Current @T _A =25°C, V _{GS} =10V (Note 2)		I _{DSM}	13	
Continuous Drain Current @T _A =70°C, V _{GS} =10V (Note 2)			10	
Pulsed Drain Current @ V _{GS} =10V (Note 3)		I _{DM}	180	
Avalanche Current (Note 3)		I _{AS}	45	
Single Pulse Avalanche Energy @ L=0.1mH, I _D =45A, V _{DD} =25V (Note 2)		E _{AS}	101	mJ
Repetitive Avalanche Energy (Note 3)		E _{AR}	10	
Power Dissipation	T _C =25°C (Note 1)	P _D	75	W
	T _C =100°C (Note 1)		37.5	
	T _A =25°C (Note 2)	P _{DSM}	2.5	
	T _A =70°C (Note 2)		1.6	
Operating Junction and Storage Temperature		T _j , T _{stg}	-55~+175	°C

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	R _{θJC}	2	°C/W
Thermal Resistance, Junction-to-ambient, max (Note 2)	R _{θJA}	50	
Thermal Resistance, Junction-to-ambient, max (Note 4)		110	

Note : 1. The power dissipation P_D is based on T_{J(MAX)}=175 °C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.

2. The value of R_{θJA} is measured with the device mounted on 1 in² FR-4 board with 2 oz. copper, in a still air environment with T_A=25 °C. The power dissipation P_{DSM} is based on R_{θJA} and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.

3. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=175 °C. Ratings are based on low frequency and low duty cycles to keep initial T_J=25°C.

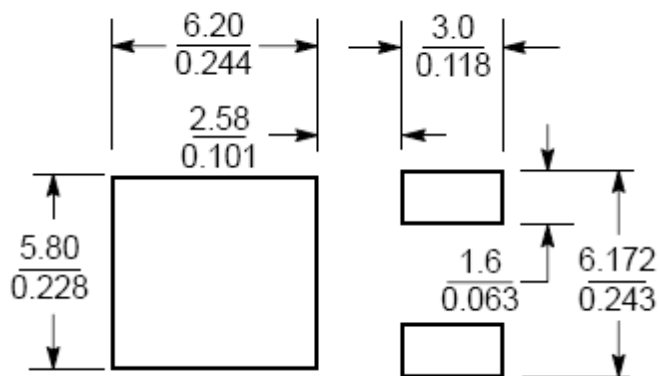
4. When mounted on the minimum pad size recommended (PCB mount), t≤10s.

Characteristics (Tj=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	60	-	-	V	V _{GS} =0V, I _D =250μA
ΔBV _{DSS} /ΔTj	-	0.06	-	V/°C	Reference to 25°C, I _D =250μA
V _{GS(th)}	2.0	-	4.0	V	V _{DS} = V _{GS} , I _D =250μA
*G _{FS}	-	29	-	S	V _{DS} = 5V, I _D =20A
I _{GSS}	-	-	±100	nA	V _{GS} =±20V, V _{DS} =0V
I _{DSS}	-	-	1	μA	V _{DS} = 48V, V _{GS} = 0V
	-	-	10		V _{DS} = 48V, V _{GS} = 0V, Tj=125°C
*R _{DS(ON)}	-	7	9.5	mΩ	V _{GS} = 10V, I _D =20A
Dynamic					
*Q _g	-	32	-	nC	V _{DD} =30V, I _D =20A, V _{GS} =10V
*Q _{gs}	-	7.5	-		
*Q _{gd}	-	9.7	-		
*t _{d(ON)}	-	16	-	ns	V _{DD} =30V, I _D =20A, V _{GS} =10V, R _G =3Ω
*t _r	-	22.8	-		
*t _{d(OFF)}	-	39.4	-		
*t _f	-	15	-		
C _{iss}	-	1500	-	pF	V _{GS} =0V, V _{DS} =30V, f=1MHz
C _{oss}	-	272	-		
C _{rss}	-	140	-		
R _g	-	2.2	-	Ω	f=1MHz
Source-Drain Diode					
*I _S	-	-	50	A	
*V _{SD}	-	0.69	1	V	I _S =1A, V _{GS} =0V
*t _{rr}	-	21	-	ns	V _{GS} =0V, I _F =20A, dI _F /dt=100A/μs
*Q _{rr}	-	18.5	-	nC	

*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

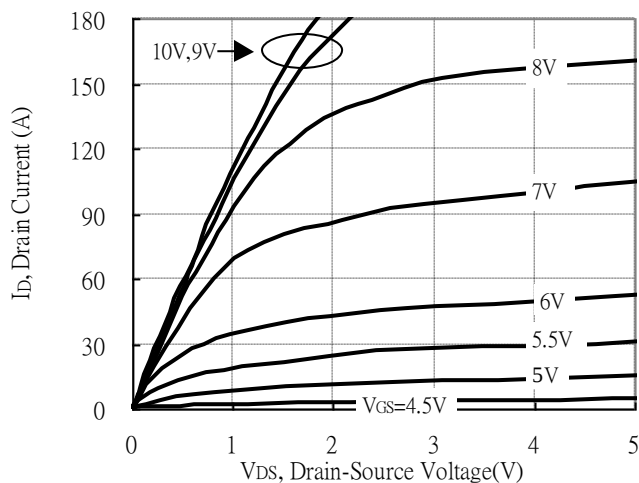
Recommended soldering footprint



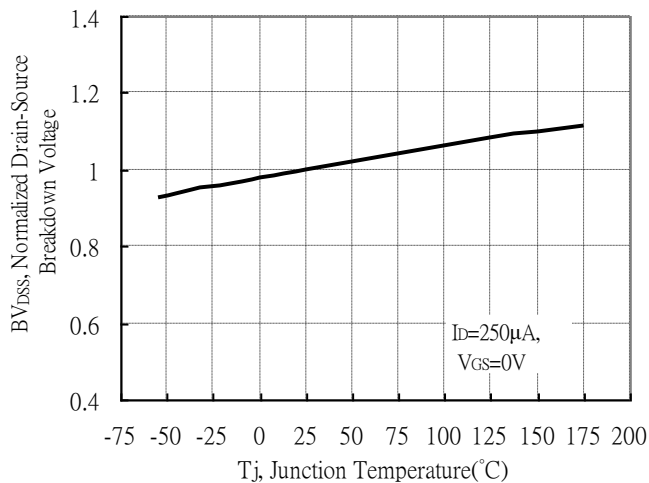
Unit ($\frac{\text{mm}}{\text{inch}}$)

Typical Characteristics

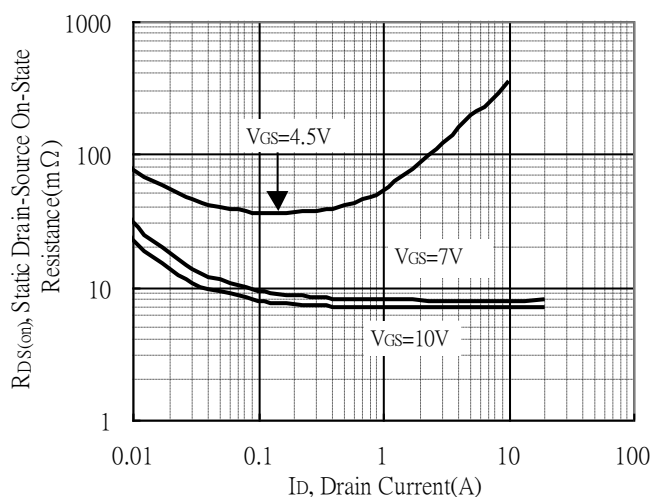
Typical Output Characteristics



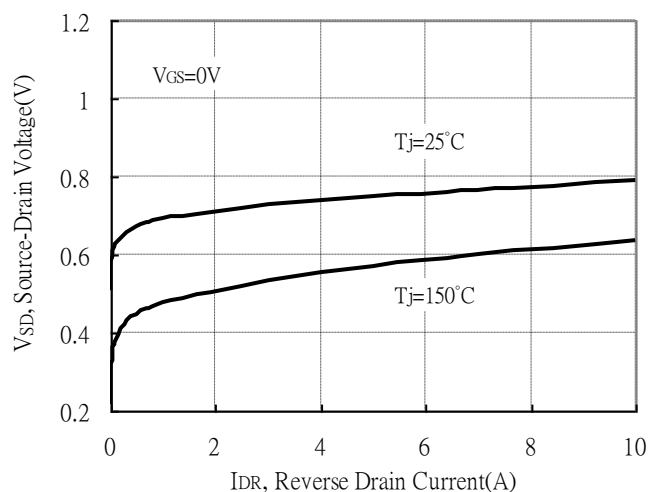
Breakdown Voltage vs Ambient Temperature



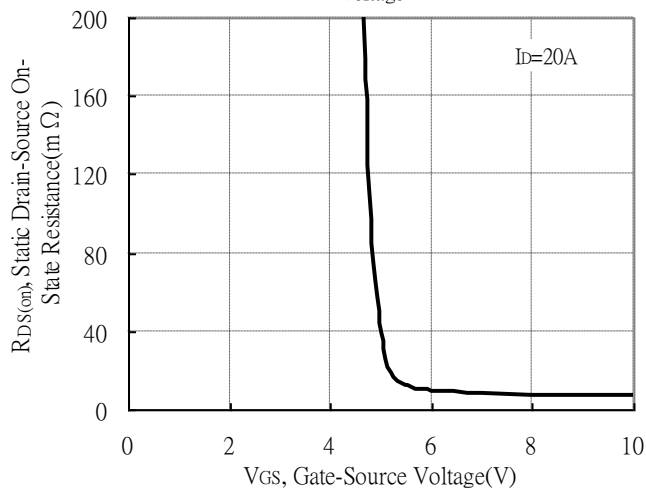
Static Drain-Source On-State resistance vs Drain Current



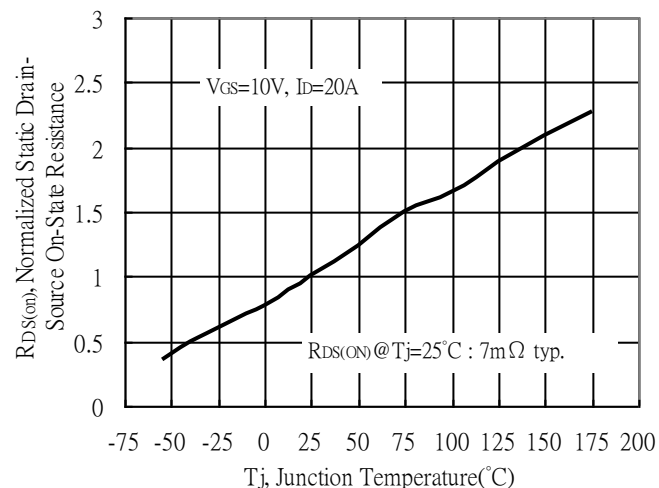
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

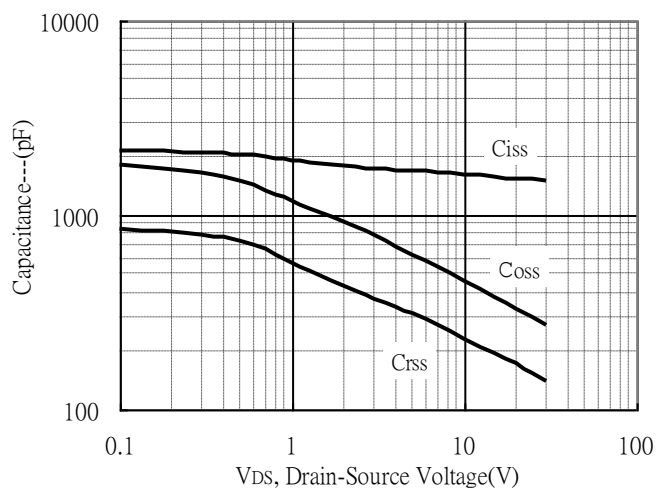


Drain-Source On-State Resistance vs Junction Temperature

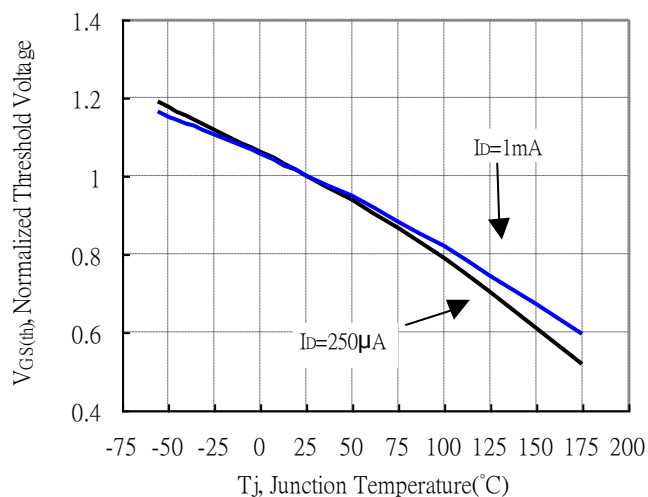


Typical Characteristics(Cont.)

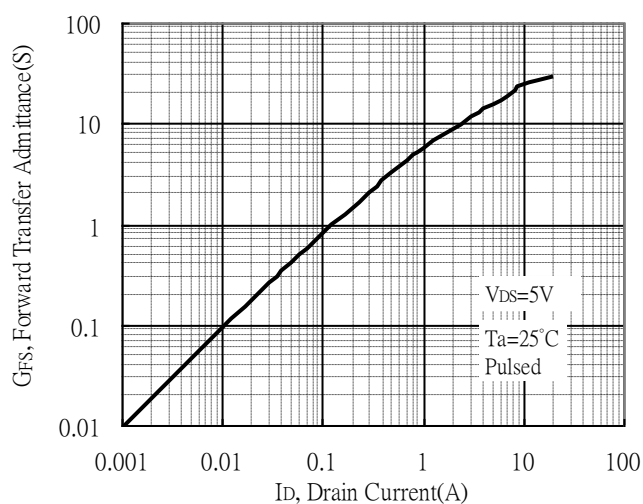
Capacitance vs Drain-to-Source Voltage



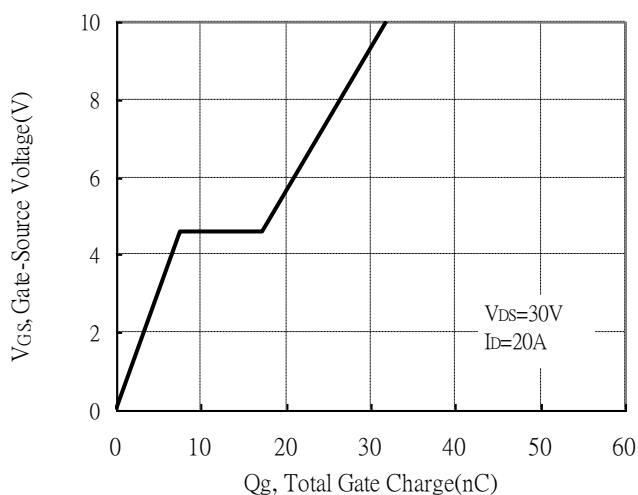
Threshold Voltage vs Junction Temperature



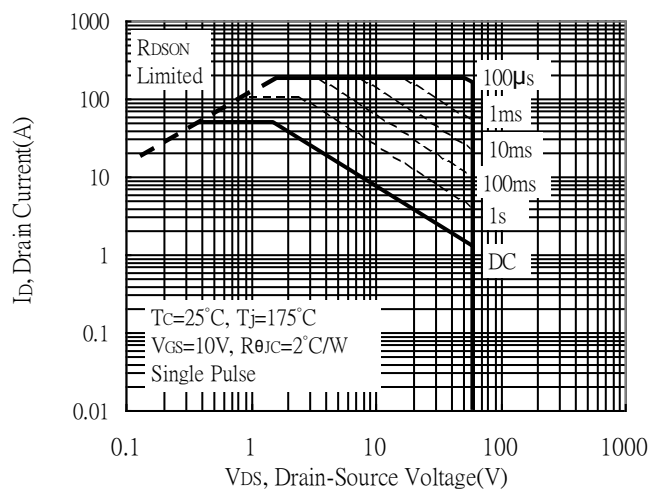
Forward Transfer Admittance vs Drain Current



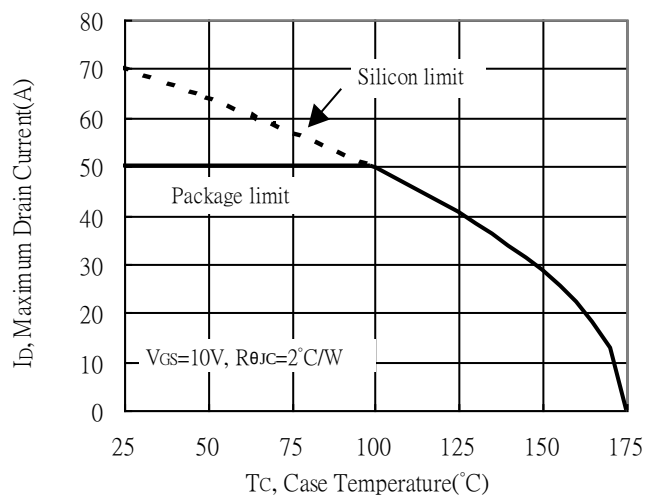
Gate Charge Characteristics



Maximum Safe Operating Area

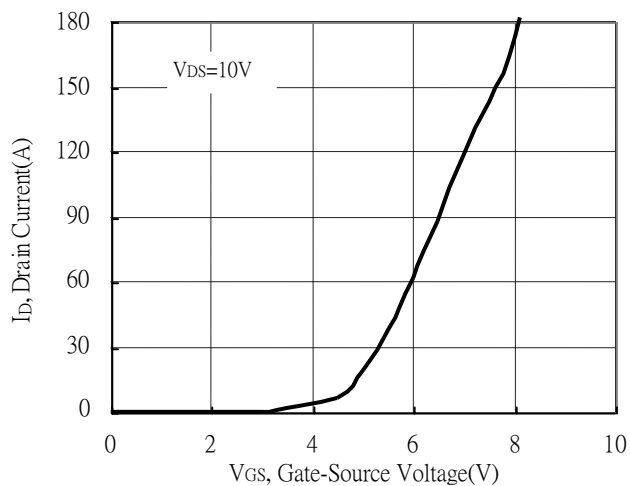


Maximum Drain Current vs Case Temperature

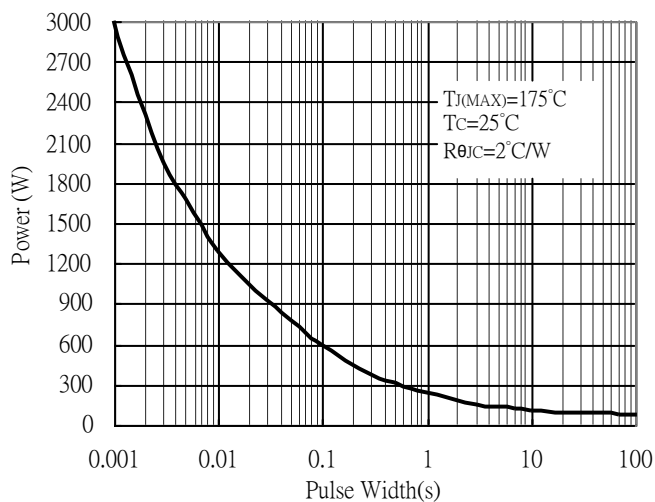


Typical Characteristics(Cont.)

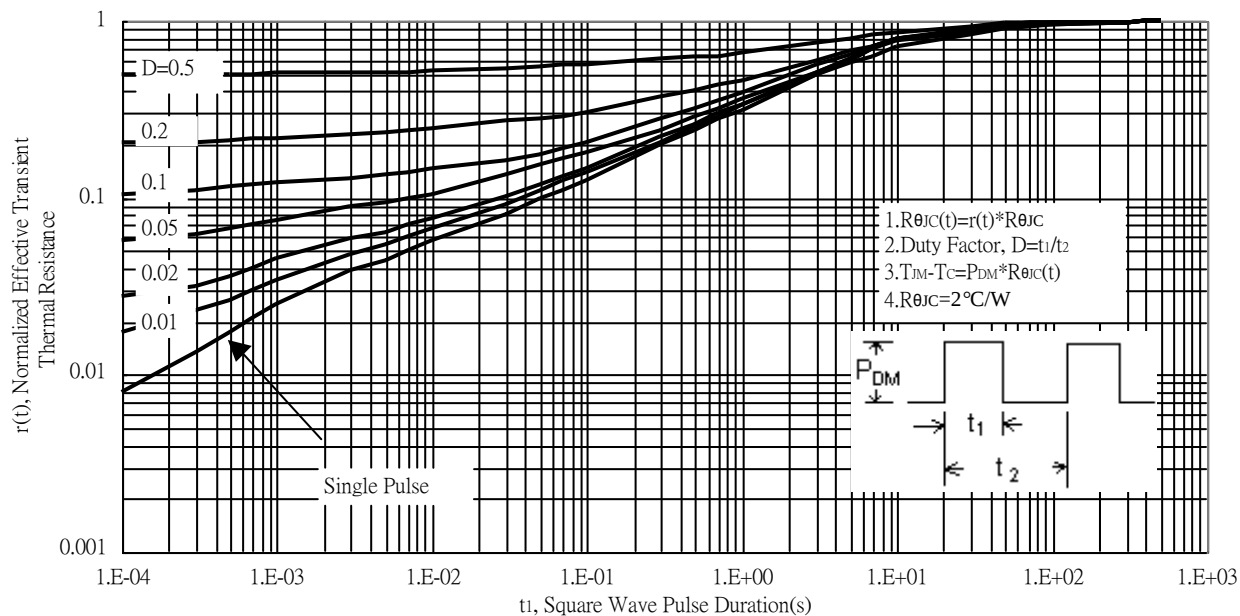
Typical Transfer Characteristics



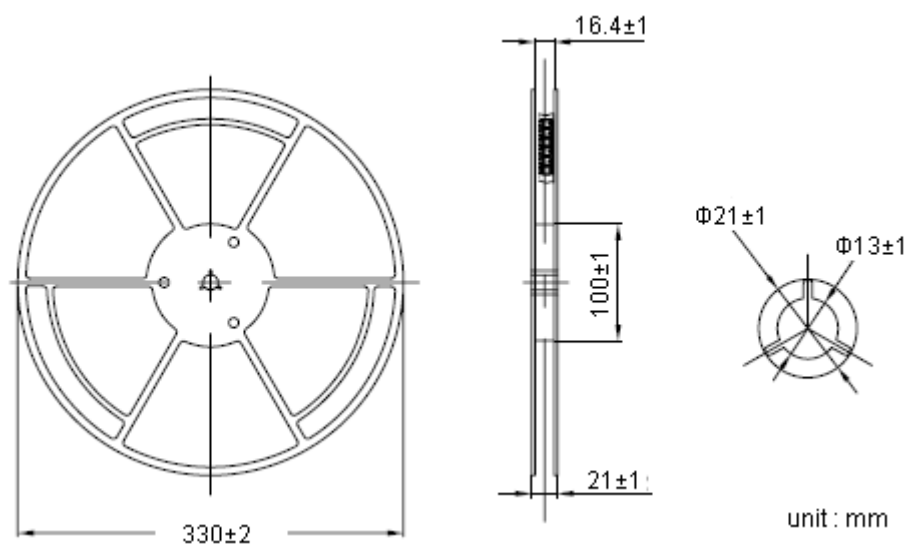
Single Pulse Power Rating, Junction to Case



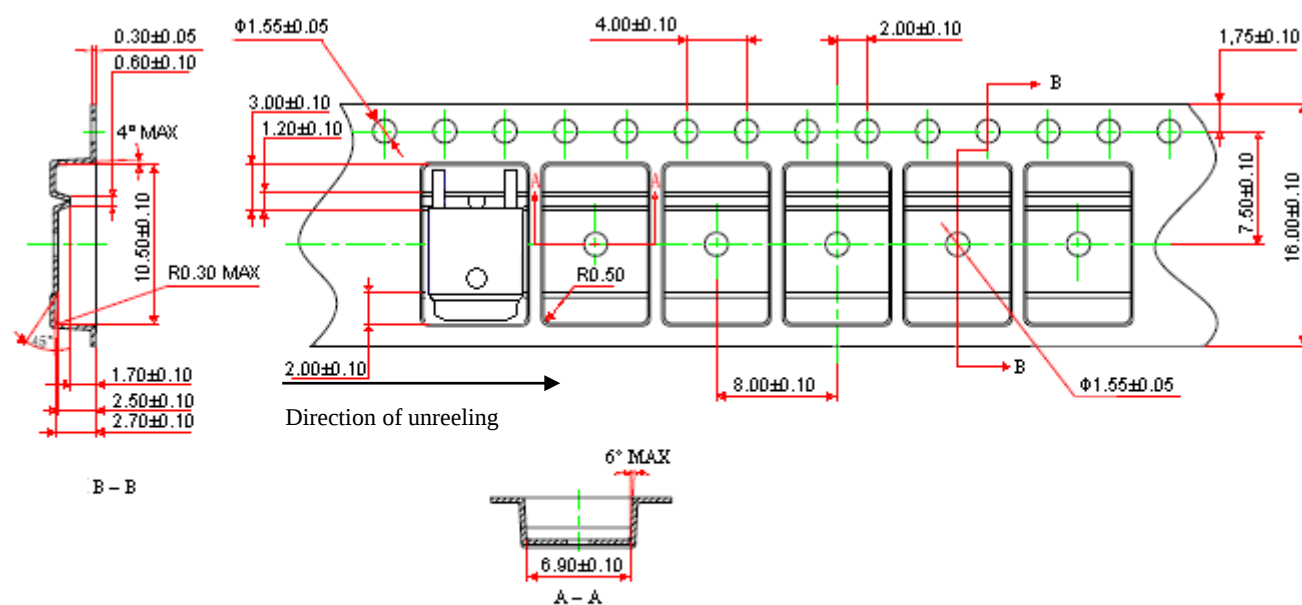
Transient Thermal Response Curves



Reel Dimension



Carrier Tape Dimension

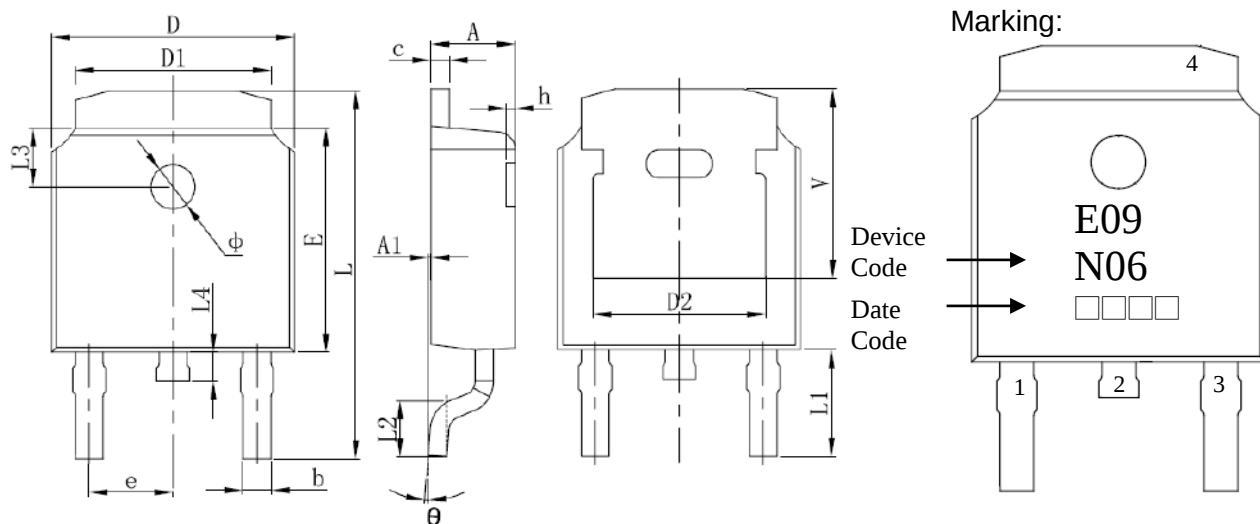


Notes:

- Notes:
1. 10 sprocket hole pitch cumulative tolerance ± 0.2 .
 2. Camber not to exceed 1mm in 100mm.
 3. Material: conductive black polystyrene, antistatic coated : $10^5 \Omega/\square \sim 10^{11} \Omega/\square$

unit : mm

TO-252 Dimension



3-Lead TO-252 Plastic Surface Mount Package

Style: Pin 1.Gate 2.Drain 3.Source
 4.Drain

Date Code :

First Code : Last digit of Christian Year

Second Code : Month Code : Jan→A, Feb→B, Mar→C, Apr→D, May→E, Jun→F, Jul→G,
 Aug→H, Sep→J, Oct→K, Nov→L, Dec→M

Last Two Codes : Production Serial Code, 01~99

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.087	0.094	2.200	2.400	L	0.382	0.406	9.712	10.312
A1	0.000	0.005	0.000	0.127	L1	0.114	REF	2.900	REF
b	0.025	0.030	0.635	0.770	L2	0.055	0.067	1.400	1.700
c	0.018	0.023	0.460	0.580	L3	0.063	REF	1.600	REF
D	0.256	0.264	6.500	6.700	L4	0.024	0.039	0.600	1.000
D1	0.201	0.215	5.100	5.460	Φ	0.043	0.051	1.100	1.300
D2	0.190	REF	4.830	REF	θ	0°	8°	0°	8°
E	0.236	0.244	6.000	6.200	h	0.000	0.012	0.000	0.300
e	0.086	0.094	2.186	2.386	v	0.207	REF	5.250	REF