

## N-Ch 60V Fast Switching MOSFETs

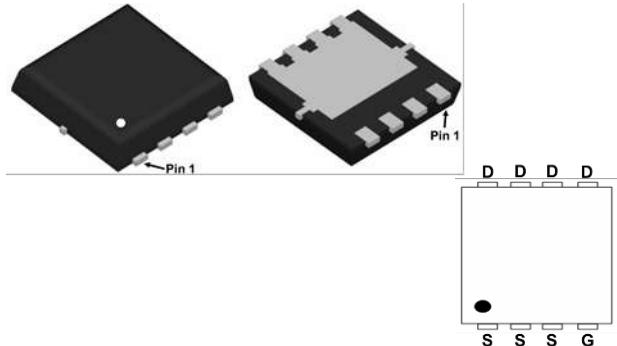
### Description:

- Advanced Trench MOS Technology
- Low Gate Charge
- Low  $R_{DS(ON)}$
- 100% EAS Guaranteed
- Green Device Available

### Application:

- Motor Control.
- DC/DC Converter.
- Synchronous rectifier applications.

### PRPAK3x3 Pin Configuration



### Product Summary

BVDSS	RDS(on)	ID
60V	8.5mΩ	30A

### Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	60	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D @ T_c = 25^\circ C$	Continuous Drain Current <sup>1,6</sup>	30	A
$I_D @ T_c = 100^\circ C$		27	A
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	100	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	26.5	mJ
$I_{AS}$	Avalanche Current	23	A
$P_D @ T_c = 25^\circ C$	Total Power Dissipation <sup>4</sup>	27.7	W
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient <sup>1</sup> ( $t \leq 10S$ )	---	25	°C/W
	Thermal Resistance Junction-ambient <sup>1</sup> (Steady State)	---	55	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-case <sup>1</sup>	---	4.5	°C/W

**Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$	60	---	---	V
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{\text{GS}}=10\text{V}$ , $I_D=15\text{A}$	---	7.0	8.5	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$ , $I_D=15\text{A}$	---	10.5	12.5	$\text{m}\Omega$
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D=250\mu\text{A}$	1.2	---	2.3	V
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=48\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	1	$\text{uA}$
		$V_{\text{DS}}=48\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=55^\circ\text{C}$	---	---	5	
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA
$R_g$	Gate Resistance	$V_{\text{DS}}=0\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	1.3	---	$\Omega$
$Q_g$	Total Gate Charge (10V)	$V_{\text{DS}}=30\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $I_D=15\text{A}$	---	15	---	$\text{nC}$
$Q_{\text{gs}}$	Gate-Source Charge		---	3.5	---	
$Q_{\text{gd}}$	Gate-Drain Charge		---	4.2	---	
$T_{\text{d}(\text{on})}$	Turn-On Delay Time	$V_{\text{DD}}=30\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $R_G=3.3\Omega$ , $I_D=15\text{A}$	---	7	---	$\text{ns}$
$T_r$	Rise Time		---	4.5	---	
$T_{\text{d}(\text{off})}$	Turn-Off Delay Time		---	26	---	
$T_f$	Fall Time		---	5	---	
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=30\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	1270	---	$\text{pF}$
$C_{\text{oss}}$	Output Capacitance		---	479	---	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	40	---	

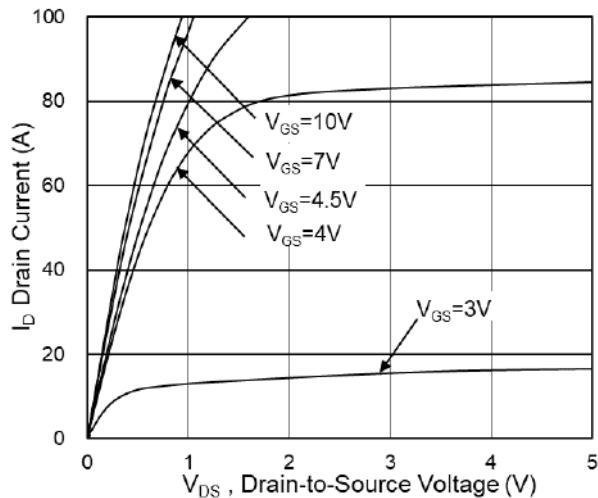
**Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current <sup>1,5,6</sup>	$V_G=V_D=0\text{V}$ , Force Current	---	---	30	A
$V_{\text{SD}}$	Diode Forward Voltage <sup>2</sup>	$V_{\text{GS}}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25^\circ\text{C}$	---	---	1.2	V
$t_{\text{rr}}$	Reverse Recovery Time	$I_F=15\text{A}$ , $dI/dt=100\text{A}/\mu\text{s}$ , $T_J=25^\circ\text{C}$	---	22	---	$\text{nS}$
$Q_{\text{rr}}$	Reverse Recovery Charge		---	72	---	$\text{nC}$

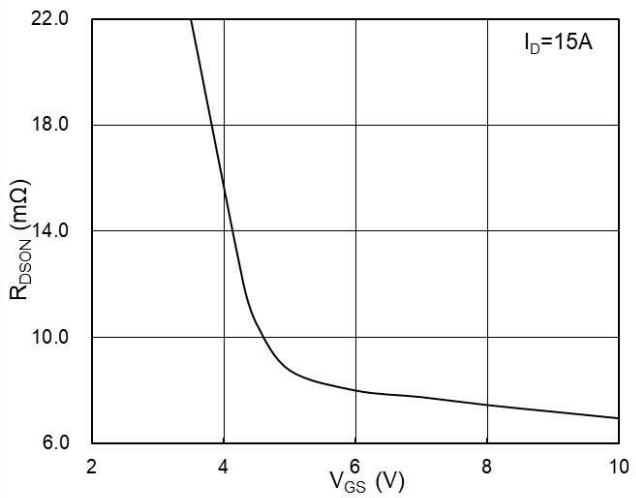
Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup>FR-4 board with 2OZ copper.
2. Single pulse width limited by junction temperature  $T_{J(\text{MAX})}=150^\circ\text{C}$ .
- 3.The EAS data shows Max. rating . The test condition is  $V_{\text{DD}}=50\text{V}$ ,  $V_{\text{GS}}=10\text{V}$ ,  $L=0.1\text{mH}$ ,  $I_{\text{AS}}=23\text{A}$
- 4.The power dissipation is limited by  $150^\circ\text{C}$  junction temperature
- 5.The data is theoretically the same as  $I_D$  and  $I_{\text{DM}}$ , in real applications , should be limited by total power dissipation.
- 6.The maximum current rating is package limited.

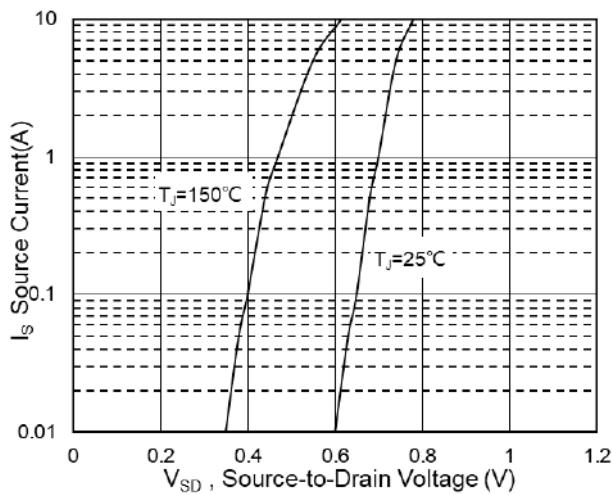
### Typical Characteristics



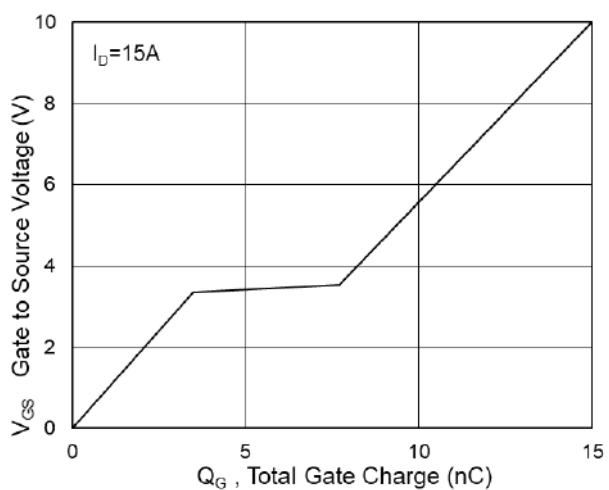
**Fig.1 Typical Output Characteristics**



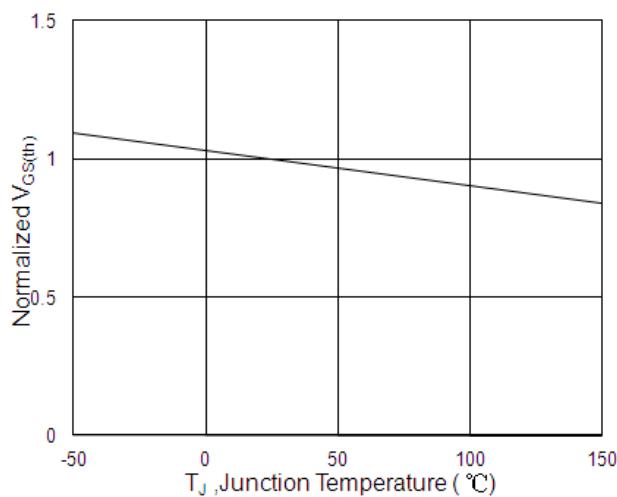
**Fig.2 On-Resistance vs G-S Voltage**



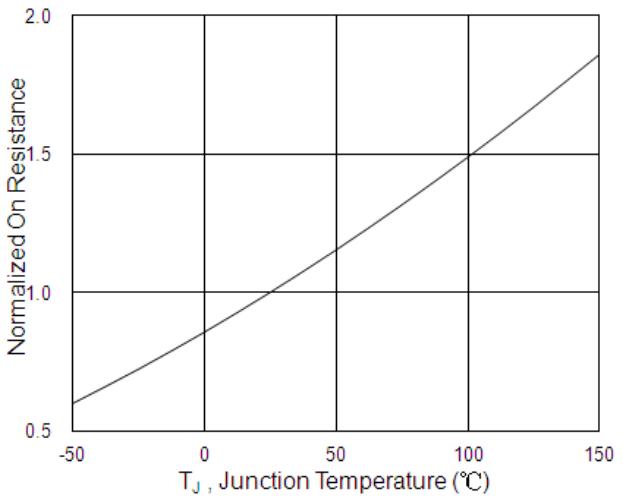
**Fig.3 Source Drain Forward Characteristics**



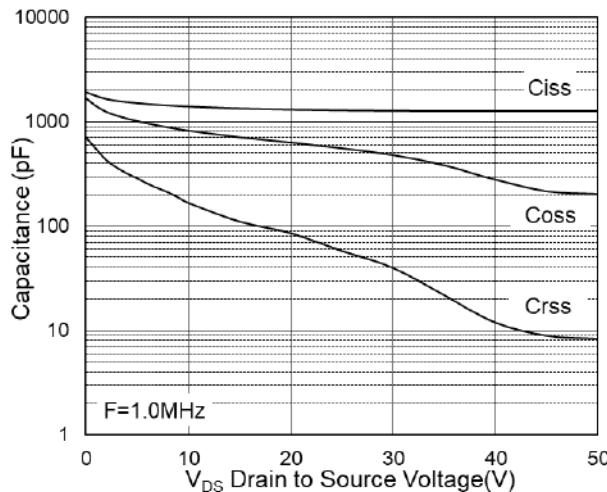
**Fig.4 Gate-Charge Characteristics**



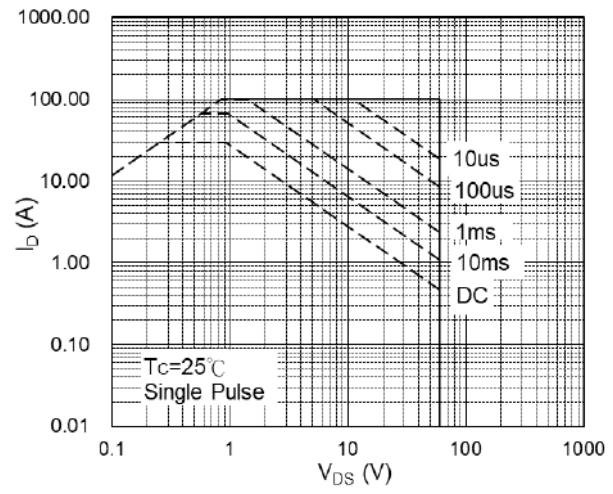
**Fig.5 Normalized  $V_{GS(th)}$  vs  $T_J$**



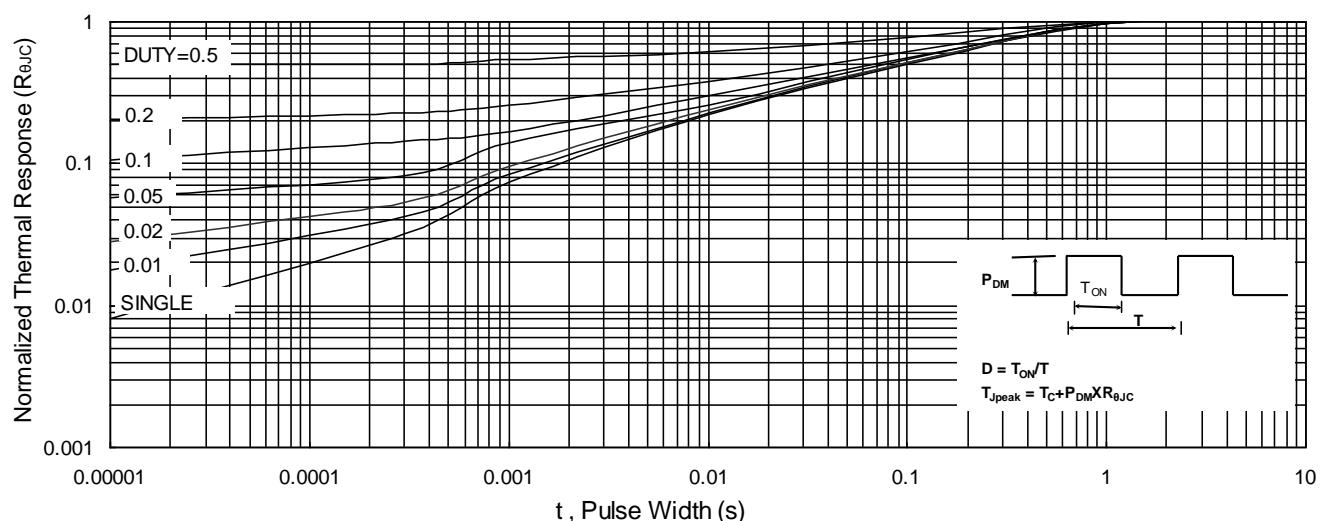
**Fig.6 Normalized  $R_{DS(on)}$  vs  $T_J$**



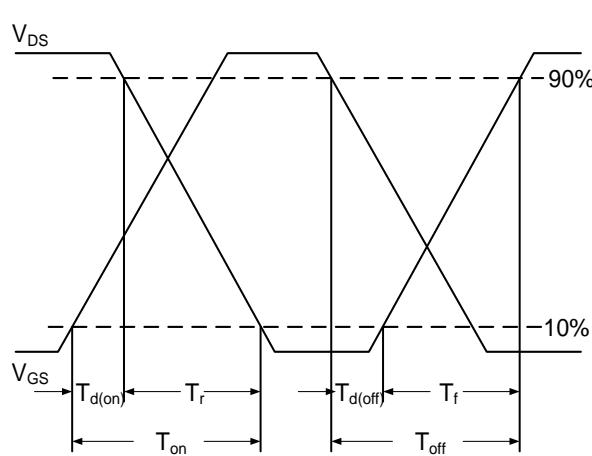
**Fig.7 Capacitance**



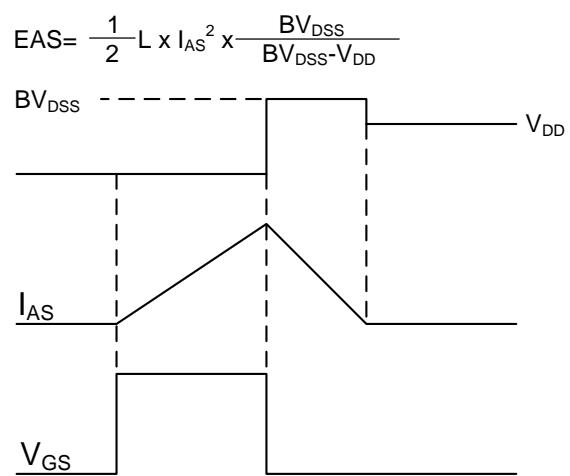
**Fig.8 Safe Operating Area**



**Fig.9 Normalized Maximum Transient Thermal Impedance**

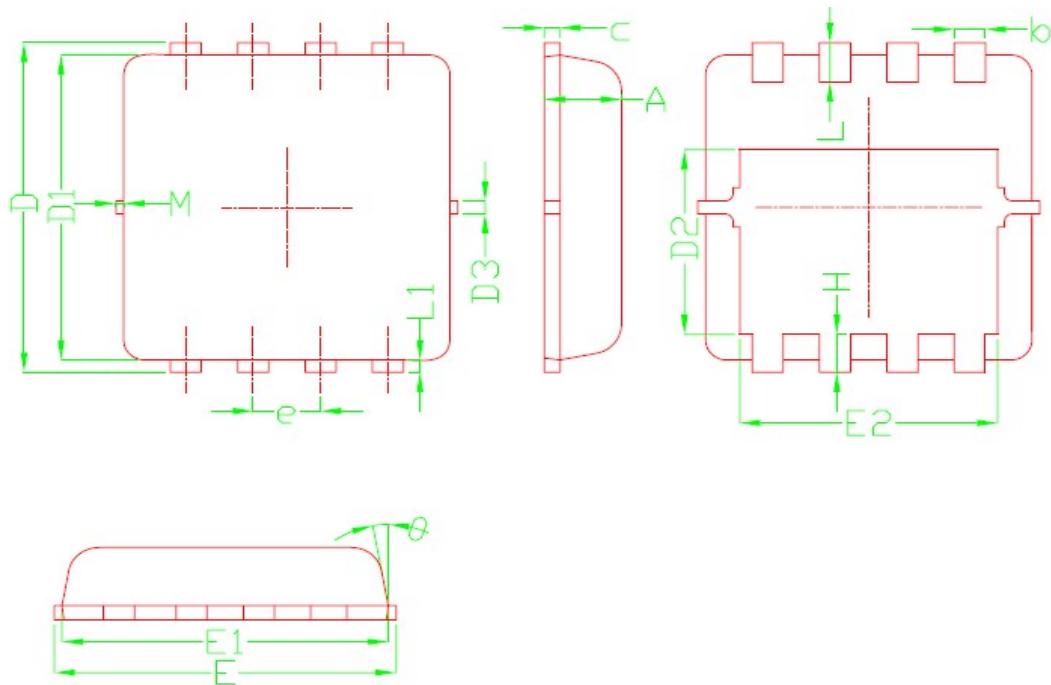


**Fig.10 Switching Time Waveform**



**Fig.11 Unclamped Inductive Switching Waveform**

## PRPAK3x3 Package Outline



SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.70	0.85	0.027	0.034
b	0.20	0.40	0.007	0.016
c	0.10	0.25	0.004	0.010
D	3.15	3.45	0.124	0.136
D1	2.90	3.20	0.114	0.126
D2	1.54	1.98	0.060	0.080
D3	0.10	0.30	0.004	0.012
E	3.15	3.45	0.124	0.136
E1	3.00	3.25	0.118	0.128
E2	2.29	2.65	0.090	0.104
e	0.65 BSC		0.025 BSC	
H	0.28	0.65	0.011	0.026
Θ	0°	14°	0°	14°
L	0.30	0.50	0.012	0.020
L1	0.13		0.005	
M	---	0.15	---	0.006