

## N-Ch 30V Fast Switching MOSFETs

### Features:

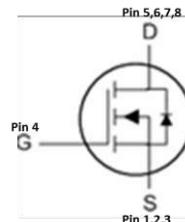
- Advanced Trench MOS Technology
- Low Gate Charge
- Low  $R_{DS(ON)}$
- 100% EAS Guaranteed
- Green Device Available



**PRPAK3X3 Pin Configuration**

### Applications:

- Power Management in Desktop Computer or DC/DC Converters.
- Isolated DC/DC Converters in Telecom and Industrial.



### Product Summary

BVDSS	RDS(on)	ID
30V	3.9mΩ	35A

### Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	30	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D @ T_c = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	35	A
$I_D @ T_c = 100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	32	A
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	120	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	80	mJ
$I_{AS}$	Avalanche Current	40	A
$P_D @ T_c = 25^\circ C$	Total Power Dissipation <sup>4</sup>	27	W
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>	---	60	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>1</sup>	---	4.5	°C/W

**N-Channel Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$	30	---	---	V
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{\text{GS}}=10\text{V}$ , $I_D=20\text{A}$	---	3.2	3.9	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$ , $I_D=15\text{A}$	---	4.9	6.1	
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D=250\mu\text{A}$	1.2	1.7	2.2	V
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=30\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	1	$\text{uA}$
		$V_{\text{DS}}=30\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=55^\circ\text{C}$	---	---	5	
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=5\text{V}$ , $I_D=20\text{A}$	---	75	---	S
$R_g$	Gate Resistance	$V_{\text{DS}}=0\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	0.7	1.65	2.6	$\Omega$
$Q_g$	Total Gate Charge (4.5V)	$V_{\text{DS}}=15\text{V}$ , $V_{\text{GS}}=4.5\text{V}$ , $I_D=20\text{A}$	---	14.7	---	$\text{nC}$
$Q_{\text{gs}}$	Gate-Source Charge		---	5.8	---	
$Q_{\text{gd}}$	Gate-Drain Charge		---	3.5	---	
$T_{\text{d}(\text{on})}$	Turn-On Delay Time	$V_{\text{DD}}=15\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $R_G=3\Omega$ $I_D=20\text{A}$	---	7.5	---	$\text{ns}$
$T_r$	Rise Time		---	20.2	---	
$T_{\text{d}(\text{off})}$	Turn-Off Delay Time		---	21.6	---	
$T_f$	Fall Time		---	4.4	---	
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=15\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	1476	---	$\text{pF}$
$C_{\text{oss}}$	Output Capacitance		---	556	---	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	70	---	

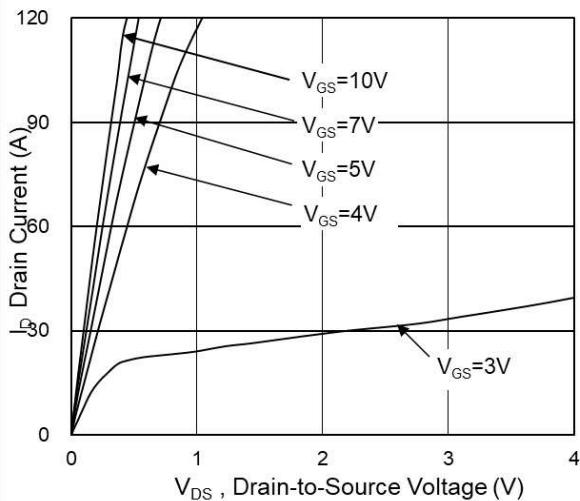
**Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current <sup>1,5</sup>	$V_G=V_D=0\text{V}$ , Force Current	---	---	30	A
$V_{\text{SD}}$	Diode Forward Voltage <sup>2</sup>	$V_{\text{GS}}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25^\circ\text{C}$	---	---	1	V

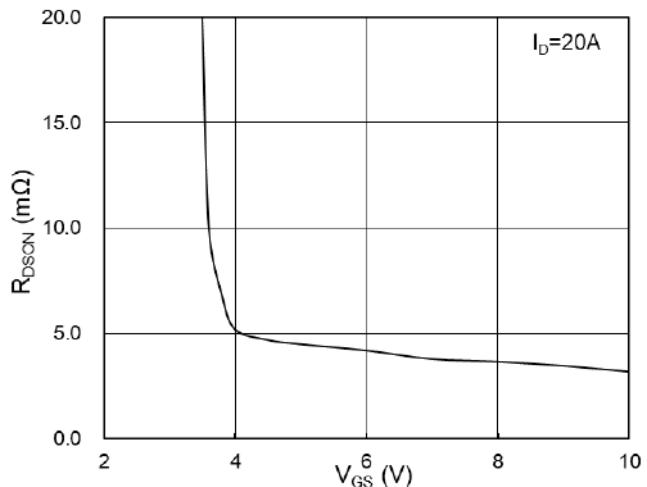
Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is  $V_{\text{DD}}=25\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $L=0.1\text{mH}$ , $I_{\text{AS}}=40\text{A}$
- 4.The power dissipation is limited by  $150^\circ\text{C}$  junction temperature
- 5.The data is theoretically the same as  $I_D$  and  $I_{\text{DM}}$  , in real applications , should be limited by total power dissipation.

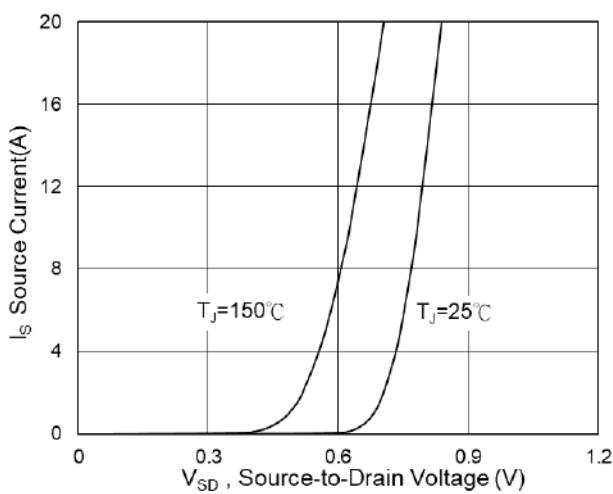
### N-Channel Typical Characteristics



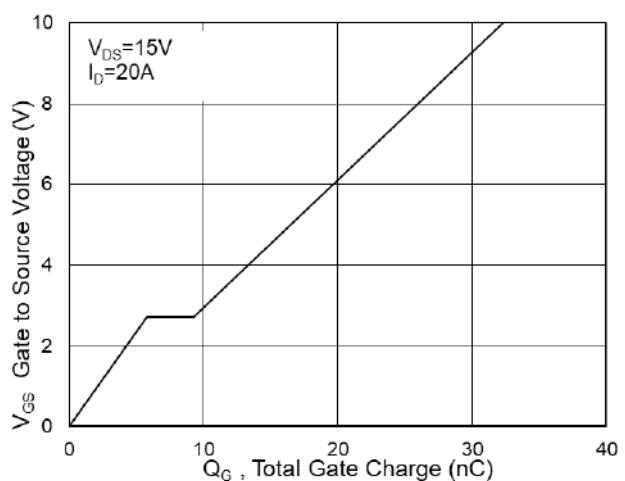
**Fig.1 Typical Output Characteristics**



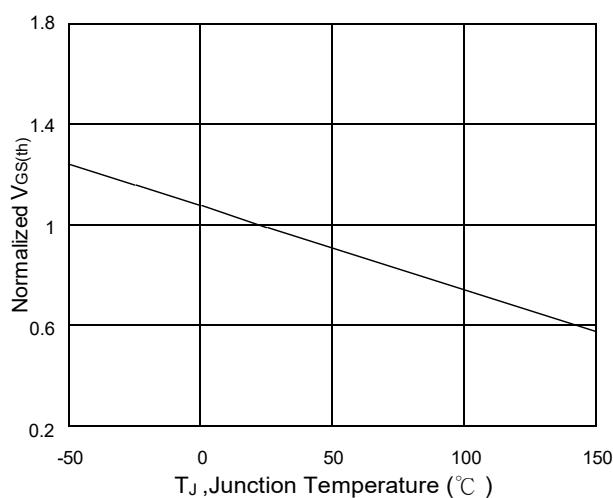
**Fig.2 On-Resistance vs G-S Voltage**



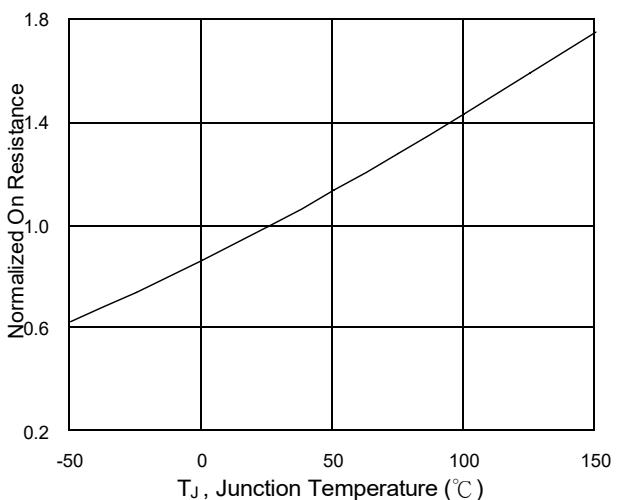
**Fig.3 Source Drain Forward Characteristics**



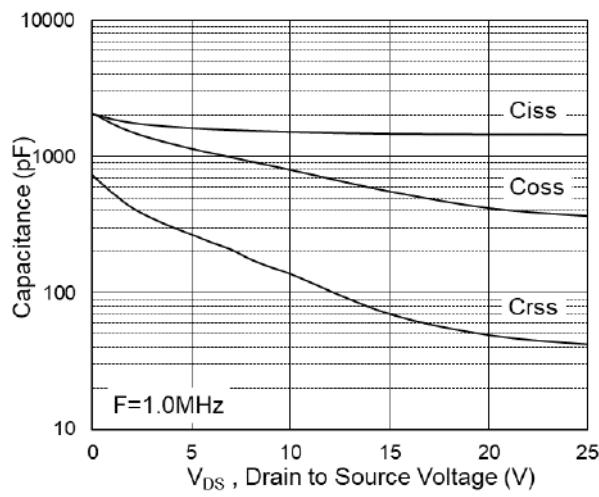
**Fig.4 Gate-Charge Characteristics**



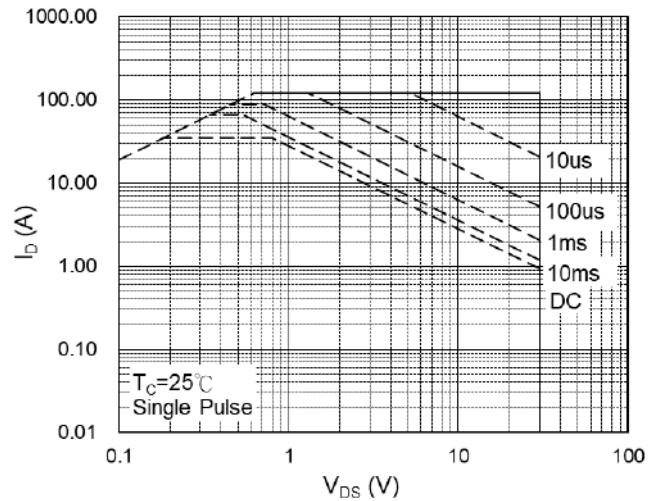
**Fig.5 Normalized  $V_{GS(th)}$  vs  $T_J$**



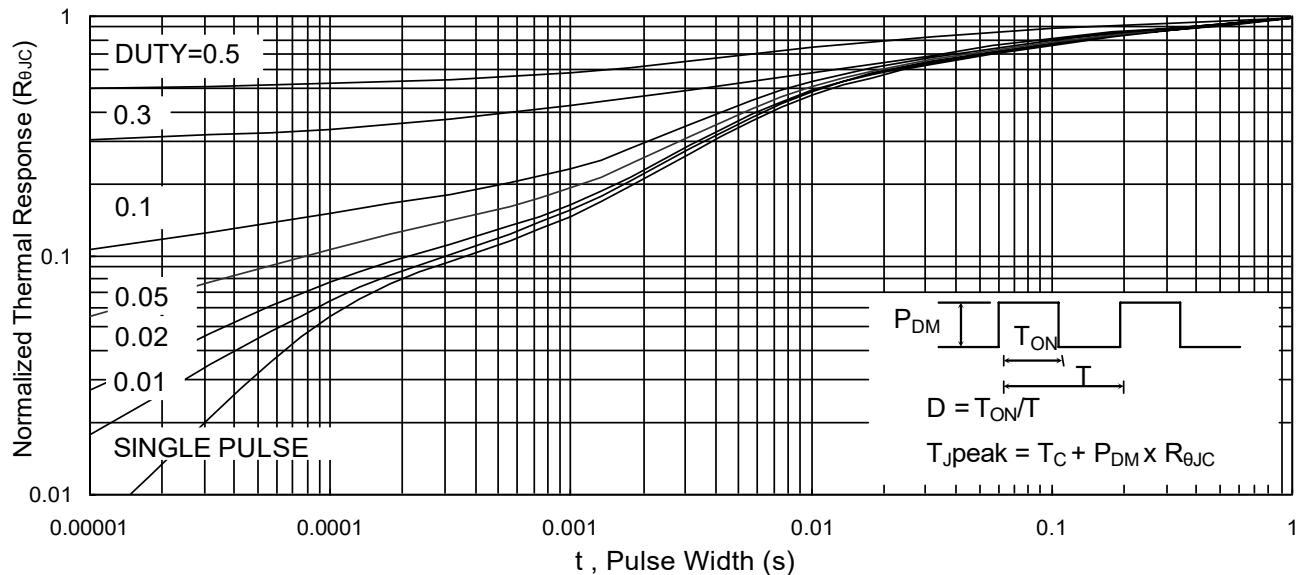
**Fig.6 Normalized  $R_{DS(on)}$  vs  $T_J$**



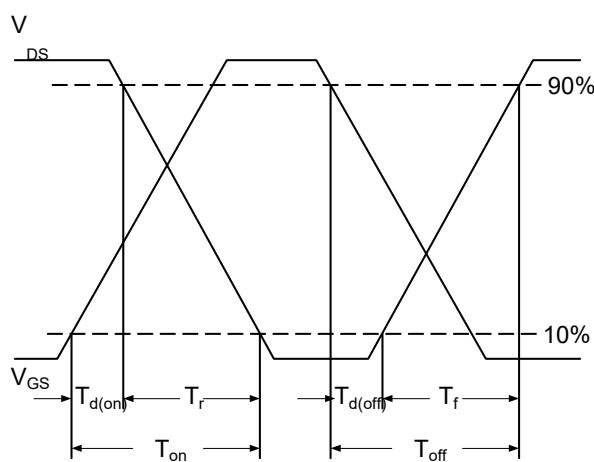
**Fig.7 Capacitance**



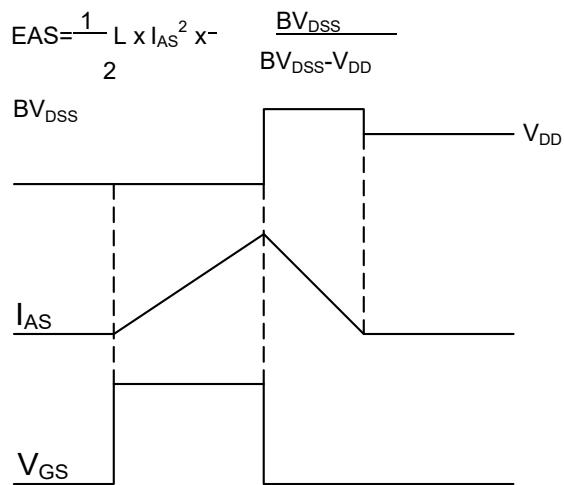
**Fig.8 Safe Operating Area**



**Fig.9 Normalized Maximum Transient Thermal Impedance**



**Fig.10 Switching Time Waveform**



**Fig.11 Unclamped Inductive Switching Wave**