

## N-Ch 150V Fast Switching MOSFETs

### Features:

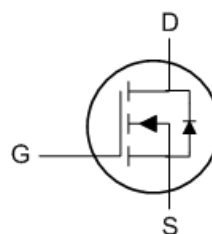
- Low  $R_{DS(ON)}$
- Green Device Available
- 100% EAS Tested
- Advanced Trench MOS Technology

### Applications:

- Power Management in TV Converter.
- DC/DC Converter.



TO252 Pin Configuration



### Product Summary

| BVDSS | RDSON | ID  |
|-------|-------|-----|
| 150V  | 56mΩ  | 23A |

### Absolute Maximum Ratings

| Symbol                      | Parameter                                  | Rating     | Units            |
|-----------------------------|--------------------------------------------|------------|------------------|
| $V_{DS}$                    | Drain-Source Voltage                       | 150        | V                |
| $V_{GS}$                    | Gate-Source Voltage                        | $\pm 20$   | V                |
| $I_D@T_C=25^\circ\text{C}$  | Continuous Drain Current <sup>1</sup>      | 23         | A                |
| $I_D@T_C=100^\circ\text{C}$ | Continuous Drain Current <sup>1</sup>      | 16         | A                |
| $I_D@T_A=25^\circ\text{C}$  | Continuous Drain Current <sup>1</sup>      | 4.5        | A                |
| $I_D@T_A=70^\circ\text{C}$  | Continuous Drain Current <sup>1</sup>      | 3.8        | A                |
| $I_{DM}$                    | Pulsed Drain Current <sup>2</sup>          | 60         | A                |
| EAS                         | Single Pulse Avalanche Energy <sup>3</sup> | 61         | mJ               |
| $I_{AS}$                    | Avalanche Current                          | 35         | A                |
| $P_D@T_C=25^\circ\text{C}$  | Total Power Dissipation <sup>4</sup>       | 72.6       | W                |
| $P_D@T_A=25^\circ\text{C}$  | Total Power Dissipation <sup>4</sup>       | 2.7        | W                |
| $T_{STG}$                   | Storage Temperature Range                  | -55 to 175 | $^\circ\text{C}$ |
| $T_J$                       | Operating Junction Temperature Range       | -55 to 175 | $^\circ\text{C}$ |

### Thermal Data

| Symbol          | Parameter                                        | Typ. | Max. | Unit               |
|-----------------|--------------------------------------------------|------|------|--------------------|
| $R_{\theta JA}$ | Thermal Resistance Junction-ambient <sup>1</sup> | ---  | 55   | $^\circ\text{C/W}$ |
| $R_{\theta JC}$ | Thermal Resistance Junction-Case <sup>1</sup>    | ---  | 2.0  | $^\circ\text{C/W}$ |

**Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)**

| Symbol              | Parameter                                      | Conditions                                                                              | Min. | Typ. | Max. | Unit |
|---------------------|------------------------------------------------|-----------------------------------------------------------------------------------------|------|------|------|------|
| BV <sub>DSS</sub>   | Drain-Source Breakdown Voltage                 | V <sub>GS</sub> =0V, I <sub>D</sub> =250uA                                              | 150  | ---  | ---  | V    |
| R <sub>DS(ON)</sub> | Static Drain-Source On-Resistance <sup>2</sup> | V <sub>GS</sub> =10V, I <sub>D</sub> =10A                                               | ---  | 47   | 56   | mΩ   |
|                     | Static Drain-Source On-Resistance <sup>2</sup> | V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A                                              | ---  | 53   | 70   | mΩ   |
| V <sub>GS(th)</sub> | Gate Threshold Voltage                         | V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA                                | 1.2  | ---  | 2.5  | V    |
| I <sub>DSS</sub>    | Drain-Source Leakage Current                   | V <sub>DS</sub> =120V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C                        | ---  | ---  | 1    | uA   |
|                     |                                                | V <sub>DS</sub> =120V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C                        | ---  | ---  | 5    |      |
| I <sub>GSS</sub>    | Gate-Source Leakage Current                    | V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V                                              | ---  | ---  | ±100 | nA   |
| g <sub>fs</sub>     | Forward Transconductance                       | V <sub>DS</sub> =5V, I <sub>D</sub> =10A                                                | ---  | 25   | ---  | S    |
| Q <sub>g</sub>      | Total Gate Charge                              | V <sub>DS</sub> =75V, V <sub>GS</sub> =10V, I <sub>D</sub> =10A                         | ---  | 19   | ---  | nC   |
| Q <sub>gs</sub>     | Gate-Source Charge                             |                                                                                         | ---  | 4.5  | ---  |      |
| Q <sub>gd</sub>     | Gate-Drain Charge                              |                                                                                         | ---  | 2.6  | ---  |      |
| T <sub>d(on)</sub>  | Turn-On Delay Time                             | V <sub>DD</sub> =75V, V <sub>GS</sub> =10V, R <sub>G</sub> =3.3Ω<br>I <sub>D</sub> =10A | ---  | 18   | ---  | ns   |
| T <sub>r</sub>      | Rise Time                                      |                                                                                         | ---  | 5.8  | ---  |      |
| T <sub>d(off)</sub> | Turn-Off Delay Time                            |                                                                                         | ---  | 26.5 | ---  |      |
| T <sub>f</sub>      | Fall Time                                      |                                                                                         | ---  | 4.5  | ---  |      |
| C <sub>iss</sub>    | Input Capacitance                              | V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1MHz                                       | ---  | 1090 | ---  | pF   |
| C <sub>oss</sub>    | Output Capacitance                             |                                                                                         | ---  | 93   | ---  |      |
| C <sub>rss</sub>    | Reverse Transfer Capacitance                   |                                                                                         | ---  | 6    | ---  |      |

**Diode Characteristics**

| Symbol          | Parameter                                | Conditions                                                    | Min. | Typ. | Max. | Unit |
|-----------------|------------------------------------------|---------------------------------------------------------------|------|------|------|------|
| I <sub>S</sub>  | Continuous Source Current <sup>1,5</sup> | V <sub>G</sub> =V <sub>D</sub> =0V, Force Current             | ---  | ---  | 20   | A    |
| V <sub>SD</sub> | Diode Forward Voltage <sup>2</sup>       | V <sub>GS</sub> =0V, I <sub>S</sub> =1A, T <sub>J</sub> =25°C | ---  | ---  | 1.2  | V    |
| t <sub>rr</sub> | Reverse Recovery Time                    | I <sub>F</sub> =10A, dI/dt=100A/μs, T <sub>J</sub> =25°C      | ---  | 45   | ---  | nS   |
| Q <sub>rr</sub> | Reverse Recovery Charge                  | T <sub>J</sub> =25°C                                          | ---  | 138  | ---  | nC   |

Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 20Z copper.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
3. The EAS data shows Max. rating . The test condition is V<sub>DD</sub>=25V,V<sub>GS</sub>=10V,L=0.1mH,I<sub>AS</sub>=35A
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub> , in real applications , should be limited by total power dissipation.

## Typical Characteristics

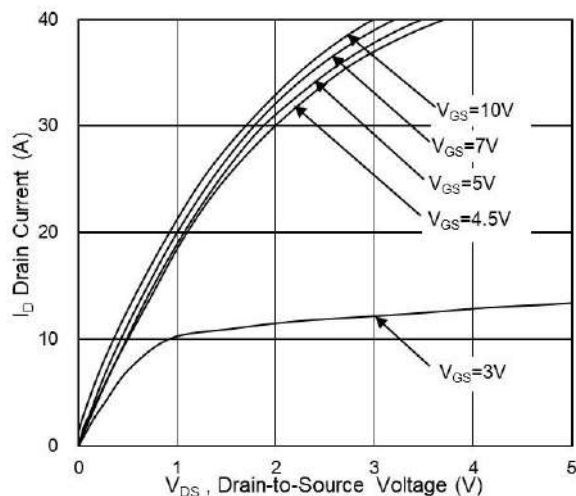


Fig.1 Typical Output Characteristics

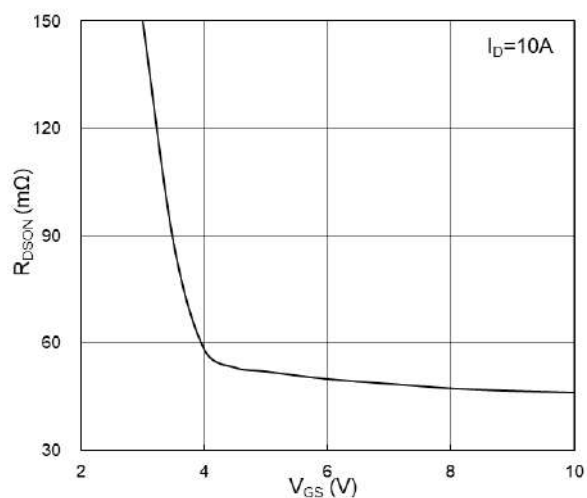


Fig.2 On-Resistance vs G-S Voltage

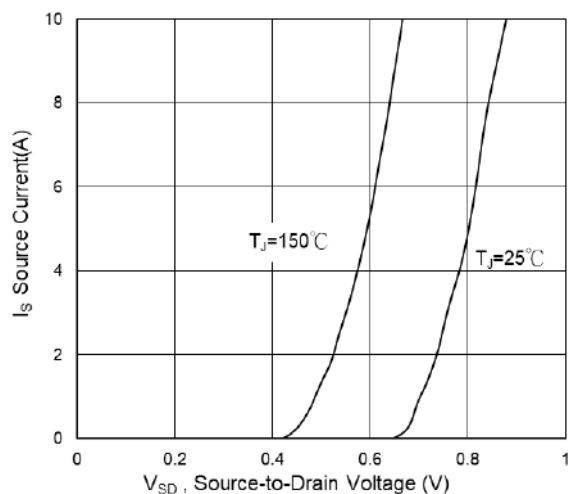


Fig.3 Source Drain Forward Characteristics

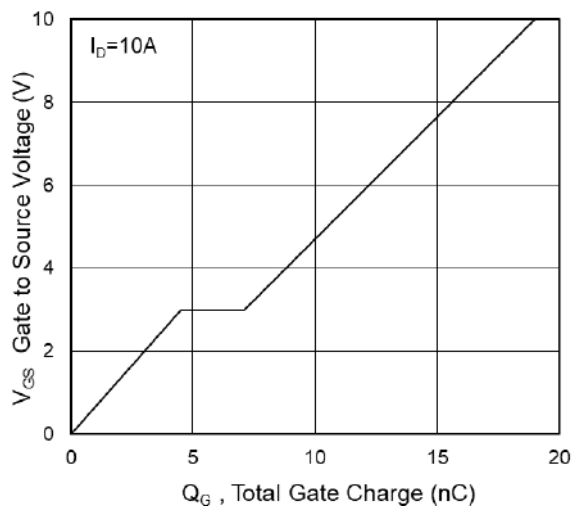


Fig.4 Gate-Charge Characteristics

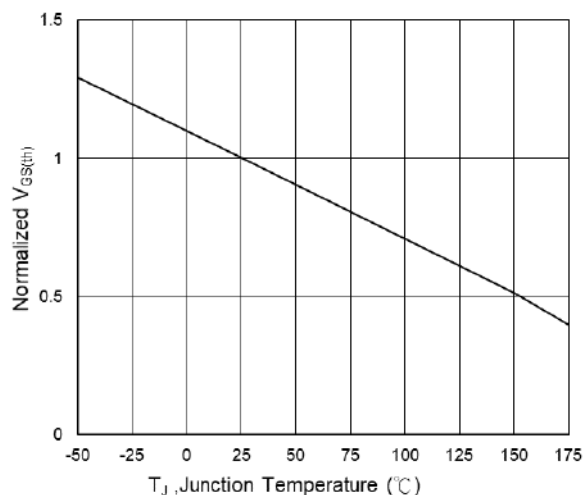


Fig.5 Normalized  $V_{GS(th)}$  vs  $T_J$

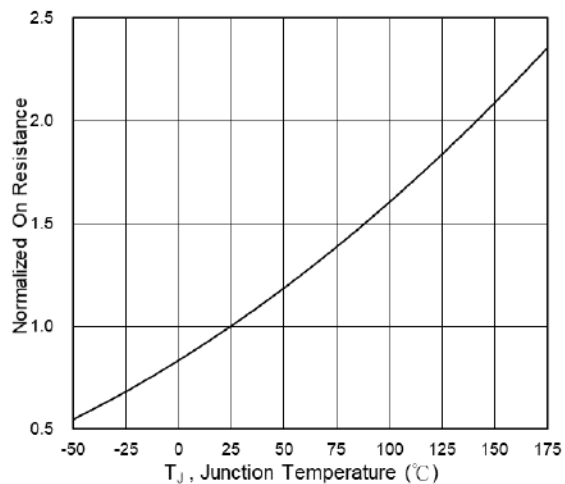
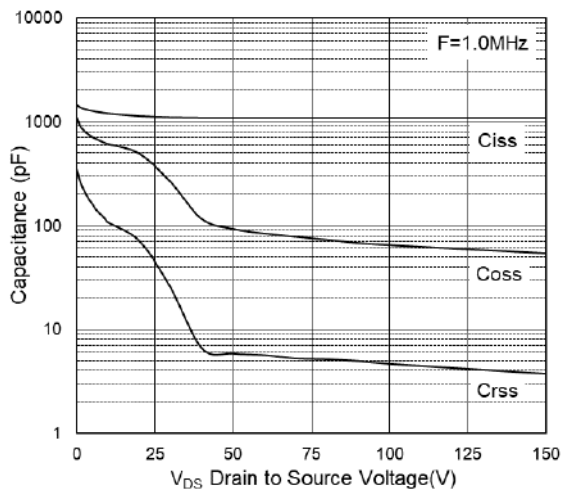
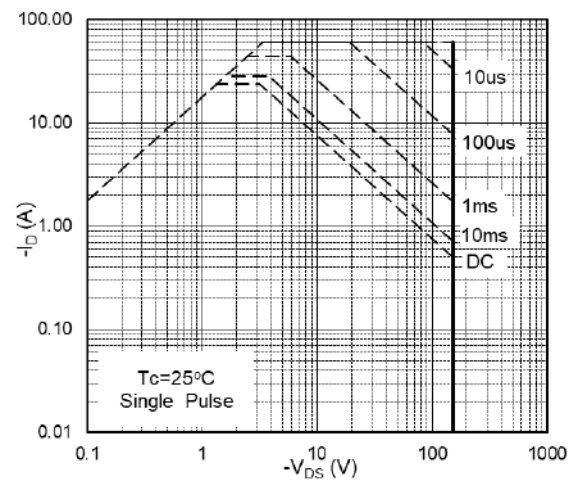


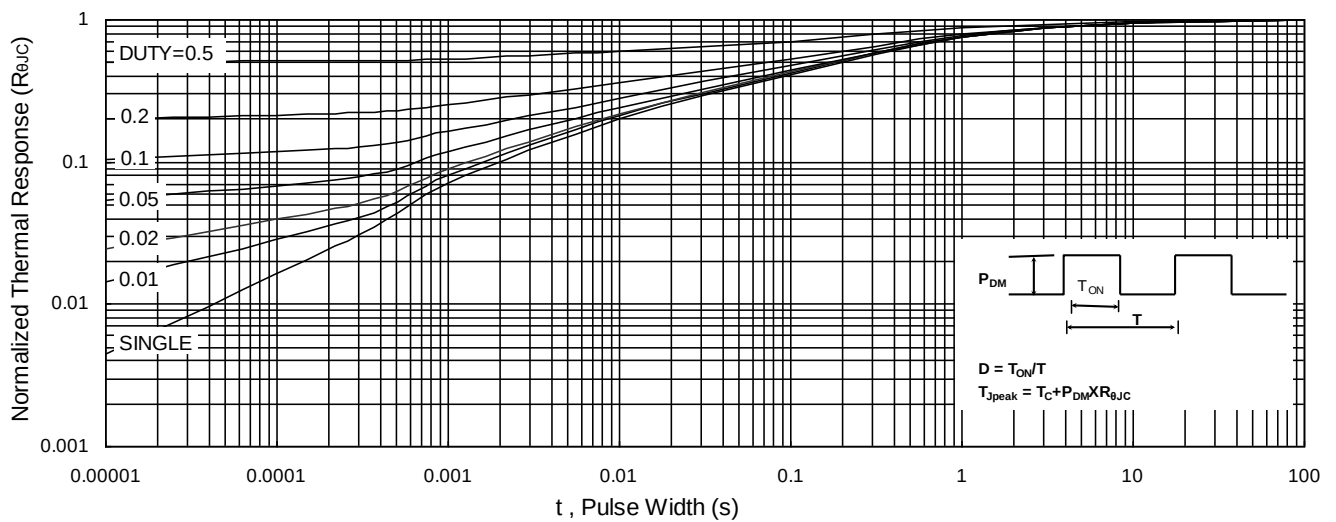
Fig.6 Normalized  $R_{DS(on)}$  vs  $T_J$



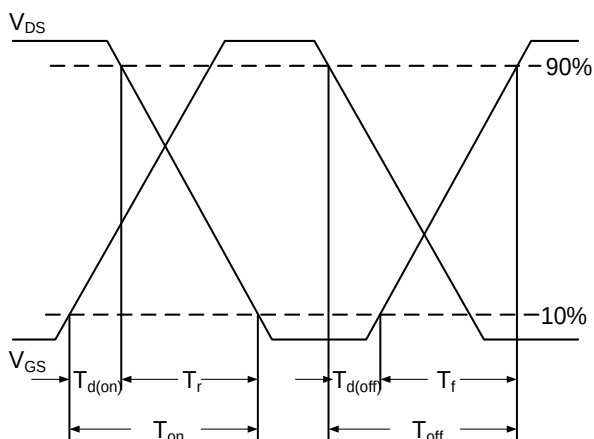
**Fig.7 Capacitance**



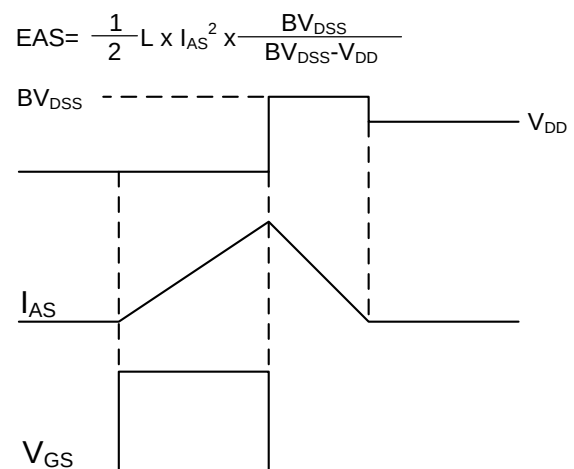
**Fig.8 Safe Operating Area**



**Fig.9 Normalized Maximum Transient Thermal Impedance**



**Fig.10 Switching Time Waveform**



**Fig.11 Unclamped Inductive Switching Waveform**